Growth, Characterization and Contacts to Ga₂O₃ Single Crystal Substrates and Epitaxial Layers

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Yao Yao

B.Sc.(Hons), Physics, National University of Singapore Ph.B.(Hons), Physics, Australian National UniversityM.S., Materials Science and Engineering, Carnegie Mellon University

> Carnegie Mellon University Pittsburgh, PA

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It is good to have an end to journey toward; but it is the journey that matters, in the end. — Ursula K. LeGuin

It has been a long and winding journey, fraught with false starts and wrong turns, now the end is finally in sight.

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Abstract

Gallium Oxide (Ga₂O₃) has emerged over the last decade as a new up-and-coming alternative to traditional wide bandgap semiconductors. It exists as five polymorphs (α -, β -, γ -, δ -, and ε -Ga₂O₃), of which β -Ga₂O₃ is the thermodynamically stable form, and the most extensively studied phase. β -Ga₂O₃ has a wide bandgap of ~4.8 eV and exhibits a superior figure-of-merit for power devices compared to other wide bandgap materials, such as SiC and GaN. These make β -Ga₂O₃ a promising candidate in a host of electronic and optoelectronic applications. Recent advances in β -Ga₂O₃ single crystals growth have also made inexpensive β -Ga₂O₃ single crystal grown from the melt a possibility in the near future.

Despite the plethora of literature on β -Ga₂O₃-based devices, understanding of contacts to this material — a device component that fundamentally determines device characteristics — remained lacking. For this research, ohmic and Schottky metal contacts to Sn-doped β -Ga₂O₃ ($\overline{2}01$) single crystal substrates, unintentionally doped (UID) homoepitaxial β -Ga₂O₃ (010) on Sn-doped β -Ga₂O₃ grown by molecular beam epitaxy (MBE), and UID heteroepitaxial β -Ga₂O₃ ($\overline{2}01$) epitaxial layers on *c*-plane sapphire by metal-organic chemical vapor deposition (MOCVD) were investigated. Each of the substrates was characterized for their structural, morphological, electrical, and optical properties, the results will be presented in the following document.

Nine metals (Ti, In, Ag, Sn, W, Mo, Sc, Zn, and Zr) with low to moderate work functions were studied as possible ohmic contacts to β -Ga₂O₃. It was found that select metals displayed either ohmic (Ti and In) or pseudo-ohmic (Ag, Sn and Zr) behavior under certain conditions. However, the morphology was often a problem as many thin film metal contacts dewetted the substrate surface. Ti with a Au capping layer with post-metallization annealing treatment was the only consistently reliable ohmic contact to β -Ga₂O₃. It was concluded that metal work function is not a dominant factor in forming an ohmic contact to β -Ga₂O₃ and that limited interfacial reactions appear to play an important role.

Prior to a systematic study of Schottky contacts to β -Ga₂O₃, a comparison of the effects of five different wet chemical surface treatments on the β -Ga₂O₃ Schottky diodes was made. It was established that a treatment with an organic solvent clean followed by HCl, H₂O₂ and a deionized water rinse following each step yielded the best results. Schottky diodes based on ($\bar{2}01$) β -Ga₂O₃ substrates and (010) β -Ga₂O₃ homoepitaxial layers were formed using five different Schottky metals with moderate to high work functions: W, Cu, Ni, Ir, and Pt. Schottky barrier heights (SBHs) calculated from current-voltage (*I*-*V*) and capacitance-voltage (*C*-*V*) measurements of the five selected metals were typically in the range of 1.0 - 1.3 eV and 1.6 - 2.0 eV, respectively, and showed little dependence on the metal work function. Several diodes also displayed inhomogeneous Schottky barrier behavior at room temperature. The results indicate that bulk or near-surface defects and/or unpassivated surface states may have a more dominant effect on the electrical behavior of these diodes compared to the choice of Schottky metal and its work function.

Lastly, working with collaborators at Structured Materials Industries (SMI) Inc., heteroepitaxial films of Ga_2O_3 were grown on *c*-plane sapphire (001) using a variety of vapor phase epitaxy methods, including MOVPE, halide organometallic vapor phase epitaxy

(HOVPE), and halide vapor phase epitaxy (HVPE). The stable phase β -Ga₂O₃ was observed when grown using MOVPE technique, regardless of precursor flow rates, at temperatures ranging between 500 – 850 °C. With HOVPE and HVPE growth techniques, instead of the stable β -phase, we observed the growth of the metastable α - and ϵ -phases, often a combination of the two. Cross-sectional transmission electron microscopy (TEM) shows the better lattice matched α -phase first growing semi-coherently on the *c*-plane sapphire substrate, followed by domain matched epitaxy of ε -Ga₂O₃ on top. Secondary ion mass spectrometry (SIMS) revealed that epilayers forming the ϵ -phase contain higher concentrations of chlorine, which suggests that compressive stress due to Cl⁻ impurities may play a role in the growth of ε -Ga₂O₃ despite it being less than thermodynamically favorable.

Contents

A	ostra	ct			III
Li	st of	Figure	S		XI
Li	st of	Tables		ХУ	KIII
1	Intro	oductio	on		1
2	Lite	rature	Review on β -Gallium Oxide (β -Ga $_2$ O $_3$)		7
	2.1	Prope	erties of β -Ga $_2$ O $_3$		7
		2.1.1	Crystal Structure		7
		2.1.2			8
	2.2	Figure	es of Merit (FOM)		10
	2.3	Growt	th of Single Crystal eta -Ga $_2$ O $_3$ \ldots \ldots \ldots \ldots \ldots \ldots \ldots		13
		2.3.1	Verneuil Method		14
		2.3.2	Floating Zone (FZ) Method		15
		2.3.3	Czochralski Methods		16

		2.3.4	Edge-Defined Film-Fed Growth (EFG) Method	19
	2.4	Epitax	ial Growth of eta -Ga $_2$ O $_3$ Thin Films \ldots \ldots \ldots \ldots \ldots \ldots	22
		2.4.1	Molecular Beam Epitaxy (MBE)	22
		2.4.2	Metal-Organic Chemical Vapor Deposition (MOCVD)	23
	2.5	Device	95	24
		2.5.1	Schotty Barrier Diodes (SBDs)	24
		2.5.2	Field-Effect Transistors	26
		2.5.3	Photodetectors	28
	2.6	β -Ga $_2$	O ₃ Ultraviolet Photodetectors	29
3	Meta	al Cont	acts	31
	3.1	Metal-	Semiconductor Contacts	31
	3.2	Currer	nt Transport Mechanisms	32
		3.2.1	Schottky Contacts	34
		3.2.2	Ohmic Contacts	36
		3.2.3	Determination of Contact Resistivity	37
4	β -G	a_2O_3M	aterial Characterization	39
	4.1	Atomic	c Force Microscopy (AFM)	40
	4.2	X-Ray	Diffraction (XRD)	41
	4.3	Optica	I Reflectivity and Transmission	42
	4.4	Electri	cal Conductivity	44
5	Ohn	nic Con	itacts to eta -Ga $_2$ O $_3$	47
	5.1	Sampl	e Preparation	48

	5.2	Titaniu	um (Ti) Ohmic Contacts	53
	5.3	Comp	arison of Different Metals for Ohmic Contacts	60
		5.3.1	Metal Selection	60
		5.3.2	Indium (In)	62
		5.3.3	Tin (Sn)	66
		5.3.4	Silver (Ag)	69
		5.3.5	Zirconium (Zr)	72
		5.3.6	Tungsten (W)	75
		5.3.7	Molybdenum (Mo)	77
		5.3.8	Zinc (Zn)	79
		5.3.9	Scandium (Sc)	81
	5.4	Summ	nary	82
6	Sch	ottky C	Contacts to β -Ga $_2$ O $_3$	87
	6.1	Experi	imental	90
		6.1.1	Extracting Schottky Barrier Data	95
	6.2	Effect	s of Surface Preparation	98
	6.3	Schot	tky Barrier Heights of Different Metals	100
	6.4	Inhom	ogeneous Barrier	102
	6.5	Nonid	eal Schottky Behavior	106
		6.5.1	Competing Transport Mechanisms	106
		6.5.2	Deviation From Schottky-Mott Behavior	107
		6.5.3	Interfacial Effects	108

7	Cha	racterization of eta -Ga $_2$ O $_3$ Thin Films Grown at SMI	111
	7.1	Growth System	111
	7.2	Heteroepitaxy on Sapphire (001)	113
	7.3	Homoepitaxy on β -Ga $_2$ O $_3$	119
8	Epit	axial Growth of Ga $_2$ O $_3$ Polymorphs	121
	8.1	Crystal Structures	122
		8.1.1 Lattice Mismatch with Sapphire (0001)	123
	8.2	Growth of Different Polymorphs	126
	8.3	Discussion	134
9	Sum	mary and Future Work	137
	9.1	Summary	137
	9.2	Current and Future Work	138
		9.2.1 Surface Treatment	138
		9.2.2 Electrical Devices	139
		9.2.3 Improved Crystal Growth and Doping	140
A	Sch	ottky Diodes	141
	A.1	Thermionic Emission Theory	141
	A.2	Extracting Schottky Diode Parameters	142
	A.3	Schottky Barrier Height From Capacitance Voltage (C-V)	144
в	Add	itional SEM of Ohmic Contacts to eta -Ga $_2$ O $_3$	147
Bibliography 151			

List of Figures

1.1	Theoretical limits of (a) breakdown field as a function of bandgap, and (b)	
	on-resistance of various wide bandgap materials as a function of breakdown	
	voltages [1]. \ldots	3
2.1	Monoclinic crystal structure of β -Ga ₂ O ₃ . The two inequivalent Ga (larger	
	spheres) and three inequivalent O (smaller spheres) sites are indicated with	
	different colors [2]. Reproduced with permission from the publisher	9
2.2	The Brillonin zone of β -Ga ₂ O ₃ . Labels indicate high-symmetry points. The	
	axes of the reciprocal unit cell ar also shown. The yellow line represents	
	the I-L line where the conduction band maximum lies [2]. Reproduced with	
	permission from the publisher	10
2.3	Electronic band structure of β -Ga ₂ O ₃ [2]. Reproduced with permission from	
	the publisher.	11
2.4	Schematic of single crystals grown using the Verneuil method	14
2.5	Schematic of single crystals grown using the float zone (FZ) method. \ldots	15

2.6	Schematic of single crystals growth using the Czochralski method	16
2.7	Picture of undoped Ga_2O_3 crystal grown by Tomm <i>et al.</i> [3]. Reproduced	
	with permission from publisher.	17
2.8	Schematic of single crystals grown using the edge-defined film-fed growth	
	(EFG) method.	19
2.9	(a) Illustration of the four growth stages and typical temperature profile	
	used by Aida <i>et al.</i> in the growth of single crystal β -Ga ₂ O ₃ by EFG. The	
	four stages are (a) seeding, (b) necking, (c) spreading and (d) main growth.	
	(b) Photograph of as-grown single crystal β -Ga ₂ O ₃ ribbon [4]. Reproduced	
	with permission from publisher.	20
2.10	Schematic of Schottky barrier diode fabricated by Sasaki et al.[5]. Repro-	
	duced from original article, \textcircled{C} 2013 IEEE	25
2.11	Foward J-V characteristics of two different Ga_2O_3 SBDs in (a) logarithmic	
	and (b) linear plots [5]. Reproduced from original article, \bigodot 2013 IEEE. $% (i)$.	25
2.12	(a) Cross-sectional schematic and (b) DC output characteristics of a Ga_2O_3	
	MESFET [6]. Reproduced with permission from publisher	27
2.13	(a) Cross-sectional schematic and (b) DC output characteristics of a Ga_2O_3	
	MOSFET [7]. Reproduced with permission from publisher	27
2.14	Operating principle for photodetectors	28
3.1	Energy-band diagrams of metal-semiconductor contacts. The metal and	
0.1		
	semiconductor energy bands when they are (a) in separate system, (b)	
	brought into one system but separated by a gap δ , (c) as the gap δ is reduced	
	and (d) as the gap δ becomes zero [8]. Reproduced with permission from	
	publisher	32

3.2	Five basic transport processes under forward bias. (1) Thermionic emission,	
	(2) tunneling, (3) recombination, (4) diffusion of electrons and (5) diffusion	
	of holes [8]. Reproduced with permission from publisher. \ldots \ldots \ldots	33
3.3	Energy-band diagrams of metal on n -type (left) and on p -type semiconduc-	
	tors (right) semiconductors under (a) thermal equilibrium, (b) forward bias,	
	and (c) reverse bias [8]. Reproduced with permission from publisher. $\ . \ .$	35
3.4	TLM array of linear contacts with various spacing between them. (b) CTLM	
	array of circular contacts with various contact separation	37
4.1	Photograph of a (a) 2-inch wafer of Sn-doped single crystal β -Ga ₂ O ₃ ($\overline{2}01$)	
	purchased from Tamura Corp Wafer is transparent in the visible with a	
	slight bluish tint due to Sn doping and (b) an undoped epitaxial film of	
	β -Ga ₂ O ₃ on Sn-doped single crystal β -Ga ₂ O ₃ (010) grown by MBE and	
	provided by Novel Crystal Technology Inc Sample is also transparent in	
	the visible with a bluish tint	39
4.2	(a) 1×1 μ m AFM scan of a Tamura β -Ga ₂ O ₃ wafer, and (b) 5×5 μ m	
	AFM scan of the β -Ga ₂ O ₃ homoepilayer. RMS roughness are 0.164 nm	
	and 0.321 nm, respectively	40
4.3	θ -2 θ XRD scan of single crystal β -Ga ₂ O ₃ ($\bar{2}$ 01) substrate	41
4.4	$\theta\text{-}2\theta$ XRD scan of lightly doped $\beta\text{-}\mathrm{Ga_2O_3}$ (010) epitaxial film grown on $\beta\text{-}$	
	Ga_2O_3 (010) substrate.	42
4.5	(a) Transmittance and (b) reflectance spectra of bulk single crystal Ga_2O_3 .	43
4.6	Low wavelength transmittance spectrum of bulk single crystal Ga_2O_3 near	
	its absorption edge. The adsorption edge is found to be ${\sim}261$ nm	44

4.7	Current-voltage (I-V) plot of Ti/Au ohmic contacts on Sn-doped single crys-	
	tal Ga_2O_3	45
5.1	Current-voltage (<i>I-V</i>) plot of Ti/Au contacts on β -Ga ₂ O ₃ having been treated	
	either by a conventional organic clean (acetone and isopropanol), or with an	
	additional acid treatment (HCl and H_2O_2). Both samples were then annealed	
	for a minute at 600 $^{\circ}$ C in flowing Ar	49
5.2	Schematic showing the process of making two planar ohmic contacts. (a)	
	A length of wire is placed over the β -Ga ₂ O ₃ specimen and loaded into the	
	UHV chamber. (b) The end result is two contacts separated by a gap	50
5.3	Schematic showing the process of making planar ohmic contacts. (a) A	
	shadow mask is attached over the β -Ga ₂ O ₃ specimen and loaded into the	
	UHV chamber. (b) The end result are circular contacts of 500 $\mu {\rm m}$ diameter	
	separated by 500 μ m spacing	50
5.4	Schematic showing the process of preparing a TEM cross-sectional sample.	
	Refer to text for description of preparation procedure	52
5.5	Current-voltage plot of Ti and Ti/Au contacts on single crystal β -Ga ₂ O ₃ , in	
	as-deposited condition and after annealing at 600 $^{\rm o}{\rm C}$ for 1 min. in Ar ambient.	54
5.6	I-V plots of Ti/Au contacts to (a) single crystal Sn-doped β -Ga ₂ O ₃ and (b)	
	undoped β -Ga ₂ O ₃ epilayer on sapphire, as a function of annealing temper-	
	ature. The samples were annealed at each temperature for 1 min.in an Ar	
	ambient	55
5.7	SEM image of Ti film on single crystal β -Ga ₂ O ₃ in the (a) as-deposited state,	
	(b) after annealing to 400 °C, (c) after annealing to 600 °C, and (d) after	
	annealing to 800 °C.	56

5.8	SEM image of Ti/Au film on single crystal β -Ga ₂ O ₃ in the (a) as-deposited	
	state, (b) after annealing to 400°C, (c) after annealing to 600°C, and after	
	annealing to 800degc.	57
5.9	(a, c) Cross-section TEM micrographs and (b, d) EDX line profiles of Ti/Au $$	
	contact on Ga_2O_3 as-deposited and after annealing at 400 °C, as indicated.	58
5.10	Ellingham diagram of oxides of select metals compared to gallium oxide.	61
5.11	Current-voltage plots of In film on single crystal β -Ga ₂ O ₃ (a) as-deposited	
	and (b) annealed at 800 °C.	62
5.12	SEM of In film on single crystal β -Ga ₂ O ₃ in as-deposited condition and after	
	annealing at 600° C as indicated	63
5.13	SEM of In/Au film on single crystal β -Ga ₂ O ₃ in as-deposited condition and	
	after annealing at temperatures indicated	64
5.14	Current-voltage plot of bulk In contacts on single crystal β -Ga ₂ O ₃ (a) as-	
	deposited and (b) annealed at 800 °C. $\ldots \ldots \ldots \ldots \ldots \ldots \ldots \ldots$	65
5.15	Current-voltage plot of Sn film on single crystal β -Ga ₂ O ₃ , as a function of	
	annealing temperature. The anneals were conducted for 1 min.each in Ar	66
5.16	SEM of Sn film on single crystal β -Ga ₂ O ₃ in as-deposited condition and after	
	annealing at temperatures indicated	67
5.17	SEM of SnAu film on single crystal β -Ga ₂ O ₃ in as-deposited condition and	
	after annealing at temperatures indicated	68
5.18	Current-voltage plot of Ag film on single crystal β -Ga ₂ O ₃ . The anneals were	
	conducted for 1 min.each in Ar	69
5.19	SEM image of Ag film on single crystal β -Ga ₂ O ₃ after annealing at temper-	
	atures indicated.	70

5.20	SEM image of Ag/Au film on single crystal $\beta\text{-}\mathrm{Ga_2O_3}$ after annealing at	
	temperatures indicated	71
5.21	Current-voltage plot of Zr film on single crystal β -Ga ₂ O ₃ . The anneals were	
	conducted for 1 min. each in Ar. Zr $I\text{-}V$ were very resistive and overlap near	
	the x-axis.	72
5.22	SEM of Zr film on single crystal β -Ga ₂ O ₃ in as-deposited condition and after	
	annealing at temperatures indicated	73
5.23	EDX intensity profiles of Ga, Zr, and O acquired along the red line across	
	the Zr/Ga_2O_3 topography after annealing at 600 °C	74
5.24	Current-voltage plot of W film on single crystal β -Ga ₂ O ₃ , as a function of	
	annealing temperature. The anneals were conducted for 1 min.each in Ar	75
5.25	SEM of W film on single crystal β -Ga ₂ O ₃ in as-deposited condition and after	
	annealing at temperatures indicated	76
5.26	Current-voltage plot of Mo film on single crystal β -Ga ₂ O ₃ , as a function of	
	annealing temperature. The anneals were conducted for 1 min.each in Ar	77
5.27	SEM of Mo film on single crystal β -Ga ₂ O ₃ in as-deposited condition and	
	after annealing at temperatures indicated	78
5.28	Current-voltage plot of Zn film on single crystal β -Ga ₂ O ₃ , as a function of	
	annealing temperature. The anneals were conducted for 1 min.each in Ar	79
5.29	SEM of Zn film on single crystal β -Ga ₂ O ₃ in as-deposited condition and after	
	annealing at temperatures indicated.	80
5.30	Current-voltage plot of Sc film on single crystal β -Ga ₂ O ₃ , as a function of	
	annealing temperature. The anneals were conducted for 1 min.each in Ar	81

5.31	SEM of Sc film on single crystal β -Ga ₂ O ₃ in as-deposited condition and after	
	annealing at temperatures indicated	82
6.1	Optical image of photoresist after a 5 min HF soak. Insert shows photoresist	
	before HF soak	91
6.2	Summary of all cleaning methods considered for this study. The first two	
	methods involving HF were rejected, leaving the five remaining methods	
	which we have studied	92
6.3	Schematic of Schottky diodes with a (a) vertical and (b) lateral device struc-	
	ture	94
6.4	Forward current-voltage $(I-V)$ characteristics of a Ni/Ga ₂ O ₃ Schottky diode	
	in the (a) linear, and (b) \log scale	96
6.5	Capacitance-voltage $(C-V)$ characteristics of a Ni/Ga ₂ O ₃ Schottky diode.	
	(a) Plot of C vs. V . (b) Plot of $1/C^2$ vs. V	98
6.6	Schottky barrier height vs. (a) ideality factor and (b) series resistance for	
	diodes pre-treated using different wet chemicals	99
6.7	Calculated Schottky barrier heights vs metal work function for Schottky	
	diodes on ($\overline{2}01$) bulk and (010) epitaxial β -Ga ₂ O ₃ . Schottky barrier height	
	values on β -Ga ₂ O ₃ as reported in the literature are also included for com-	
	parison.	101
6.8	(a) Schematic of a diode with two distinct barrier regions. (b) Log I vs V	
	plot of a Ni/Ga_2O_3 Schottky diode at room temperature showing two linear	
	regions, modeled as two regions of different barrier heights	103
6.9	Percentage of devices showing double barrier behavior at room temperature	
	for (a) Ni and (b) Cu as a function of contact size	105

7.1	(a) Photo of vapor deposition system at Structured Materials Industries Inc.	
	(SMI), used in growing all samples discussed in this chapter. (b) Close-up	
	photo of the growth chamber, containing the 13-inch graphite platter	111
7.2	Growth rates as a function of (a) chamber pressure and (b) substrate tem-	
	perature	113
7.3	10-90° θ -2 θ XRD scan of β -Ga ₂ O ₃ oriented film grown on <i>c</i> -plane sapphire	
	(001) substrate. \ldots	114
7.4	Rocking curves around the (402) peak for SMI's epitaxial film of $\beta\text{-}\mathrm{Ga_2O_3}$	
	on sapphire substrates. The FWHM is 0.145°	114
7.5	Transmittance spectra of bulk sapphire substrate, bulk $\beta\text{-}\mathrm{Ga_2O_3}$ substrate,	
	and a β -Ga ₂ O ₃ thin film grown on sapphire by SMI	115
7.6	Transmittance spectra of bulk β -Ga ₂ O ₃ substrate and β -Ga ₂ O ₃ thin film on	
	sapphire near their adsorption edge	116
7.7	TEM images of (a) interface between β -Ga ₂ O ₃ and sapphire substrate. Growth	
	is epitaxial with an abrupt interface. (b) β -Ga ₂ O ₃ epitaxial layer on sapphire	
	substrate. Columnar growth can be observed after ${\sim}100$ nm	117
7.8	(a) 2 \times 2 $\mu \rm{m}$ AFM images and (b) SEM image of $\beta \rm{-}Ga_2O_3$ thin film on	
	sapphire	118
7.9	SEM of β -Ga ₂ O ₃ thin film grown on untreated bulk β -Ga ₂ O ₃ single crystal	
	substrate.	119
7.10	(a) 5 \times 5 μm AFM image and (b) SEM image of $\beta \text{-}\text{Ga}_2\text{O}_3$ thin film grown	
	on cleaned bulk β -Ga ₂ O ₃ single crystal substrate.	120
8.1	Unit cells of (a) α -Ga ₂ O ₃ , (b) β -Ga ₂ O ₃ , (c) ε -Ga ₂ O ₃ , and (d) sapphire.	122
0.1	(a) a - (a)	144

8.2	Plane view crystal structure of (a) sapphire (001), (b) α -Ga ₂ O ₃ (001), (c)	
	β -Ga ₂ O ₃ ($\overline{2}01$), and ε -Ga ₂ O ₃ (001). This shows an average lattice mismatch	
	between α , β , ε -Ga ₂ O ₃ to sapphire of 4.7%, 6.12%, and 5.6% respectively.	124
8.3	XRD of α , β , and ε -Ga ₂ O ₃ . The growth conditions are shown in Table. 8.3.	126
8.4	(a) Transmittance spectra and (b) optical adsorption edge of α , β , and ε -	
	Ga_2O_3 films with growth conditions shown in Table. 8.3	127
8.5	Cross-section TEM of β -Ga ₂ O ₃ film on sapphire (006)	128
8.6	XRD of five heteroepitaxial Ga2O3 grown on c-plane sapphire under growth	
	conditions given in Table 8.4	129
8.7	SEM images showing growth of heteroepitaxial β -Ga ₂ O ₃ grown on <i>c</i> -plane	
	sapphire under growth conditions given in Table 8.4. Images correspond to	
	(a) $\#86$, (b) $\#87$, (c) $\#88$, and (d) $\#89$, respectively	130
8.8	HRTEM of near-substrate growth of Ga_2O_3 on <i>c</i> -plane sapphire along the	
	[110] zone axis. A interfacial layer of α -Ga ₂ O ₃ with approximate thickness of	
	${\sim}10~\mathrm{nm}$ begins growing on the substrate, followed by a thick layer of $\varepsilon\text{-}\mathrm{Ga}_2\mathrm{O}_3$	131
8.9	(a) HRTEM of α -Ga ₂ O ₃ /sapphire interface along the [110] zone axis. (b)	
	Crystal model of α -Ga ₂ O ₃ along the (110) view direction. (c) We can match	
	the lattice planes observed to be the $(1\overline{1}2)$ and $(1\overline{1}4)$ planes. Since the	
	spacings of the (100) planes of α -Ga ₂ O ₃ and sapphire are 4.3 Å and 4.1	
	Å respectively, every 20 layers of $\alpha\text{-}\mathrm{Ga_2O_3}$ corresponds to 21 layers of sap-	
	phire. A misfit dislocation is generated every 8.6 nm. (d) Selected area	
	diffraction of α -Ga ₂ O ₃ along the [110] zone axis	132

8.10	(a) HRTEM of ε -Ga ₂ O ₃ / α -Ga ₂ O ₃ interface along the [$\overline{1}10$] zone axis. (b)	
	Crystal model of ε -Ga ₂ O ₃ along the ($\overline{1}10$) view direction. (c) We can match	
	the lattice planes observed to the (116) and $(\bar{1}\bar{1}6)$ planes. (d) Selected area	
	diffraction of ε -Ga ₂ O ₃ along the [$\overline{1}$ 10] zone axis	133
8.11	$\rm Cl^-$ concentration in atoms/cc and $\rm Ga_2O_3,~Al_2O_3$ and O concentration in	
	arbitary units, measured by SIMS for samples (a) $\#105$, (b) $\#112$, and	
	(c) #165, respectively. Sample #105 shows $\alpha\text{-phase}$ and was grown at low	
	temperature, sample #112 shows $\varepsilon\text{-phase}$ and was grown at low temperature.	
	Sample #165 shows ε -phase and was grown at high temperature	135
8.12	Cl^- concentration in three samples measured by SIMS. Sample 105 shows	
	$\alpha\text{-phase}$ and was grown at low temperature of 650 °C, sample 112 shows	
	$\varepsilon\text{-phase}$ and was grown at low temperature of 650 °C. Sample 165 shows	
	ε -phase and was grown at high temperature of 850 °C	136
9.1	Schematic of a (a) metal-oxide-semiconductor (MOS) capacitor, and (b)	
	metal-oxide-semiconductor field effect transistor (MOSFET)	139
B.1	SEM image of Zr/Au film on single crystal β -Ga ₂ O ₃ after annealing at tem-	
	peratures indicated	147
B.2	SEM image of W/Au film on single crystal β -Ga ₂ O ₃ after annealing at tem-	
	peratures indicated	148
B.3	SEM image of Mo/Au film on single crystal β -Ga ₂ O ₃ after annealing at	
	temperatures indicated	148
B.4	SEM image of Zn/Au film on single crystal $\beta\text{-}\mathrm{Ga_2O_3}$ after annealing at tem-	
	peratures indicated	149

B.5	SEM image of Sc/Au film on single crystal β -Ga ₂ O ₃ after annealing at tem-	
	peratures indicated	149

List of Tables

1.1	Material properties of major semiconductors and β -Ga ₂ O ₃ [1]	3
2.1	Crystal Data for β -Ga ₂ O ₃ [9]	8
2.2	Calculated and experimental lattice parameters of a β -Ga ₂ O ₃ unit cell [2].	9
2.3	Figure of merit comparison between Ga_2O_3 and other common semiconduc-	
	tors [10]	12
5.1	Work functions ϕ_M of selected metals [11]	60
5.2	Properties and observed behavior of metals investigated as contacts to Sn-	
	doped ($\overline{2}01$) β -Ga ₂ O ₃	83
6.1	Summary of Schottky contacts to Ga_2O_3 reported in the literature. Abbre-	
	viations used are defined below the table. $FZ = floating zone growth, CZ$	
	= Czochralski method growth, $EFG = edge-defined$ film-fed growth, $PES =$	
	photoemission spectroscopy, $IPE = internal photoemission$, $HVPE = halide$	
	vapor phase epitaxy.	89
6.2	Work functions ϕ_M of selected metals [11]	93

6.3	Summary of results from $I-V$ measurements of Schottky diodes pre-treated	
	with different wet chemicals	98
6.4	Summary of results from Schottky metal comparison	100
8.1	Crystal parameters for α -Ga ₂ O ₃ , β -Ga ₂ O ₃ , ε -Ga ₂ O ₃ [12, 13]	122
8.2	Summary of epitaxial growths of α - and ε -Ga ₂ O ₃ reported in the literature.	
	RF = radio frequency, CVD = chemical vapor deposition.	125
8.3	Growth conditions for α , β , and ε -Ga ₂ O ₃ films with growth conditions shown	
	in Table. 8.3. All films were grown at 650 °C.	127
8.4	Growth conditions for four growth runs of heteroepitaxial Ga_2O_3 on <i>c</i> -plane	
	sapphire using HVPE.	129
8.5	Growth conditions for three growth runs of heteroepitaxial Ga_2O_3 on c -plane	
	sapphire using HVPE. Sample 105 shows α -phase and was grown at low	
	temperature, sample 112 shows $\varepsilon\text{-}\mathrm{phase}$ and was grown at low temperature.	
	Sample 165 shows ε -phase and was grown at high temperature	135



Introduction

It has been said that if future historians were to give our modern age a label, it could very well be known as "the semiconductor age" [14]. Indeed, semiconductor science form the foundation for the rapid technological advancement of the digital era. This is especially true for silicon–the second most abundant element on the planet after oxygen–and is found in almost every modern day electronic device. However, silicon has inherent shortcomings for applications in fields such as optoelectronics, power and high-temperature electronics, photovoltaics and solid state lighting, where other semiconducting materials show superior properties and are therefore becoming increasingly important. Semiconducting oxides is an example of such a group of materials that have garnered the spotlight in recent years.

By conventional definitions, oxides are generally considered insulating materials with limited functionality. However, with doping, oxides can exhibit electrical conductivities ranging from highly-insulating to metallic. Oxides bandgaps can also be tuned from below 1 eV to about 5 eV through alloying. Of all the semiconducting oxides, Ga₂O₃ has largest bandgap of ~4.8 eV [2,15,16]. β -Ga₂O₃ is the most stable phase out of the five polymorphs of Ga₂O₃ [?], it fact it is the only stable phase up to at least 1800 °C [17]. Its bandgap can be tuned by alloying with Al_2O_3 [18] or In_2O_3 [19]. Pure, undoped and stoichiometric, β -Ga₂O₃ behaves as an electrical insulator, but it can be doped with Si, Sn and other dopants to become *n*-type semiconducting.

Earlier interest on β -Ga₂O₃ focused primarily on its potential as one of the transparent conducting oxides (TCOs). Due to its wide bandgap, β -Ga₂O₃ is not only transparent to visible light but the deep ultraviolet (UV) wavelength region too. Unlike other TCOs such as ZnO, studies on Ga₂O₃ was scarce simply because of the difficulties in preparing single crystals and thin films of sufficient quality. It was only the last decade or so when high quality Ga₂O₃ has been successfully produced. In the intervening years, the feasibility of a variety of devices based on β -Ga₂O₃ have been demonstrated, including transparent conductors and electrodes [20], light-emitting diodes, transparent electronic devices, gas sensors, phosphors and in particular UV-blind photodetectors which takes advantage of β -Ga₂O₃'s large bandgap [18,21–27].

Around 2013, the first Schottky diodes (SDs) and field-effect transistors (FETs) on Ga_2O_3 were demonstrated. This brought about a renewal of interest as its potential as a wide bandgap material was recognized. The breakdown field of β -Ga₂O₃ is estimated to be 8 MV/cm, which is about three times larger than those of conventional wide bandgap materials such as 4H-SiC and GaN. Baliga's figure of merit (FOM) is a basic parameter showing how suitable a material is for power devices, defined as $\xi \mu E_c^{-3}$ [28]. Note that the FOM is proportional to the cube of the breakdown field, but only linearly proportional to the electron mobility and dielectric constant. Therefore, Baliga's FOM of β -Ga₂O₃ is at least four times larger than those of 4H-SiC or GaN. As shown in Table 1.1, besides diamond, β -Ga₂O₃ possesses the most favorable material properties concerning high-power electronics among the materials compared, but it is much cheaper to manufacture due to

its available to be grown from the melt.

	Si	GaAs	4H-SiC	GaN	Diamond	β -Ga ₂ O ₃
Band Gap E_g (eV)	1.1	1.4	3.3	3.4	5.5	4.8-4.9
Breakdown Field E_c (MV/cm)	0.3	0.4	2.5	3.3	10	8
Electron mobility $\mu \ (\mathrm{cm}^2/\mathrm{Vs})$	1,400	8,000	$1,\!000$	1,200	2,000	300
Relative dielectric constant ξ_s	11.8	12.9	9.7	9.0	5.5	10
Baliga's FOM $\xi \mu E_c^{\ 3}$	1	15	340	870	$24,\!664$	3444

Table 1.1: Material properties of major semiconductors and β -Ga₂O₃ [1].

Fig. 1.1 shows the theoretical limits of on-resistance as a function of breakdown voltage for these semiconductors, as calculated using the parameters shown in Table 1.1. Again β -Ga₂O₃ is superior to all other material besides diamond.

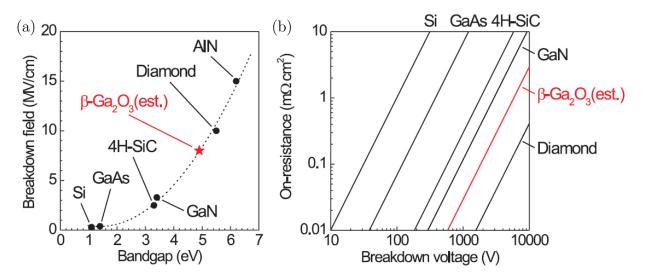


Figure 1.1: Theoretical limits of (a) breakdown field as a function of bandgap, and (b) onresistance of various wide bandgap materials as a function of breakdown voltages [1].

In our study of β -Ga₂O₃, we have found that knowledge about contacts to β -Ga₂O₃ is very scarce at the moment, even though it is evidently crucial in the fabrication of any β -Ga₂O₃-based device. Our research is therefore focused on understanding and finding

reliable contacts to β -Ga₂O₃.

The remainder of this document is organized as follows:

In Chapter 2, I will provide background review of β -Ga₂O₃. This includes crystallographic and electronics structure of β -Ga₂O₃; growth of single crystal and thin films material; fabrication and characterization of contacts to β -Ga₂O₃; and a brief summary of electronic and optoelectronic devices that hitherto have been demonstrated for β -Ga₂O₃.

In Chapter 3, I will provide an introduction to the theory behind metal-semiconductor contacts. This includes the formation of ohmic and Schottky contacts and the different transport mechanisms that creates them.

In Chapter 4, I present results from our characterization study on Sn-doped β -Ga₂O₃ ($\overline{2}01$) single crystal substrates (purchased from Tamura Corp.) and lightly doped β -Ga₂O₃ (010) epitaxial layer grown by MBE on sapphire (0001) (from Novel Crystal Technology Inc). A variety of techniques including X-ray diffraction (XRD) and electron microscopy will be used to study the crystallographic, morphological, optical and electrical properties of these materials.

In Chapter 5 and 6, I present results from our work on contacts to both Sn-doped single crystal and heteroepitaxial films of β -Ga₂O₃. Amongst the metals investigated for ohmic contacts include Ti, In, Ag, Sn, W, Mo, Sc, Zn, and Zr. We found that Ti/Au consistently form ohmic contacts to both doped single crystal β -Ga₂O₃ and undoped epitaxial β -Ga₂O₃. In, while exhibiting ohmic behavior, has a tendency to dewet the substrate surface. Schottky contacts of W, Cu, Ni, Ir, and Pt are also investigated and Schottky barrier heights (SBHs) were calculated.

In Chapter 7 and 8, we characterized films grown at Structured Materials Industries (SMI) Inc. on sapphire (001) and bulk β -Ga₂O₃ using MOCVD and HVPE techniques.

MOCVD consistently provides films of β -Ga₂O₃, while HVPE produced metastable phases α -Ga₂O₃ and ε -Ga₂O₃. We hypothesized that the presence of Cl impurity competes with oxygen in the crystal lattice, causing compressive stress which promotes the growth of the metastable phases.

Finally in Chapter 9, we will summarize our all of our findings and outline future work to be done.



Literature Review on β -Gallium Oxide (β -Ga₂O₃)

2.1 Properties of β -Ga₂O₃

2.1.1 Crystal Structure

 β -Ga₂O₃ has a base-centered monoclinic structure with space group C2/m [29]. Crystal data for β -Ga₂O₃ are listed in Table 2.1.

 β -Ga₂O₃ contains two crystallographically inequivalent Ga atoms, as shown in Fig. 2.1. Ga₁ sites have a distorted tetrahedral coordination geometry and are shown as green polygons, Ga₂ sites have a distorted octahedral coordination geometry and are shown as purple polygons. The O atoms are in distorted cubic close pack geometry with three inequivalent sites, two of which are in threefold coordinated geometry and one is in a fourfold coordinated geometry.

able 2.1 : Crystal L	Data for β -Ga ₂ O ₃ [9]
M_r	187.44
Space Group	C2/m
a	12.214(3) Å
b	3.0371(9) Å
С	5.7981(9) Å
$oldsymbol{eta}$	103.83°
V	$208.85(1) \text{ Å}^3$
Z	4
D_x	$5.961~{\rm Mg/m^3}$

Table 2.1: Crystal Data for β -Ga₂O₃/9].

2.1.2 Electronic Structure

In a monoclinic system the shape of the Brillouin zone depends non-trivially on the lattice vectors; in fact, in a monoclinic system, five distinct shapes of the Brillouin zone are possible [30,31]. Because of this, there is still contention over the correct shape of the Brillouin zone, and therefore the band structure of β -Ga₂O₃ [17, 32–39]. The most recent calculation is reported by Peelaers and Van de Walle [2]; the Brillouin zone they have identified is shown in Fig. 2.2

Density functional theory (DFT) is used in construction the band structure and the result is shown in Fig. 2.3. According to their calculated band structure, the fundamental bandgap is indirect and has a magnitude of 4.84 eV. The direct bandgap (at Γ) is 4.88 eV and is only 0.04 eV larger than the fundamental bandgap.

Peelaers *et al.* also calculated lattice parameters as shown in Table 2.2 from a minimization of the forces and stresses. The calculated lattice parameters agreed very well with experimental values.

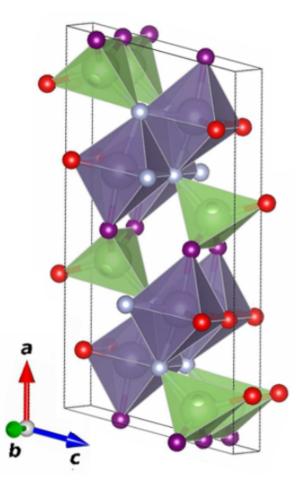


Figure 2.1: Monoclinic crystal structure of β -Ga₂O₃. The two inequivalent Ga (larger spheres) and three inequivalent O (smaller spheres) sites are indicated with different colors [2]. Reproduced with permission from the publisher.

Table 2.2:	Calculated	and	experimental	lattice	parameters	of e	$a \beta - Ga_2 O_3$	unit	cell [2].

	Calculated	Experimental
a (Å)	12.27	12.21
b (Å)	3.05	3.04
c (Å)	5.82	5.80
β (°)	103.82	103.83

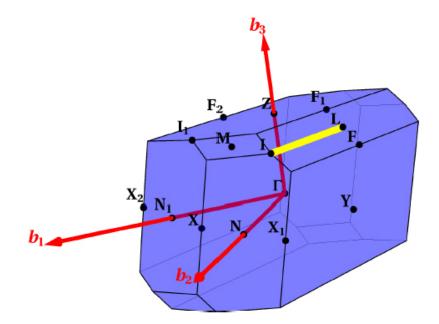


Figure 2.2: The Brillonin zone of β -Ga₂O₃. Labels indicate high-symmetry points. The axes of the reciprocal unit cell ar also shown. The yellow line represents the I-L line where the conduction band maximum lies [2]. Reproduced with permission from the publisher.

2.2 Figures of Merit (FOM)

Besides the famous Baliga's figure of merit (BFOM = $\varepsilon \mu E_C^3$), Huang [10] defined three new FOMs, defined as

- material figure of merit (HMFOM) = $E_C \sqrt{\mu}$
- chip area figure of merit (HCAFOM) = $\varepsilon E_C^2 \sqrt{\mu}$
- thermal figure of merit (HTFOM) = $\frac{\sigma_{th}}{\varepsilon E_C}$

In comparison to other traditional semiconductors and other wide bandgap materials, the material and chip area FOMs are very good for Ga_2O_3 , but the thermal FOM is very low. Refer to Table 2.3 for details.

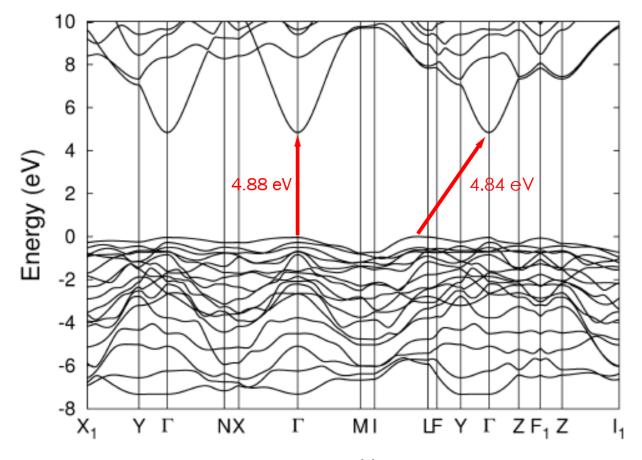


Figure 2.3: Electronic band structure of β -Ga₂O₃ [2]. Reproduced with permission from the publisher.

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Material	Electron	Relative	Material Electron Relative Critical	Thermal	BFOM	HMFOM	i Hi	HTFOM	Direct/	<u>A vailability</u>
	Mobility	Dielectric	Field E_C	Mobility Dielectric Field E_C Conductiv- $\varepsilon \mu E_C^3$	$\varepsilon \mu E_{C}^{3}$	$E_C\sqrt{\mu}$	$arepsilon E E_{C}^{2}\sqrt{\mu}$	$\sigma_{th}/arepsilon E_C$	indirect	of n - &
	$\mu ({ m cm}^2/{ m Vs})$		$\begin{array}{l} \text{Constant (MV/cm)} \\ \varepsilon \end{array}$	${ m ity}\;\sigma_{th}$ ${ m (W/mK)}$					band gap <i>p</i> -type	p-type
Si	1400	11.9	0.3	130	1	1	1	1.0000	Indirect	Yes
GaAs	8500	13.1	0.4	55	16	°	IJ	0.2882	Direct	\mathbf{Yes}
Ge	3900	16	0.1	58	0.14		0.25	0.9955	Indirect	Yes
GaP	250	11.1	1	110	9	1	4	0.2721	Indirect	Yes
SiC (6H)	330	9.66	2.4	200	98	4	25	0.8292	Indirect	Yes
SiC (4H)	1000	9.7	3.2	200	707	6	78	0.6193	Indirect	\mathbf{Yes}
GaN	1200	6	3.3	110	863	10	85	0.1017	Direct	Yes
Diamond	2000	5.5	10	2000	24454	40	614	0.9986	Indirect	No n -type
Ga2O3	300	10	8	13	3415	12	277	0.0045	Indirect	No p -type
ZnO									Direct	No p -type
c-BN									Indirect.	γ_{es}

2.3 Growth of Single Crystal β -Ga₂O₃

What makes growing β -Ga₂O₃ single crystals especially challenging is firstly, its high melting point of ~1800°C (different values are reported in the literature, including 1740°C [?], 1795°C [40], 1807°C [41] and 1820°C [42]). and secondly, its high affinity for twinning and cleaving [43, 44].

Single crystals of β -Ga₂O₃ was first grown using the Verneuil method as early as 1964 [43, 45, 46]. Within the last decade, β -Ga₂O₃ has been successfully grown from the melt using the floating zone (FZ) method [20, 44, 47–51], the Czochralski method [3, 42, 52, 53], and more recently, the edge-defined film-fed growth (EFG) method [4, 54]. 2-inch wafers of doped and undoped β -Ga₂O₃ grown by the edge-defined film-fed growth (EFG) method are available commercially from Namiki Precision Jewel Co. and Tamura Corp. in Japan.

2.3.1 Verneuil Method

In the Verneuil technique, a fine powder (typically 1-20 μ m in size) of the material to be grown is shaken through a wire mesh and allowed to fall through an oxy-hydrogen flame. The powder melts and a film of liquid is formed on the top of the seed crystal. The melt freezes as the seed crystal is slowly lowered. The challenge of the Verneuil method is to balance the rate of charge feed and the rate of lowering of the seed to maintain a constant growth rate and diameter.

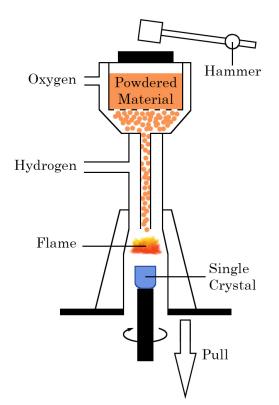


Figure 2.4: Schematic of single crystals grown using the Verneuil method.

Unfortunately the Verneuil method cannot be easily scaled up and is therefore not suitable for large-scale production of single crystals.

2.3.2 Floating Zone (FZ) Method

In the floating zone (FZ) method, the feed material is made into a sintered rod and the seed crystal is attached to one end. A molten zone is maintained by surface tension between the feed and the seed. As the zone is slowly moved along the feed, a single crystal will start forming over the seed. Fig. 2.5 shows a schematic of a growing single crystal using the floating zone technique.

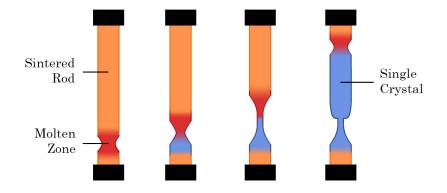


Figure 2.5: Schematic of single crystals grown using the float zone (FZ) method.

The first reported successful growth of β -Ga₂O₃ using the FZ method is by Ueda *et al.* in 1997 [20]. The feed rod was prepared with Ga₂O₃ powder (4N), grounded with or without SnO₂ powder (4N) in a mortar and pressed at 280 MPa, then sintered in air at 1300°C for 16 h. The crystal was grown at a rate of 15 mm/h in a mixture of N₂ and O₂ gas. The samples grown had dimensions of $(3-5)\times(5-10)\times(\sim0.3)$ mm. Undoped samples grown in an O₂ environment had conductivity $\sigma < 10^{-9}$ ($\Omega \cdot \text{cm}$)⁻¹. Introduction of N₂ increased the conductivity σ to as high as 38 ($\Omega \cdot \text{cm}$)⁻¹ at a N₂/O₂ ratio of 4/6. Higher concentration of N₂ caused the Ga₂O₃ growth to become unstable. The Sn-doped sample had a conductivity of $\sigma = 0.96$ ($\Omega \cdot \text{cm}$)⁻¹.

2.3.3 Czochralski Methods

In the Czochralski method, the material that is to be grown is melted in a non-reacting crucible with induction or resistance heating in a controlled environment. A seed crystal, which is maintained at a temperature lower than the melting point, is lowered into the molten melt. The temperature gradient causes the molten charge to start solidifying on the seed crystal. As the seed is slowly pulled and rotated at a controlled rate, a single crystal boule will start to grow from the melt. Fig. 2.6 shows a schematic of the Czochralski growth process.

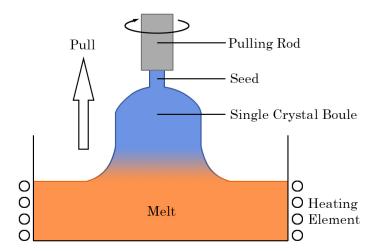


Figure 2.6: Schematic of single crystals growth using the Czochralski method.

The first successful attempt to grow single crystal β -Ga₂O₃ using the Czochralski method was reported by Tomm *et al.* in 2000 [3]. Due to β -Ga₂O₃'s high melting point of ~1800°C, an Ir crucible must be used. To prevent the Ir crucible from oxidizing, the growth environment should be kept oxygen-deficient. However, an oxygen-deficient environment promotes the decomposition of Ga₂O₃ into the more volatile GaO, Ga₂O and Ga species.

$$Ga_2O_3(s) \to 2GaO(g) + \frac{1}{2}O_2(g)$$
 (2.1)

$$2GaO(g) \rightarrow Ga_2O(g) + \frac{1}{2}O_2(g)$$
(2.2)

$$Ga_2O(g) \to 2Ga(g) + \frac{1}{2}O_2(g)$$
 (2.3)

To mitigate this problem, Tomm *et al.* used a dynamic growth atmosphere of 90% Ar and 10% CO₂. CO₂ decomposes as CO₂ \rightarrow CO $+\frac{1}{2}$ O₂ as a function of temperature. This provided high oxygen concentration at higher temperature and low oxygen concentration at lower temperature. Ga₂O₃ (5N) was used as the starting material, growth rate was 2 mm/h and the rotation rate is 15 min⁻¹. Fig. 2.7 shows a photograph of a crystal grown by Tomm *et al.*; the boule has diameter of ~1 cm, and appears transparent.

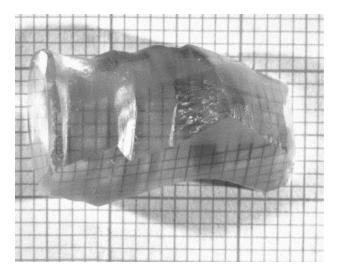


Figure 2.7: Picture of undoped Ga_2O_3 crystal grown by Tomm et al. [3]. Reproduced with permission from publisher.

Galazka and Irmscher *et al.* expanded on this work and studied the growth of Ga_2O_3 under several different conditions [42]. They found that CO_2 combined with overpressure (up to 20 bar) is very effective in suppressing Ga₂O₃ decomposition and Ir oxidation. The Czochralski grown β -Ga₂O₃ exhibited a relatively high dislocation density, with etch pit density (EPD) of $\sim 10^5$ cm⁻². Electron concentration was measured to vary between ~ 3 – 5×10^{17} cm⁻³. Resistivity was measured to vary between 0.095–0.150 Ω -cm. Mobility was measured to vary between 120–130 cm²/(V·s). Donor activation energy was measured to vary between 30–40 meV. Higher electron concentrations were obtained for samples grown with CO₂. Despite higher oxygen partial pressure, the decomposition of CO₂ has a reducing effect and promotes oxygen deficiency.

2.3.4 Edge-Defined Film-Fed Growth (EFG) Method

In 1965, Harold Labelle at Tyco Industries started developing a method for growing sapphire fibers by inserting tungsten wire into a molten alumina melt, he observed that crystalline alumina dendrites formed on the tungsten, and by controlling the temperature he could increase the size of the resulting crystals. Eventually, dies with orifices corresponding to the desired shape and size of the crystals were used. The resulting process is essentially a Czochralski growth technique with control over the crystal size, which LaBelle coined the edge-defined film-fed growth method (EFG) [55, 56]. EFG today remains very similar to the process Labelle developed almost half a century ago, and is used in the large-scale production of sapphire single crystals.

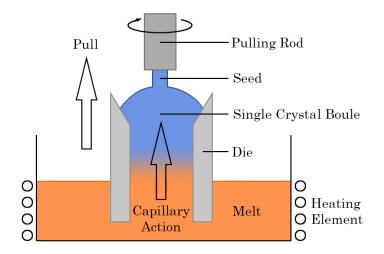


Figure 2.8: Schematic of single crystals grown using the edge-defined film-fed growth (EFG) method.

Like in the Czochralski method, the starting material is melted in a non-reacting crucible with induction or resistance heating in a controlled environment. A die with an orifice is placed into the melt, capillary action caused the melt to travel up the orifice to the top of the die, where a seed of specific crystallinity draws out a crystal according to the shape of the die—typically a tube, rod or ribbon. The EFG method provides the ability to produce crystals of specific shapes that are impossible with other techniques, and therefore saves post-processing costs associated with machining and other finishing processes. The method also allows for control over crystal orientation. Fig. 2.8 shows a schematic of the edge-defined film-fed (EFG) growth process.

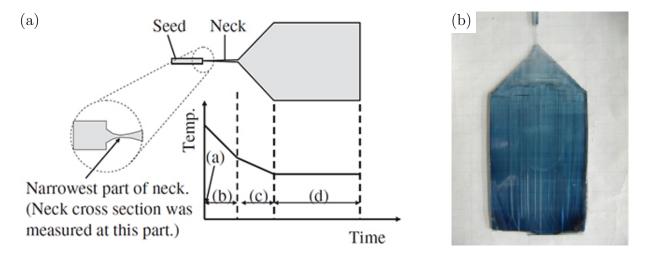


Figure 2.9: (a) Illustration of the four growth stages and typical temperature profile used by Aida et al. in the growth of single crystal β -Ga₂O₃ by EFG. The four stages are (a) seeding, (b) necking, (c) spreading and (d) main growth. (b) Photograph of as-grown single crystal β -Ga₂O₃ ribbon [4]. Reproduced with permission from publisher.

The first reported growth of Ga_2O_3 using the EFG method is by Shimamura *et al.* in 2006 [54]. While they demonstrated the possibility of growing Ga_2O_3 with the EFG method, the 2-inch crystals that they grew suffered from severe cracks and polycrystalline inclusions. Soon after, Aida *et al.* at Namiki Precision Jewel Co. Ltd. reported the successful growth of single crystalline β -Ga₂O₃ of up to 50 mm in diameter by the EFG method [4]. Their growth process consisted of four stages, as shown in Fig. 2.9(a), specifically the four stages are 1) seeding, 2) necking, 3) spreading and 4) main growth process. During necking, the growth temperature was higher and the seed was pulled at high speed to form a thin neck. Aida *et al.* found that the two most important factors in ensuring single crystal growth are the seeding temperature and neck cross section. Since dislocations in the seed crystal will propagate during growth, a narrower neck minimizes the number of dislocations that will propagate into the grown crystal. A higher seeding temperature discourages the formation of new dislocations and promotes atomic rearrangements. Typical etch pit density (EPD) was measured to be $1-5 \times 10^{-4}$ cm⁻².

2.4 Epitaxial Growth of β -Ga₂O₃ Thin Films

Heteroepitaxial thin films of β -Ga₂O₃ have been deposited on substrates including Al₂O₃, Si, GaAs, TiO₂, ZrO₂:Y, MgO [57]. Epitaxial β -Ga₂O₃ were grown by various methods including atomic layer deposition (ALD) [58–61], molecular-beam epitaxy (MBE) [24,62,63], pulsed laser deposition (PLD) [16,64], and MOCVD [65,66]. Step-flow homoepitaxial β -Ga₂O₃ thin layers was demonstrated by plasma-assisted MBE [67] and MOCVD [57]. In this document, we will focus only on MOCVD and MBE because these are two most dominate growth methods known to yield good quality epitaxial film with atomic layer control.

2.4.1 Molecular Beam Epitaxy (MBE)

In MBE, high purity powders containing the elements of the material to be grown are heated in separate effusion cells under high vacuum. The powders are heated to slowly sublime and condense on the substrate surface. The key to MBE growth is the slow rate of growth which allows the film to grow epitaxially in a step-flow manner.

Oshima *et al.* grew (201) oriented β -Ga₂O₃ film on *c*-plane sapphire using MBE [?]. Evaporated Ga from an effusion cell and rf-activated oxygen plasma were used in the growth. α -Ga₂O₃ inclusions were observed for films grown at lower temperature; this is because α -Ga₂O₃ has the same corundum structure as the sapphire substrate. Higher growth temperature promotes the formation of a single stable β phase. Oshima *et al.* later grew homoeitaxial β -Ga₂O₃ on single crystal (100) oriented β -Ga₂O₃ [67]. Based on their earlier work, 800°C was chosen for the growth temperature. Step flow growth was observed and the RMS surface roughness was measured to be 0.5 nm under AFM.

In 2013 Sasaki *et al.*. grew device-quality, Sn-doped *n*-type β -Ga₂O₃ homoepitaxial

films using MBE [?]. Ga and Sn were evaporated from Ga (6N) metal and SnO₂ (4N) powder heated in normal Knudsen cells. The oxygen source was an ozone (5%)–oxygen(95%) gas mixture. A range of growth temperature between 600°C and 800°C was used. The carrier concentration of the β -Ga₂O₃ epitaxial layers could be varied within a range of 10¹⁶–10¹⁹ cm³ by changing the SnO₂ K-cell temperature. They found that the epitaxial growth rate on the (010) plane was about ten times more than that on the (100) plane. The growth rate on the (100) plane was much lower than on other orientations because the adhesion energy on (100) terraces is lower than on other planes, so atoms supplied to the (100) surface are more likely to re-evaporate.

2.4.2 Metal-Organic Chemical Vapor Deposition (MOCVD)

In MOCVD, metalorganics containing the metal element to be grow are flowed into a high vacuum chamber through a carrier gas. The precursors chemically react on a heated substrate in step-flow growth mode and form an epitaxial film on the substrate. Deposition of Ga_2O_3 films by MOCVD offers many advantages, including conformal deposition over device topography and the capability for scale-up to high-volume production, good step coverage, producing uniform, pure, reproducible, and adherent films.

Kim *et al.* first grew Ga_2O_3 film on *p*-type Si using MOCVD in 2004 [68]. Trimethylgallium (TMGa) and O_2 are used as precursors, and growth was carried out at temperatures between 500–600°C. Ar was used as a carrier gas and the TMGa bubbler was hold at -5 °C.

More recent attempts at MOCVD film growth used a rotating disc vertical MOCVD reactor. Gases are flowed in from an upper showerhead unto a substrate platter, which is a horizontal, rotating disc. This method has proven to achieve uniform film deposition on large-area substrates [66, 69, 70].

Wagner *et al.* successfully grew homoepitaxial film of β -Ga₂O₃ using MOCVD on β -Ga₂O₃ (100) [57]. TMGa and pure oxygen or water were used as precursors. They found that with pure oxygen as the oxidant, nano-crystals in the form of wires or agglomerates were observed. With water as an oxidant, smooth homoepitaxial β -Ga₂O₃ layers were obtained under suitable conditions.

2.5 Devices

2.5.1 Schotty Barrier Diodes (SBDs)

One of the simplest semiconductor devices to fabricate is a SBD. This device has two terminals consisting of an ohmic and an Schottky contact on a semiconductor structure. If the ohmic contact is biased negative with respect to the Schottky contact, the device is forward biased, the barrier to current flow is reduced and electrons can readily overcome the barrier through thermionic emission or tunneling. If the ohmic contact is biased positive with respect to the Schottky contact, the device is reverse biased, the barrier is increased and only a very small amount of current flows into the semiconductor. Asymmetric I-Vcurves are characteristic of Schottky diodes.

In 2013, Sasaki *et al.* reported the first SBDs on undoped single crystal β -Ga₂O₃ [5]. a schematic of the their SBD is shown in Fig. 2.10. A Pt/Ti/Au (15 nm / 5 nm / 250 nm) stack is used as the Schottky contact. The bottom surface was first treated by reactive ion etching (RIE) before an ohmic contact of Ti/Au (20 nm / 250 nm) was deposited. The Schottky barrier height of Pt on β -Ga₂O₃ was calculated to be 1.35–1.52 eV. However, they used an electron effective mass of $m^* = 0.342m_0$ [35] to calculate an effective Richardson constant of 41.1 A/(cm·K)², which has been reported to be incorrect [2]. The correct

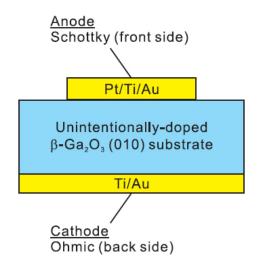


Figure 2.10: Schematic of Schottky barrier diode fabricated by Sasaki et al. [5]. Reproduced from original article, © 2013 IEEE.

electron effective mass is $m^* = 0.28m_0$ [2,17,38], which would give a Richardson constant of 33.65 A/(cm·K)². The current-voltage plots of their SBD are shown in Fig. 2.11.

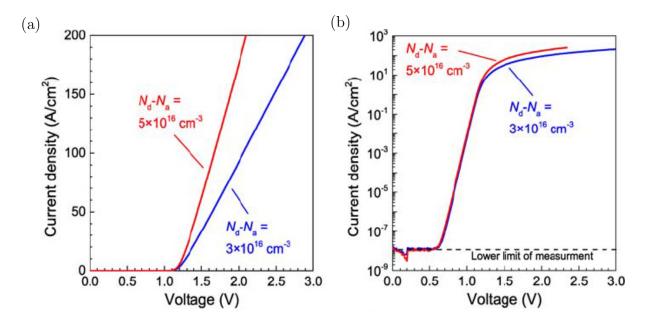


Figure 2.11: Foward J-V characteristics of two different Ga_2O_3 SBDs in (a) logarithmic and (b) linear plots [5]. Reproduced from original article, \bigcirc 2013 IEEE.

Sasaki *et al.* later fabricated a SBD on a n^- -Ga₂O₃ epitaxial layer on a n^+ -Ga₂O₃ substrate. The substrate and epitaxial layer were a Si-doped β -Ga₂O₃ (010) and a 1.2 μ m thick Sn doped β -Ga₂O₃, with $N_d - N_a$ were 1×10^{19} cm⁻³ and 6×10^{16} cm⁻³ respectively. Au was used as the Schottky contact, an ideality factor of 1.13 was calculated but no Schottky barrier height was reported.

2.5.2 Field-Effect Transistors

A simple field-effect transistor (FET) consists of a conducting channel between two ohmic contacts, defined as the source and the drain. The conductivity of the channel is modulated using a Schottky contact positioned between the source and the drain, which is referred to as as a gate. The control over the conductivity is achieved by altering the number of the carriers beneath the gate.

Sasaki and Higashiwaki *et al.* [1,6,71] first demonstrated a MESFET on a homoepitaxial structure of β -Ga₂O₃. A schematic of the MESFET is shown in Fig. 2.12(a).

From the DC output characteristics of the Ga_2O_3 MESFET shown in Fig. 2.12(b), we observe a perfect pinch-off of the drain current, an off-state breakdown voltage of 250V, a high on/off drain current of around 10^4 and a small gate leakage current.

Higashiwaki *et al.* [7] later demonstrated the first MOSFET on a homoepitaxial structure of β -Ga₂O₃. A schematic of the MOSFET is shown in Fig. 2.13(a). A 20 nm Al₂O₃ film was used as a gate dielectic layer.

From the DC output characteristics of the Ga_2O_3 MOSFET shown in Fig. 2.13(b), they found breakdown voltage of 370V, a high on/off drain current of over 10 orders of magnitude.

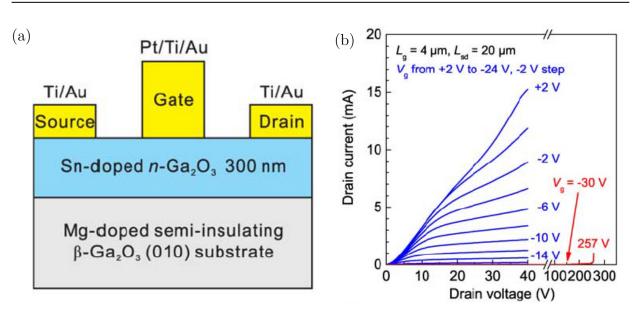


Figure 2.12: (a) Cross-sectional schematic and (b) DC output characteristics of a Ga_2O_3 MES-FET [6]. Reproduced with permission from publisher.

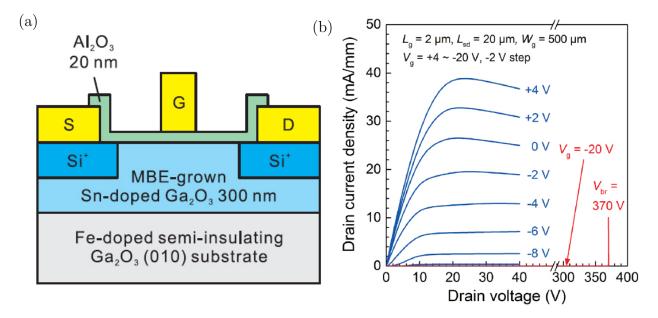


Figure 2.13: (a) Cross-sectional schematic and (b) DC output characteristics of a Ga_2O_3 MOS-FET [7]. Reproduced with permission from publisher.

2.5.3 Photodetectors

All semiconductor photodetectors work on the same fundamental principle, through the creation of electron-hole pairs under illumination by light (Fig. 2.14).

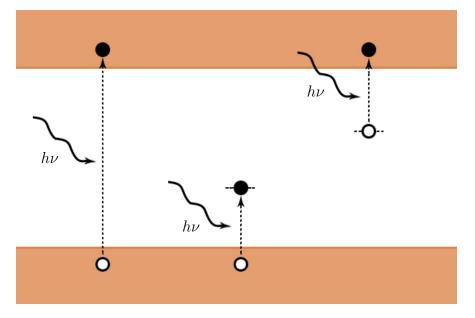


Figure 2.14: Operating principle for photodetectors.

When a photon with sufficient energy is incident on the semiconductor, the photon can be absorbed to excite an electron into a conductive band where it is free to move within the crystal. The promoted electron leaves behind a hole in its place, which is also free to move within the crystal. This process creates what is commonly referred to as an electron-hole pair. Under the influence of an electric field, the electrons and holes will drift to form a photocurrent. Electrodes at the edges of the active area of the semiconductor collects these mobile carriers in a circuit, the photocurrent can then be measured as an indication of photoresponse. Ideally, every single incident photon will be absorbed to form an electron-hole pair, and all electrons and holes will contribute to the photocurrent. In practice however, not all photons are absorbed, and electrons and holes often recombine before they can be swept away by the electric field to instead give up their energy as heat.

A photodetector is often described by its quantum efficiency (η). η is defined as the ratio of countable events produced per number of incident photons, in other words it is the probability of creating an electron-hole pair from an absorbed photon. It is equal to the current responsivity times the photon energy of the incident radiation.

$$\eta = \frac{I_{ph}/q}{P_{inc}/h\nu} = \frac{I_{ph}h\nu}{qP_{inc}}$$
(2.4)

 P_{inc} is the incident optical power in watts, I_{ph} is the photocurrent, h is the Plank constant and ν is the radiation frequency.

Responsivity or response coefficient (R) is the ratio of number of incident photons per incident optical power. It depends on wavelength, bias voltage, and temperature. The plot of responsivity as a function of wavelength gives the spectral response of the detector. λ is the radiation wavelength, c is the speed of light and q is the electron charge.

$$R = \frac{I_{ph}}{P_{inc}} = \frac{q\eta}{h\nu} = \frac{q\eta\lambda}{hc}$$
(2.5)

It is important to note that the responsivity is independent of the active optical surface of the photodetector structure.

2.6 β -Ga₂O₃ Ultraviolet Photodetectors

In recent years, ultraviolet (UV) photodetectors (PDs) have received much attention in the various field due to wide range of industrial, military, biological and environmental applications. UV PDs have many applications in various areas such as engine control, solar UV monitoring, astronomy, lithography aligners, secure space-to-space communications, or detection of missiles. In particular UV PDs have attracted significant attention in the recent years, due to the rise of new requirements of civil and military industries to improve UV instrumental that is capable of operating at high temperature and in harsh environments. Therefore, many attempts have been made to fabricate PD devices with these features for operation in the UV region of the spectrum whilst remaining blind to visible wavelengths.

 β -Ga₂O₃ is a promising candidate for a deep-ultraviolet photodetector that is blind to wavelengths above 280 nm, the so-called solar-blind photodetector. Ga_2O_3 photodetectors have been demonstrated using thin films [18, 21, 24], nanowires [22, 23], and single crystal [25–27]. Kokubun *et al.* reported that photodetectors based on sol-gel prepared β - Ga_2O_3 films have a spectral response with solar-blind sensitivity with responsivity of 8×10^{-5} A/W [18]. The low detector efficiency may be due to the polycrystalline films and the wide electrode distance of 1.5mm, and therefore the improvements in film quality and device structure are necessary to improve the detector performance. they later showed enhanced responsivity to 10^3 A/W in Au-Ga₂O₃ Schottky photodiodes fabricated on single-crystal substrates by postannealing at 400 °C[26]. Oshima et al. demonstrated ohmic-type metalsemiconductor-metal photodetector fabricated on ($\bar{2}01$)-oriented β -Ga₂O₃ thin films grown on c-plane sapphire. Under 254nm light illumination and 10 V bias voltage, the photoresponsivity was 0.037 A/W, which corresponded to a quantum efficiency of 18% [24]. They later demonstrated a Schottky photodetector with a high responsivity of 2.6-8.7 A/W based on β -Ga₂O₃ single crystals with a high resistive surface layer formed by annealing at 1100 °C for 6 h [67].



Metal Contacts

3.1 Metal-Semiconductor Contacts

Most electrical connections to semiconductor based electronics are provided by metalsemiconductor contacts. The physics behind the rectifying behavior of a metal-semiconductor system was proposed independently by Schottky [72] and Mott [73] in 1938. They both theorized that a potential barrier would arise due to accumulation of space charge, which they named the Schottky barrier and Mott barrier respectively.

The barrier can be better visualized by considering an energy-band diagram as shown in Fig. 3.1. When the metal and semiconductor are brought in contact with each other, carriers will flow till the Fermi levels align and equilibrium is reached. This leads to the accumulation of a space charge region and causes the energy bands to bend. This later became known as the Schottky-Mott model which described the behavior of an ideal metal-semiconductor contact. In the Schottky-Mott model, the barrier height to charge injection (ϕ_b) at the interface is a strict function of the metal work function (ϕ_m) and the electron affinity of the semiconductor (χ_s). The so-called Schottky barrier height for n-type semiconductors is

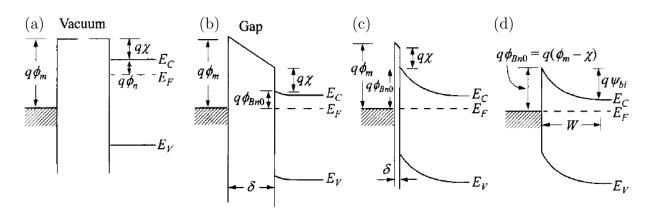


Figure 3.1: Energy-band diagrams of metal-semiconductor contacts. The metal and semiconductor energy bands when they are (a) in separate system, (b) brought into one system but separated by a gap δ , (c) as the gap δ is reduced and (d) as the gap δ becomes zero [8]. Reproduced with permission from publisher.

expressed as

$$q\phi_b = q(\phi_m - \chi_s) \tag{3.1}$$

The Schottky-Mott model provides a good starting point to approximating the actual Schottky barrier, but in reality, the surface of β -Ga₂O₃ is not ideal due to the presence of surface states and other anomalies. Empirical measurements of barrier height often deviate from the Schottky-Mott model.

3.2 Current Transport Mechanisms

Fig. 3.2 shows the five main processes for current transport across metal-semiconductor contacts.

Of the five, thermionic emission and tunneling are the main transport mechanisms. Their relative dominance results in three current transport mechanisms: (1) thermionic

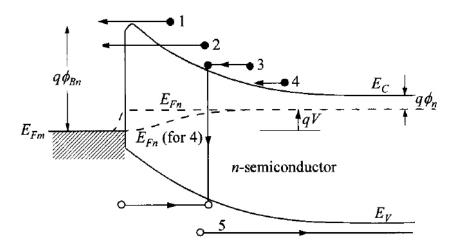


Figure 3.2: Five basic transport processes under forward bias. (1) Thermionic emission, (2) tunneling, (3) recombination, (4) diffusion of electrons and (5) diffusion of holes [8]. Reproduced with permission from publisher.

emission (TE) over the barrier, (2) field emission (FE) near the Fermi level through tunneling and (3) thermionic-field emission (TFE) at an energy between TE and FE. While FE is a pure tunneling process, TFE is tunneling of thermally excited carriers which see a thinner barrier than FE. The relative criterion can be set by comparing the thermal energy kT to E_{00} which is defined as

$$E_{00} = \frac{q\hbar}{2} \sqrt{\frac{N}{M^* \varepsilon_s}} \tag{3.2}$$

Where N is the carrier concentration, M^* is the electron effective mass, and ε_s the semiconductor perimittivity. When $kT \gg E_{00}$, TE dominates and the original Schottky-barrier behavior prevails without tunneling. When $kT \ll E_{00}$, FE (or tunneling) dominates. When $kT \approx E_{00}$, TFE is the main mechanism which is a combination of TE and FE.

TE is favored when the semiconductor is lightly doped, where the width of the space charge region is wide and difficult to tunnel through, and when the metal has a high work function, which according to the Schottky-Mott equation produces a higher barrier height. It is the predominate transport mechanism across Schottky contacts.

Conversely, FE is favored when the semiconductor is heavily doped, where the width of the space charge region is narrow and easy to tunnel through, and when the metal has a low work function, which according to the Schottky-Mott equation produces a low barrier height or no barrier height. It is the predominate transport mechanism across ohmic contacts.

3.2.1 Schottky Contacts

According to thermionic emission theory, A Schottky diode under forward bias has the following current-voltage (I-V) characteristics,

$$I = I_S \left[\exp \frac{qV_D}{kT} - 1 \right] \tag{3.3}$$

where q is the electronic charge, V_D the voltage applied across the diode, k the Boltzmann constant, T the absolute temperature and I_S the saturation current expressed by

$$I_S = A_{\text{eff}} A^{**} T^2 \exp\left(-\frac{q\phi_B}{kT}\right)$$
(3.4)

 A_{eff} is the effective area of the diode, A^{**} is the Richardson constant and ϕ_B is the Schottky barrier height of the diode.

The presence of a Schottky barrier impedes the flow of electrons, but the impediment is not equivalent in forward and reverse bias. This type of contact is referred to as a Schottky, or rectifying, contact. The current-voltage curve for a Schottky contact is non-linear and asymmetric due to the current blocking properties of the Schottky barrier. The energy-band diagrams under equilibrium, forward and reverse bias are shown in Fig. 3.3

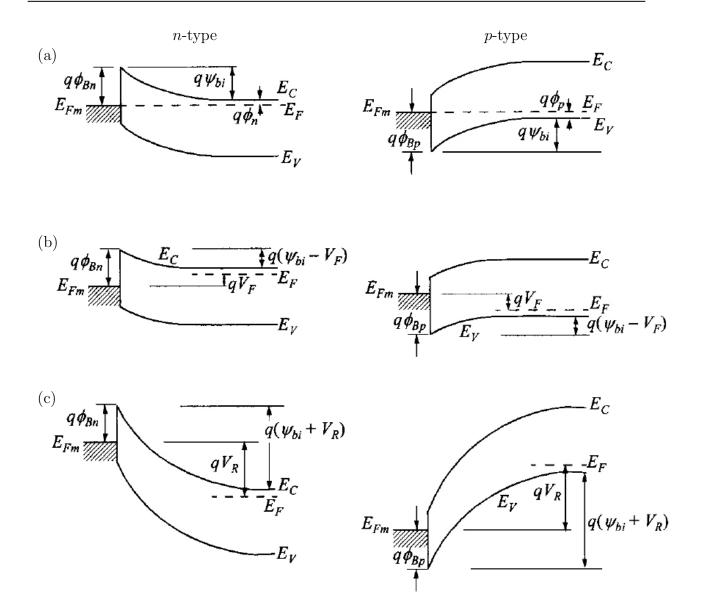


Figure 3.3: Energy-band diagrams of metal on n-type (left) and on p-type semiconductors (right) semiconductors under (a) thermal equilibrium, (b) forward bias, and (c) reverse bias [8]. Reproduced with permission from publisher.

For n-type semiconductors, in forward bias, the barrier for carriers is reduced from the semiconductor side to the metal, in reverse bias this is increased. The barrier to electron flow from the metal to the semiconductor remains almost unchanged. This allows the flow

of electrons from the semiconductor to the metal under forward bias but not the other way around. The opposite is true for p-type semiconductors.

For n-type semiconductors, according to the Schottky-Mott model, large work function metals are expected to give rise to large Schottky barriers and are more likely to form Schottky contacts.

3.2.2 Ohmic Contacts

In most devices, it is necessary to produce contacts with minimum resistance to charge injection, such that current can freely flow in either direction. These contacts are referred to as ohmic contacts, due to a constant resistance as a function of applied voltage. According to the Schottky-Mott model, the barrier height at the metal-semiconductor interface can be minimized by reducing the metal workfunction. If the metal work function is equal to or slightly smaller than that of the semiconductor, an ohmic-contact behavior results automatically. Charge accumulation (from metal to semiconductor) instead of depletion occurs with negligible voltage drop.

We define the specific contact resistivity in terms of $\Omega \cdot \text{cm}^2$. The product of R_c and the area A of the contact is called the specific contact resistance ρ_c expressed as

$$\rho_c = \left[\frac{\partial J}{\partial V}\right]_{V=0}^{-1} \tag{3.5}$$

For intermediate doping concentrations $(kT/E_{00} \gg 1)$, the Thermionic Emission mechanism dominates the current conduction and the specific contact resistance becomes

$$\rho_c = \frac{k}{qA^*T} \exp\left(\frac{q\phi_B}{kT}\right) \tag{3.6}$$

It is clearly dependent on temperature, and at higher temperatures, there is more thermionic emission current, which results in a smaller ρ_c .

3.2.3 Determination of Contact Resistivity

The most widely used method for determining the specific contact resistance is the Transmission Line Model (TLM). In this particular approach, a linear array of contacts is fabricated with various spacing between them, as shown in Fig. 3.4(a).

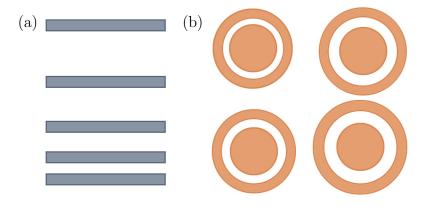


Figure 3.4: *TLM array of linear contacts with various spacing between them. (b) CTLM array of circular contacts with various contact separation.*

The total resistance is given by

$$R_T = 2R_c + l\frac{R_{sh}}{W_c} \tag{3.7}$$

where R_c is the contact resistance, R_{sh} is the sheet resistance of the semiconductor layer l the

separation and W_c is the width of the contact. By plotting R_T against l and extrapolating to the y-intercept, we can determine the contact resistance due to the metal-semiconductor contact. However, the linear TLM technique suffers from edge effects from the contacts due to current crowding. In 1982 Marlow and Das [74] proposed a new circular TLM method, where circular contacts with varying separation are used instead of rectangular contacts, as shown in Fig. 3.4(b). Since the contacts are in concentric rings, they are no longer subject to edge effects. A correction factor

$$c = \frac{R_1}{s} \ln \frac{R_1 + s}{R_1}$$
(3.8)

needs to be included or the contact resistance would be underestimated. R_1 is the radius of the inner contact and s is the separation between the inner and outer contact.



β -Ga₂O₃ Material Characterization

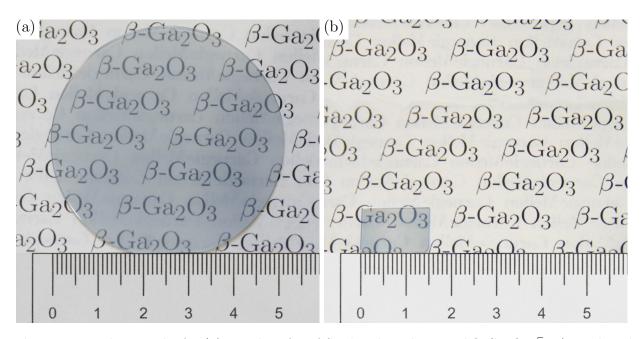


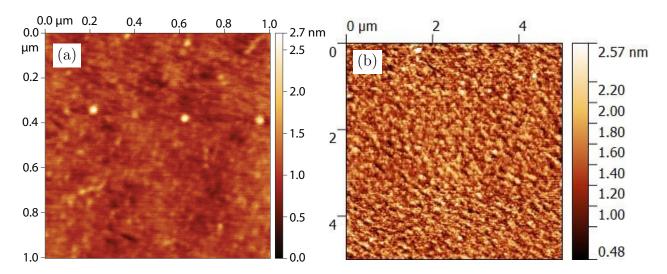
Figure 4.1: Photograph of a (a) 2-inch wafer of Sn-doped single crystal β -Ga₂O₃ (201) purchased from Tamura Corp.. Wafer is transparent in the visible with a slight bluish tint due to Sn doping and (b) an undoped epitaxial film of β -Ga₂O₃ on Sn-doped single crystal β -Ga₂O₃ (010) grown by MBE and provided by Novel Crystal Technology Inc.. Sample is also transparent in the visible with a bluish tint.

Subsequent work on electrical contacts in the next two chapters (Chapters 5 and 6) of

this thesis are all based on β -Ga₂O₃ material obtained commercially. Two-inch wafers of Sn-doped single crystal β -Ga₂O₃($\overline{2}01$) were purchased from Tamura Corp. A photograph of one of the wafers is shown in Fig. 4.1(a). The wafer is transparent in the visible with a pale bluish tint. This coloration has been attributed to infrared absorption by the free carriers [75]. Wafers are typically 680 μ m thick, with one side chemo-mechanically polished. Doping concentration of the wafers is 5–8 × 10¹⁸ cm⁻³.

A 10×15 mm homoepitaxially grown lightly-doped β -Ga₂O₃ epilayer (7.2 × 10¹⁶ cm⁻³) on heavily-doped β -Ga₂O₃ (010) (4.1 × 10¹⁸ cm⁻³) was obtained from Novel Crystal Technology Inc. in Japan. The epilayer was grown by molecular beam epitaxy (MBE), and is ~2 μ m thick.

In this chapter, a variety of techniques are used to characterize the morphological, microstructural, optical and electrical properties of the β -Ga₂O₃ samples.



4.1 Atomic Force Microscopy (AFM)

Figure 4.2: (a) $1 \times 1 \ \mu m \ AFM \ scan \ of \ a \ Tamura \ \beta - Ga_2O_3 \ wafer, \ and \ (b) \ 5 \times 5 \ \mu m \ AFM \ scan \ of \ the \ \beta - Ga_2O_3 \ homoepilayer. RMS \ roughness \ are \ 0.164 \ nm \ and \ 0.321 \ nm, \ respectively.$

AFM was performed on a NT-MDT Integra scanning probe microscope (SPM) in semicontact mode. Fig. 4.2 shows a $1 \times 1 \ \mu m$ AFM scan of a Tamura β -Ga₂O₃ wafer and a $5 \times 5 \ \mu m$ AFM scan of the β -Ga₂O₃ homoepilayer. Both samples have a relatively smooth surface morphology with a surface root-mean-square (RMS) roughness of 0.164 nm and 0.321 nm, respectively.

4.2 X-Ray Diffraction (XRD)

 θ -2 θ scans between $2\theta = 10^{\circ} - 90^{\circ}$ were performed using a Panalytical XPert Pro MPD X-Ray Diffractometer, which is fitted with a PIXcel detector for rapid scanning.

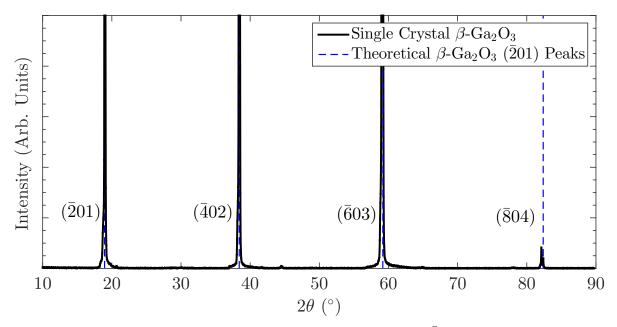


Figure 4.3: θ -2 θ XRD scan of single crystal β -Ga₂O₃ ($\overline{2}$ 01) substrate.

For the single crystal β -Ga₂O₃ substrate (Fig. 4.3), sharp peaks were observed at $2\theta = 18.949^{\circ}$, 38.420° , 59.184° and 82.375° . These peaks can be assigned to the ($\bar{2}01$), ($\bar{4}02$), ($\bar{6}03$) and ($\bar{8}04$) Bragg reflections of monoclinic β -Ga₂O₃ respectively, in agreement

with reported data of β -Ga₂O₃ of the Joint Committee on Powder Diffraction Standards (JCPDS, PDF no. 87-1901, which is calculated based on lattice parameters found by Åhman *et al.* [9]).

For homooepitaxial β -Ga₂O₃ (Fig. 4.4), a sharp peak was observed at $2\theta = 60.961^{\circ}$, which corresponds to the (020) Bragg reflection of monoclinic β -Ga₂O₃.

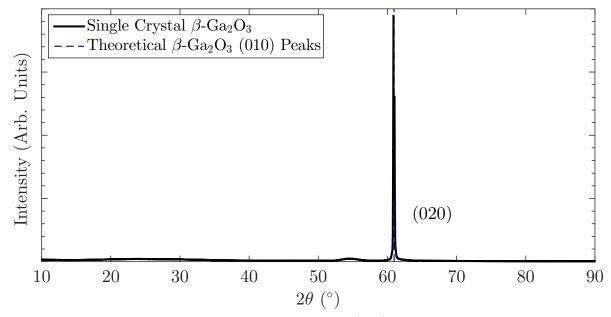


Figure 4.4: θ -2 θ XRD scan of lightly doped β -Ga₂O₃ (010) epitaxial film grown on β -Ga₂O₃ (010) substrate.

From the XRD profiles, both the bulk substrate and the epilayer are shows good crystallinity and appear to be highly oriented.

4.3 Optical Reflectivity and Transmission

Optical properties of the samples were examined using the Optronic Laboratories OL 770 Portable UV-VIS multi-channel spectroradiometer. The spectroradiometer is equipped with a xenon lamp and is capable of scanning wavelengths from 193 nm to 788 nm. Transmittance and reflectance of a Sn-doped ($\overline{2}01$) single crystal β -Ga₂O₃ wafer is shown in Fig. 4.5.

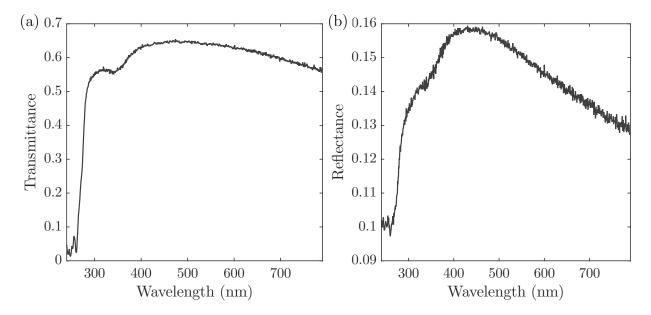


Figure 4.5: (a) Transmittance and (b) reflectance spectra of bulk single crystal Ga_2O_3 .

 β -Ga₂O₃ is shown to have strong transmittance extending into the UV range. The lower transmission of bulk β -Ga₂O₃ at higher wavelengths is due to the presence of Sn dopants in the crystal. This agrees with the observation that the wafer has a blue tint, since more blue light is transmitted. Fig. 4.6 shows a close-up of the adsorption edge of the β -Ga₂O₃ transmittance spectra. For single crystal β -Ga₂O₃, the absorption edge starts at ~261 nm, which corresponds to an approximate bandgap of 4.75 eV, very close to the theoretical value of 4.8 eV.

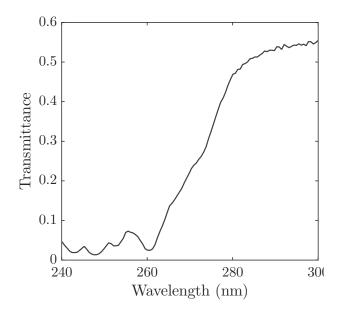


Figure 4.6: Low wavelength transmittance spectrum of bulk single crystal Ga_2O_3 near its absorption edge. The adsorption edge is found to be ~261 nm.

4.4 Electrical Conductivity

500 μ m diameter circular contacts of Ti(20 nm)/Au(100 nm) were created for electrical characterization by depositing the metal through a molybdenum shadow mask. The contacts were annealed in Ar ambient up to 800°C for one minute to achieve ohmic behavior. For details on preparing ohmic conducts, please refer to Chapter 5.

Current-voltage (*I-V*) measurements were obtained with a Signatone S-1160 probe station in tandem with an Agilent 4155 Semiconductor Parameter Analyzer. As shown in Fig. 4.7, with annealing it is possible to obtain ohmic contacts to the single crystal β -Ga₂O₃

These 500 μ m diameter circular ohmic contacts were fabricated at the four corners of a square sample of each substrate. Hall measurements based on the Van der Pauw method were performed using a MMR H-50 Hall measurement system. Ten measurements were carried out with the magnetic field B = 3500 G. For Sn-doped single crystal β -Ga₂O₃, the

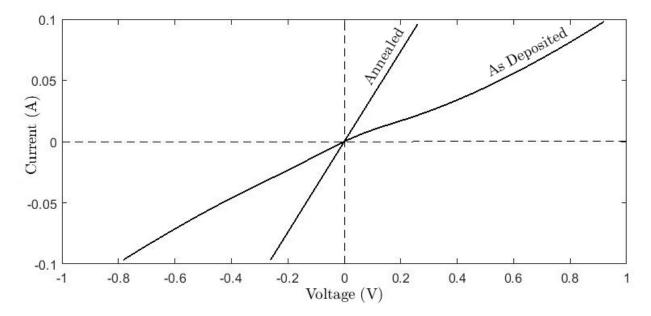


Figure 4.7: Current-voltage (I-V) plot of Ti/Au ohmic contacts on Sn-doped single crystal Ga_2O_3 .

resistivity, mobility, and carrier density was found to be $24.7 \pm 0.1 \text{ m}\Omega \cdot \text{cm}$, $49.9 \pm 8 \text{ cm}^2/\text{Vs}$, and $5.55 \pm 1.5 \times 10^{18} \text{ cm}^{-3}$, respectively. The measured carrier concentration is comparable to that provided by Tamura Corp. $(5.1 \pm 1.5 \times 10^{18} \text{ cm}^{-3})$, and the mobility is comparable with those found in the literature with the same doping concentration [49, 51].



Ohmic Contacts to β -Ga₂O₃

As discussed in Chapter 1, research on wide bandgap semiconductor devices based on β -Ga₂O₃ is in its very early stages, and there is little understanding of how to control devicerelevant interfaces to this material. For the development of useful β -Ga₂O₃-based devices, it will be critical to control the properties of electrical contacts.

There are several problems pertaining to Ga₂O₃ contacts, such as adherence, electrical instability and high contact resistance. In this chapter, Results from a systematic and comparative study to investigate various metals as possible ohmic contacts to β -Ga₂O₃ will be presented. Nine metals (Ti, In, Ag, Sn, W, Mo, Sc, Zn and Zr) were selected for this study, based on properties such as work function, melting temperature, and oxide stability. For each contact metal, its electrical behavior and morphology under various processing conditions were studied, including annealing up to 800 °C (in flowing Ar), and adding a Au capping layer. As previously discussed, methods as such ion implantation facilitates the formation of ohmic contacts, but they will not be included in this study. Select metals displayed either ohmic (Ti and In) or pseudo-ohmic (Ag, Sn and Zr) behavior under certain conditions; however, the morphology was often a problem. It was concluded that metal work function is not a dominant factor in forming an ohmic contact to β -Ga₂O₃ and that limited interfacial reactions appear to play an important role.

5.1 Sample Preparation

Prior to metal deposition, each substrate was first degreased by sequentially sonicating in acetone, isopropanol and deionized (DI) water. It was then soaked in 10% hydrochloric acid (HCl) for 5 min, rinsed in DI water, soaked in boiling hydrogen peroxide (H_2O_2) at 85 °C for 5 min, rinsed again in DI water and blown dry in nitrogen. This cleaning procedure was initially found to improve the rectifying behavior of Schottky diodes, and will be discussed more in-depth in Chapter 6. The same cleaning procedure is used in the investigation of ohmic contacts. While this may not be the most optimal treatment for the preparation of ohmic contacts, the addition of HCl and H_2O_2 treatment on top of the conventional organic clean (acetone and isopropanol) did show an improved ohmic conductivity.

As shown in Fig. 5.1, The corresponding I-V plots for Ti/Au contacts with the addition of an acid treatment were more conductive than just an organic clean in both the asdeposited form and after annealing. The effect of descumming the samples in oxygen plasma for a minute was also tested, but this step showed no noticeable effect on contact behavior and performance, and was not performed in subsequent studies.

Aside from indium bulk contacts that were made by soldering pieces of indium wire onto the substrate, each metal was deposited by electron beam evaporation (base pressure: $1-5 \times 10^{-9}$ torr) on unheated substrate from elemental sources. Since at least two contacts are necessary to set up any electrical measurement, the metal of interest were evaporated onto the Ga₂O₃ substrate with a length of wire across the substrate, the wire acts as a

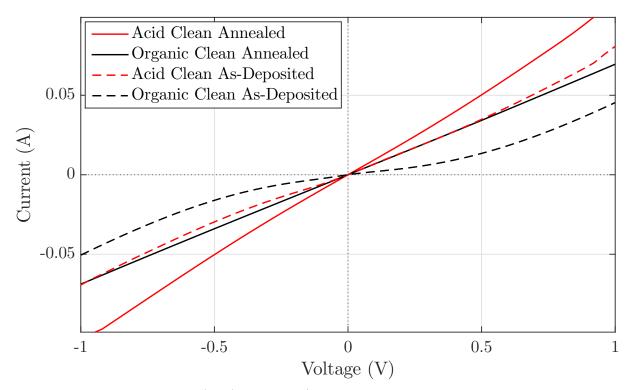


Figure 5.1: Current-voltage (I-V) plot of Ti/Au contacts on β -Ga₂O₃ having been treated either by a conventional organic clean (acetone and isopropanol), or with an additional acid treatment (HCl and H₂O₂). Both samples were then annealed for a minute at 600 °C in flowing Ar.

shadow mask, resulting in two lateral contacts separated by a gap the width of the wire mask (Fig. 5.2).

This method is sufficient for a preliminary test of ohmic behavior. However, this method was found to be limiting in two ways: One being that having only two contacts produces only one set of data for each sample. It is therefore impossible to determine how representative our results are. The second is this method produces contacts of different contact sizes, and makes it different for comparison across samples. For these reasons, in the end a setup where metals were evaporated through a shadow mask with fixed circular holes (500 μ m diameter) and fixed separation (also 500 μ m) was chosen. A schematic of this method is shown in Fig. 5.3.

CHAPTER 5. OHMIC CONTACTS TO β -GA₂O₃

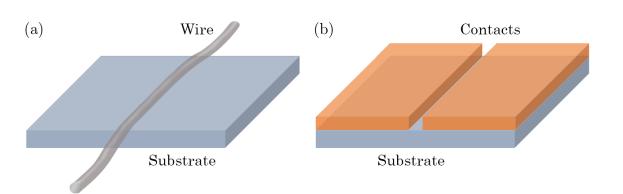


Figure 5.2: Schematic showing the process of making two planar ohmic contacts. (a) A length of wire is placed over the β -Ga₂O₃ specimen and loaded into the UHV chamber. (b) The end result is two contacts separated by a gap.

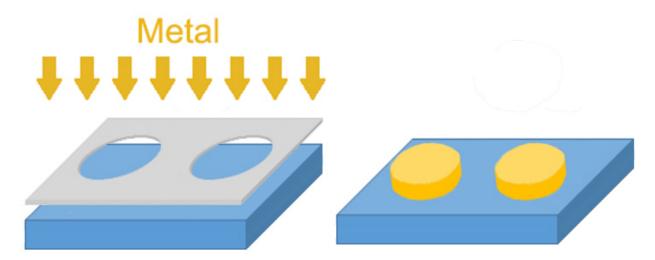


Figure 5.3: Schematic showing the process of making planar ohmic contacts. (a) A shadow mask is attached over the β -Ga₂O₃ specimen and loaded into the UHV chamber. (b) The end result are circular contacts of 500 µm diameter separated by 500 µm spacing.

The purities of the metal sources were as follows: Ti (99.995%), In (99.99%), Ag (99.99%), Sn (99.9999%), Mo (99.98%), W (99.98%), Zr (grade 702), Zn (99.99%), Sc (99.9%). Evaporation rates were kept at ≤ 1 Å/s to ensure uniform deposition. For all metals both bare (20 nm thick) and Au-coated (100 nm) metal films were deposited and characterized before and after annealing. As explained in Chapter 3, annealing is a common method that helps in the formation of ohmic contacts. The current-voltage (I-V) behavior was measured for each contact material in the as-deposited condition and after heat treatment in a quartz tube in a resistively heated furnace at 400 – 800 °C in flowing Ar.

I-V measurements were performed with two probes on adjacent contacts (spacing = 500 μ m) using an HP 4155B Semiconductor Parameter Analyzer and a Signatone S-1060H-4QR probe station. Scanning electron microscope (SEM) images were acquired using a FEI Quanta 600 FEG scanning electron microscope at 15 keV. Energy dispersive X-ray (EDX) characterizations were conducted using an Oxford INCA XMAX 80mm SDD EDX detector.

In order to better understand the chemistry at the Ti/Ga_2O_3 interface, cross-sectional samples for TEM were prepared. A schematic of the sample preparation is shown in Fig. 5.4

First, two pieces of β -Ga₂O₃ substrate measuring ~ 2 × 10 mm were cut with a diamond-coated wire saw. Using the same contact preparation techniques outlined earlier, the substrate surface was cleaned and a wire was placed over the samples and loaded into a UHV chamber. Contacts of Ti/Au (20 nm/100 nm) were deposited using electron beam evaporation at a rate of ~ 1 Å/s. After deposition, current-voltage behavior was immediately measured across the two contacts. For preparation of annealed samples, they then underwent the annealing process in a rapid thermal annealer (RTA), and current-voltage behavior was measured again.

After the contacts were fabricated, the two samples were epoxied together with the contact sides sandwiched in the middle. The stack was then inserted into a molybdenum rod with a fork at the end, which was in turn inserted into a brass tube. Oftentimes the β -Ga₂O₃ stack was too thick to be inserted directly into the Mo rod and had to be polished until it was thin enough to fit. Epoxy was applied to secure in between the parts, with careful attention to not trap any air bubbles.

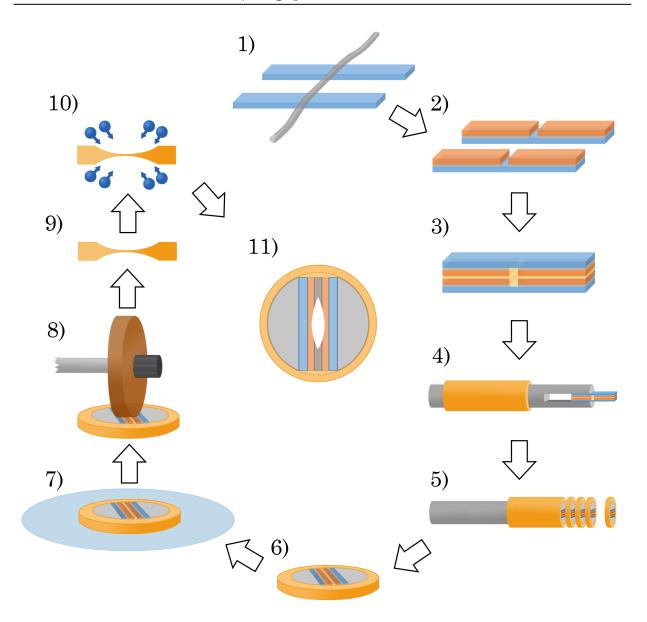


Figure 5.4: Schematic showing the process of preparing a TEM cross-sectional sample. Refer to text for description of preparation procedure.

Once the epoxy has cured, the rod was cut into slices of $\sim 200 \ \mu\text{m}$. This was further polished until it is $\sim 80 \ \mu\text{m}$ thick. It was then dimpled on a rotating dimpling wheel, which creates a 'dimple' in the middle of the slice. Finally, the sample was loaded into a precision

ion-milling system (PIPS) where it was bombarded and further thinned by Ar ions. This creates a narrow slit in the middle, where the surrounding area is thin enough for electrons to transmit through.

Transmission electron microscopy (TEM) was performed with FEI Titan G2 80-300 operating at 300 kV. Images were taken with a 2048×2048 slow-scan CCD camera.

5.2 Titanium (Ti) Ohmic Contacts

Ti/Au has been commonly reported as an ohmic contact metal for Ga_2O_3 -based devices [6, 24, 25, 76-79]. We deposited both bare Ti (20 nm) and Ti/Au (20 nm/100 nm) contacts on single crystal β -Ga₂O₃, and measured its current-voltage behavior in the as-deposited form as well as after annealing to 600 °C for 1 min in flowing Ar. As shown in Fig. 5.5, neither bare Ti nor Ti/Au were ohmic as deposited. After annealing, the bare Ti contacts not only were not ohmic, they also became more resistive. In contrast, the Ti/Au contacts demonstrated ohmic behavior after annealing.

Having established Ti/Au as a viable ohmic contact to β -Ga₂O₃, an annealing series of Ti/Au (20 nm/100 nm) contacts on both Sn-doped single crystals and an SMI undoped epilayer on sapphire were conducted, to investigate whether there is an optimal annealing temperature for each substrate. For the single crystal β -Ga₂O₃, the contacts were annealed between 300 - 1000 °C in 100 °C increments for 1 min. at each temperature. For the undoped epilayer, the contacts were annealed between 400 - 1000 °C in 200 °C increments for 1 min. at each temperature.

As shown in the I-V plots (Fig. 5.6), the optimum annealing temperature for Ti/Au contacts on the Sn-doped single crystal is ~400 °C, which produced the lowest resistivity.

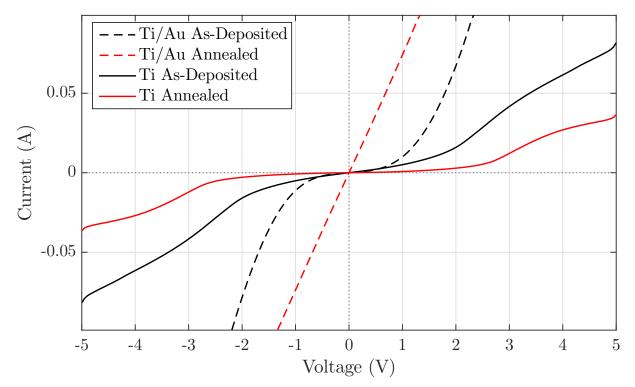


Figure 5.5: Current-voltage plot of Ti and Ti/Au contacts on single crystal β -Ga₂O₃, in asdeposited condition and after annealing at 600 °C for 1 min. in Ar ambient.

The contacts deteriorated when annealed above 500 °C. The optimum annealing temperature for Ti/Au contacts on the undoped epilayer on sapphire is \sim 800 °C. It is not surprising the doped material requires a lower annealing temperature, since doping reduces the width of the depletion region, which makes carrier injection easier.

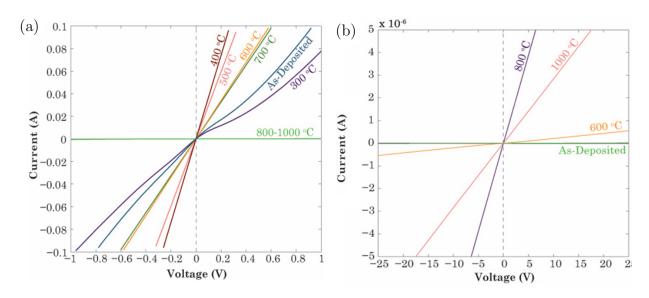


Figure 5.6: *I-V* plots of *Ti/Au* contacts to (a) single crystal Sn-doped β -Ga₂O₃ and (b) undoped β -Ga₂O₃ epilayer on sapphire, as a function of annealing temperature. The samples were annealed at each temperature for 1 min. in an Ar ambient.

SEM images of the contact surface (Fig. 5.7) shows that Ti/Ga_2O_3 contacts maintained a continuous, smooth morphology throughout the annealing series, despite possible microstructural changes. This is significant because, as we will later demonstrate, several other choice metals dewetted on the substrate and fail to completely cover the surface. As can be seen here, Ti showed complete coverage of the substrate surface and experienced no adhesion or wetting problems. This could in fact be due to the fact that titanium readily bonds to surface oxygen to form titanium oxide, and it is therefore energetically favorable for Ti to wet the surface.

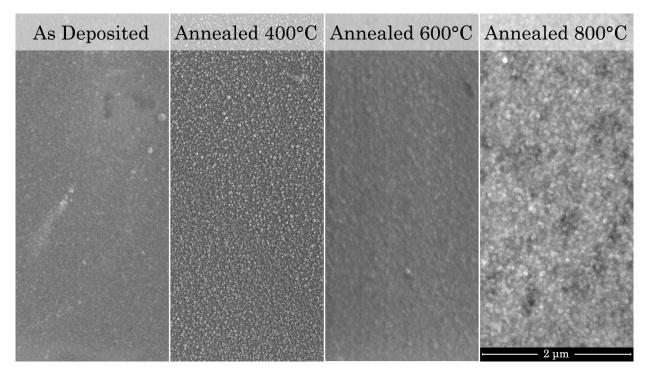


Figure 5.7: SEM image of Ti film on single crystal β -Ga₂O₃ in the (a) as-deposited state, (b) after annealing to 400 °C, (c) after annealing to 600 °C, and (d) after annealing to 800 °C.

For the Ti/Au stack, while the contact maintained a smooth continuous morphology up to an annealing temperature of 600 °C, at 800 °C we begin to observe patches where the contact appears to start dewetting from the substrate (Fig. 5.8). Interestingly, this is the temperature where the Ti/Au contact on the single crystal Ga₂O₃ becomes highly resistive. We hypothesize that this is due to interdiffusion of Au towards the Ti/Ga₂O₃ interface. In order to further study the chemistry at the metal-semiconductor interface, we prepared cross-sectional TEM samples of Ti/Au contacts on β -Ga₂O₃ single crystal substrate.

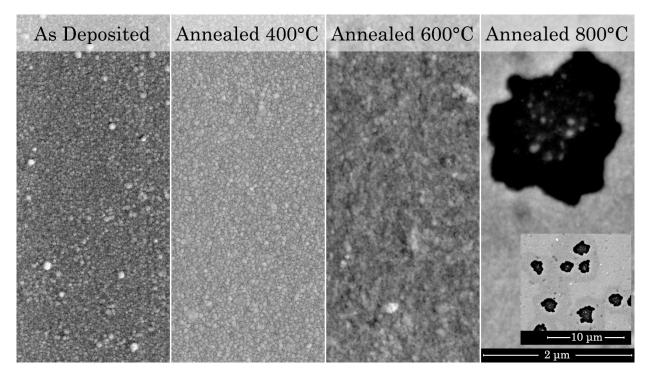


Figure 5.8: SEM image of Ti/Au film on single crystal β -Ga₂O₃ in the (a) as-deposited state, (b) after annealing to 400°C, (c) after annealing to 600°C, and after annealing to 800degc.

Two cross-sectional samples of Ti/Au (20 nm/100 nm) on β -Ga₂O₃ were prepared. One was left in the as-deposited condition, and the other is annealed at 400 °C for 1 min. in Ar. The annealed contacts were verified to be ohmic before further TEM sample preparation.

From the TEM micrographs (Fig. 5.9(a) & (c)), it can be observed that while the layer structure is visible in both images, interfaces for the as-deposited sample are sharper and more distinct, whereas in the annealed sample interdiffusion appears to have taken place, blurring and shifting the boundaries between layers. The different contrast of the layers indicate that they have different thicknesses. This is because the effect of Ar^+ ion milling is different on each layer. For example, Au is more resistant to ion-milling than Ti, is thicker and less electrons are able to transmit through it, therefore it would appear darker in the TEM image.

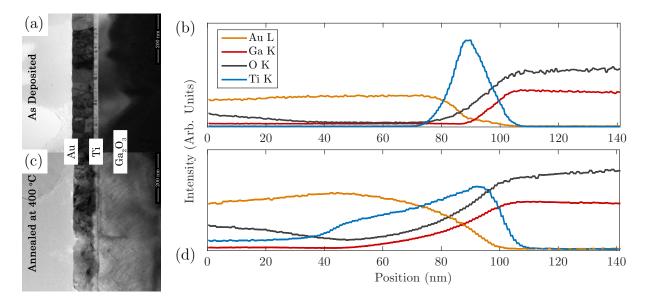


Figure 5.9: (a, c) Cross-section TEM micrographs and (b, d) EDX line profiles of Ti/Au contact on Ga_2O_3 as-deposited and after annealing at 400 °C, as indicated.

According to the TEM images, the thicknesses of the Ti and Au layers are measured to be 19.4 nm and 93.8 nm respectively in the as-deposited condition. For the sample which was annealed at 400 °C, it was hard to discern where the Ti/Au interface is, but the total thickness of the Ti/Au bilayer is measured to be 113.2 nm. These are consistent with the intended deposition thicknesses of 20 nm (Ti) and 100 nm (Au).

The blurring of the interfacial boundaries for the annealed sample is confirmed to be a result of chemical interdiffusion from the EDX line profile as shown in Fig. 5.9(b) & (d). There was not significant interdiffusion observed for the as-deposited sample. The line profiles measure the elemental composition of the sample from the Au surface into the Ga_2O_3 substrate. In particular, we see how Ti was concentrated to a depth of 80-100 nm in the as-deposited form, it had significantly diffused towards the surface after annealing. Similar diffusion of Au deeper into the sample is also observed, though not as dramatic as Ti. Furthermore, both Ga and O were observed in higher concentration towards the sample surface. While we have not confirmed what phases have formed as a result of this interdiffusion, it is believed that formation of oxides of Ti, which are thermodynamically favorable, are at least partially responsible for our observations.

Therefore, although Ti readily forms an ohmic contact to Ga_2O_3 , Ti is not thermodynamically stable with Ga_2O_3 . The free energies of formation of Ti oxides are more negative than that of Ga_2O_3 , which indicates that Ti will reduce Ga_2O_3 , possibly forming an insulating oxide layer at the interface, if given sufficient time at elevated temperatures. This phenomenon likely causes the degraded electrical behavior for annealing temperatures > 500 °C. It therefore would be desirable to have alternative choices for metals to use as ohmic contacts.

5.3 Comparison of Different Metals for Ohmic Contacts

5.3.1 Metal Selection

Two primary considerations went into our choice of ohmic contact metal. First, as discussed in Chapter 3, in order to achieve ohmic behavior, the work function ϕ_M of the metal chosen should be lower or comparable to the electron affinity χ_s of β -Ga₂O₃. The only reported value of the electron affinity for β -Ga₂O₃ in the literature is 3.7 eV [80], measured using ultraviolet photoelectron spectroscopy (UPS) of Ga₂O₃ formed through oxygen ion bombardment of GaN(1000). It is unclear how reliable this value is. Since Ti has a workfunction of 4.33 eV, and became ohmic after annealing treatment, we decided to consider metals with workfunctions up to 4.6 eV. Table 5.1 shows the workfunction of our selected metals.

Element ϕ (eV) Element ϕ (eV) Element ϕ (eV) 3.5 Sc 4.26Sn 4.42 Ag \mathbf{Zr} 4.05Zn 4.3W 4.55Τi In 4.14.33Mo 4.6

Table 5.1: Work functions ϕ_M of selected metals [11].

Our second consideration is based on the stability of the metal in contact with the oxide substrate. Ideally, its oxide should be thermodynamically less stable than Ga_2O_3 , so as not to reduce the substrate and form a metal oxide, which we conjecture will degrade the contact's performance over time. The stability of metal oxides can be represented on an Ellingham diagram, which plots the free energy of formation against temperature. An Ellingham diagram with oxides of selected metals is shown in Fig. 5.10.

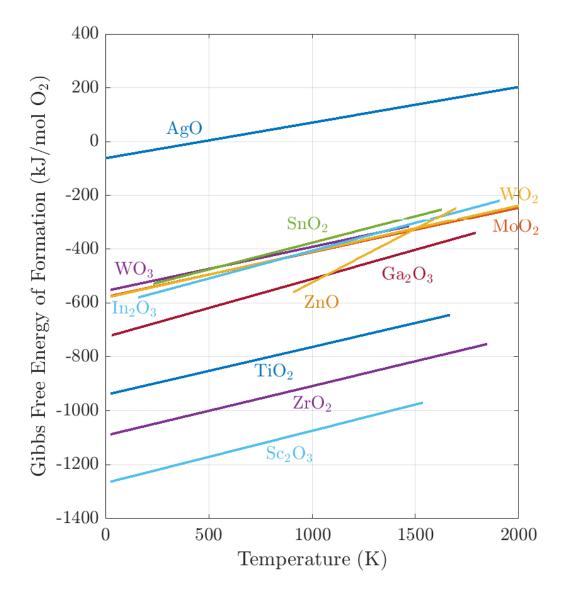


Figure 5.10: Ellingham diagram of oxides of select metals compared to gallium oxide.

5.3.2 Indium (In)

Indium was an initial metal of interest, as it has a relatively low Φ_M (4.1 eV) and, based on thermodynamics, should not reduce gallium oxide. As shown in Fig. 5.11(b), A thin film of In metal became ohmic after annealing at 600 °C for 1 min, which agrees with results of another study [81].

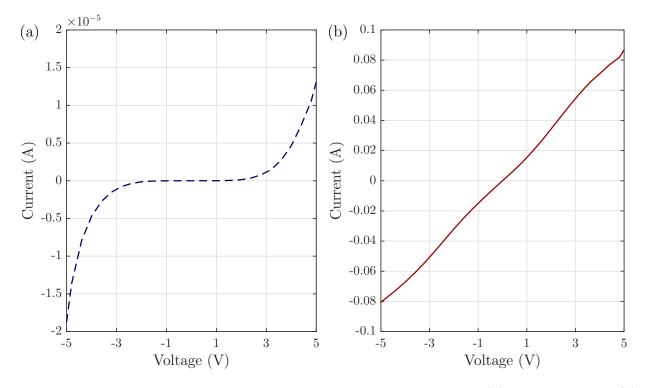


Figure 5.11: Current-voltage plots of In film on single crystal β -Ga₂O₃ (a) as-deposited and (b) annealed at 800 °C.

Although the contacts showed ohmic or ohmic-like behavior after annealing, the I-V behavior was not very stable, bending at several points along the line.

This can be be better understood by looking at the SEM images of the sample surface, as shown in Fig. 5.12.

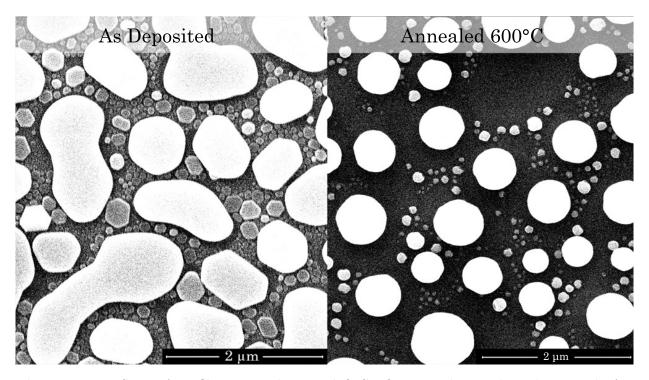


Figure 5.12: SEM of In film on single crystal β -Ga₂O₃ in as-deposited condition and after annealing at 600°C as indicated.

Even as-deposited, the indium dewetted on the surface. After the annealing process, they further melted and coalesced together. As such, the In does not form a continuous conductive film. This is likely due to low bonding affinity between indium and oxygen. The use of an Au capping layer on the In contacts did not result in improved morphology, either with or without annealing.

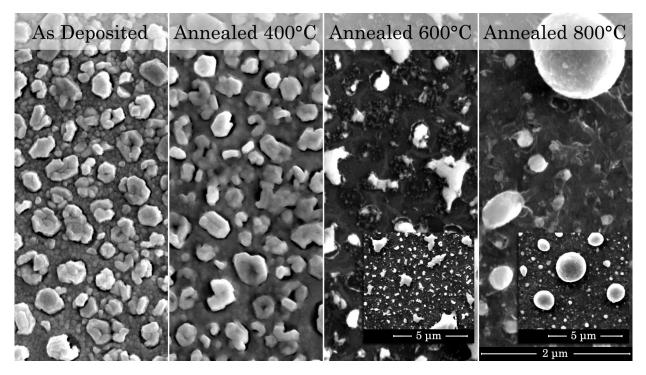


Figure 5.13: SEM of In/Au film on single crystal β -Ga₂O₃ in as-deposited condition and after annealing at temperatures indicated.

Interestingly, in contrast with thin film In, bulk indium yields stable and reliable ohmic contacts. Fig. 5.14 shows representative I-V plots of bulk In contacts before and after annealing at 800°C for 1 min. in Ar.

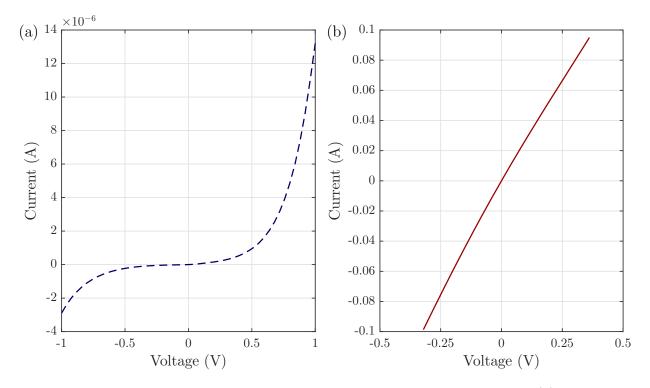


Figure 5.14: Current-voltage plot of bulk In contacts on single crystal β -Ga₂O₃ (a) as-deposited and (b) annealed at 800 °C.

Due to the thickness of the bulk In, the contact remained continuous even after annealing. This shows that In upon annealing does form ohmic contact to β -Ga₂O₃. However, bulk In is not a practical contact material because it is difficult to control its size and position. Moreover, In's low melting point also makes it a less desirable contact material.

5.3.3 Tin (Sn)

Next we investigated tin as a potential ohmic contact. Sn is chosen because it is an ntype dopant in Ga_2O_3 and yields transparent conducting films with conductivity up to at least 38 Ω^{-1} cm⁻¹ [82]. Although Sn and Sn/Au formed pseudo-ohmic contacts in the as-deposited condition, the electrical behavior degraded upon annealing.

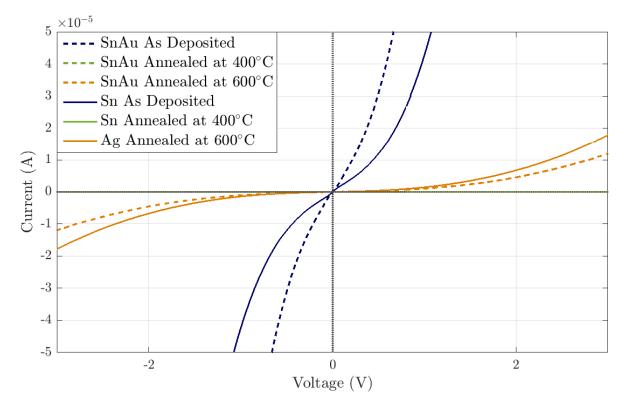


Figure 5.15: Current-voltage plot of Sn film on single crystal β -Ga₂O₃, as a function of annealing temperature. The anneals were conducted for 1 min. each in Ar.

Sn contacts were found to dewet Ga_2O_3 , both in the as-deposited condition and after annealing, having a morphology that appeared similar to the In contacts. Dewetting became increasingly pronounced with increasing annealing temperature, which explains the degradation in electrical behavior.

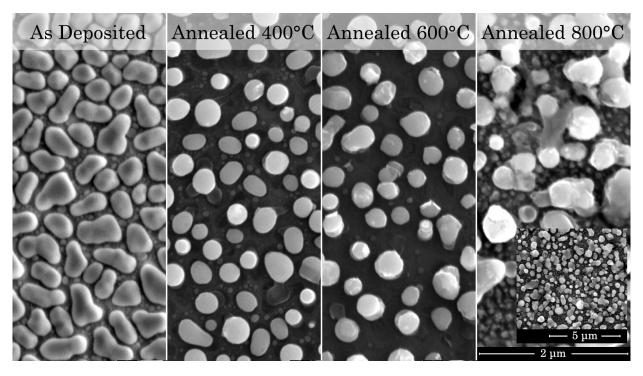


Figure 5.16: SEM of Sn film on single crystal β -Ga₂O₃ in as-deposited condition and after annealing at temperatures indicated.

CHAPTER 5. OHMIC CONTACTS TO β -GA₂O₃

A Au capping layer not only did not improve the morphology or electrical characteristics, in fact, the Au and Sn appear to have formed an intermetallic which coalesced into larger droplets on the substrate surface.

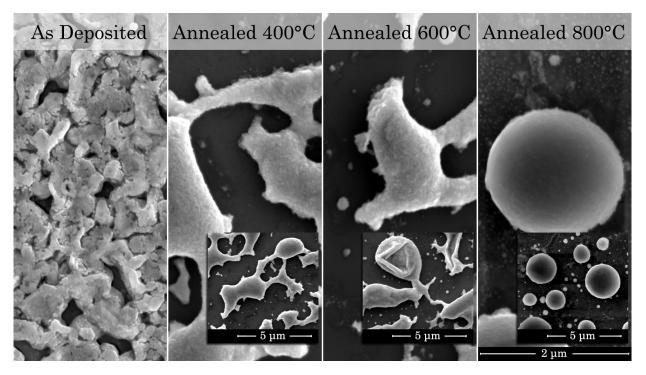


Figure 5.17: SEM of SnAu film on single crystal β -Ga₂O₃ in as-deposited condition and after annealing at temperatures indicated.

5.3.4 Silver (Ag)

Ag has a low work function and does not oxidize easily. In principle it could be a very good candidate as an ohmic contact to β -Ga₂O₃. Electrically, a bare Ag contact degraded with annealing. The addition of a Au capping layer prevented this degradation and showed better overall conductivity and nearly ohmic behavior in the as-deposited form as well as after annealing to 600 °C. At 800 °C, it degraded and became resistive. The contacts unfortunately did not became fully ohmic at any annealing temperature.

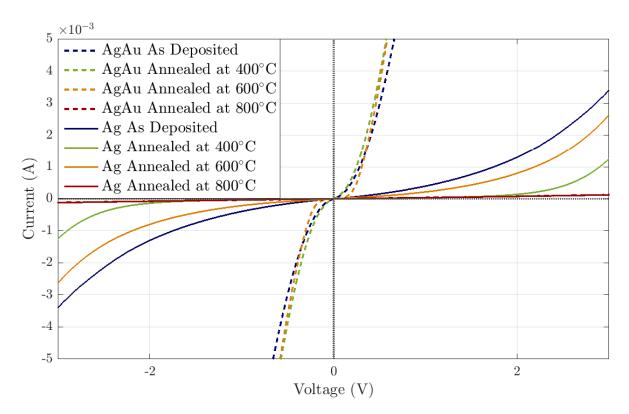


Figure 5.18: Current-voltage plot of Ag film on single crystal β -Ga₂O₃. The anneals were conducted for 1 min. each in Ar.

CHAPTER 5. OHMIC CONTACTS TO β -GA₂O₃

SEM images shows that Ag suffers from same dewetting problem that affected In and Sn contacts. Interestingly, Ag appeared to form continuous films in the as-deposited condition, and only began dewetting the Ga_2O_3 surface upon annealing, as shown in Fig. 5.19.

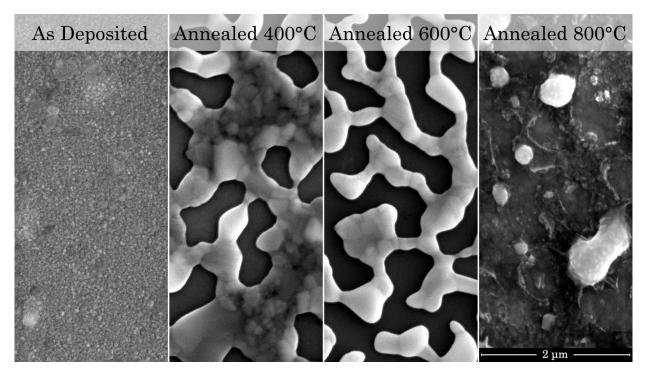


Figure 5.19: SEM image of Ag film on single crystal β -Ga₂O₃ after annealing at temperatures indicated.

On the other hand, Ag/Au appeared continuous up to 600 °C, the grain sizes are observed to increase with annealing temperature. At 800 °C, the metal film finally dewetted, thus resulting in the loss of conductivity as we observed from the I-V plots.

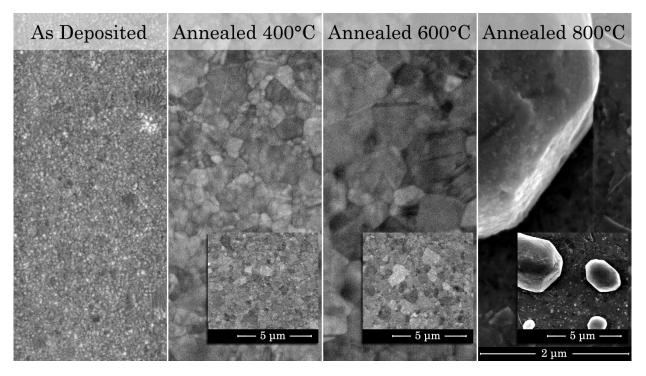


Figure 5.20: SEM image of Ag/Au film on single crystal β -Ga₂O₃ after annealing at temperatures indicated.

5.3.5 Zirconium (Zr)

The next metal under consideration was zirconium. Zr sits directly below Ti in the periodic table. With its low Φ_M (4.05 eV) and chemical similarity to Ti, we expected Zr to readily form an ohmic contact to Ga₂O₃. As in the case of Ti, the Zr contacts also displayed improved electrical behavior with lower resistivity with a Au capping layer. In fact, in the as-deposited condition, Zr/Au contacts displayed pseudo-ohmic behavior. However, the contacts continually degraded with increasing anneal temperature, unlike Ti/Au contacts, which showed improved ohmic behavior with moderate (400 – 500 °C) annealing.

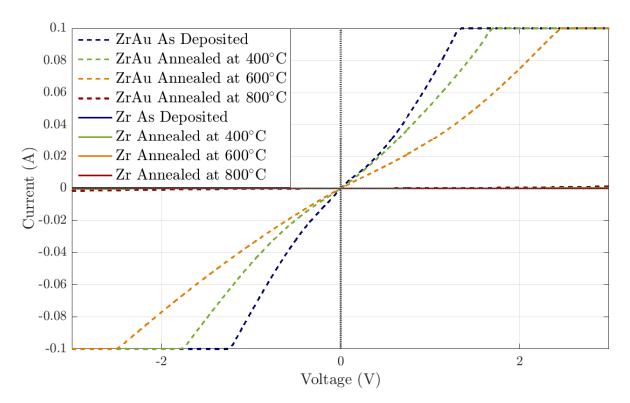


Figure 5.21: Current-voltage plot of Zr film on single crystal β -Ga₂O₃. The anneals were conducted for 1 min. each in Ar. Zr I-V were very resistive and overlap near the x-axis.

We attribute the cause of this degradation to reduction of the Ga_2O_3 by Zr to form

free Ga plus Zr oxide. The free energy of formation for ZrO_2 ($\Delta G_f = -1019 \text{ kJ/mol}$ at 400 °C) is more negative than either Ga₂O₃ ($\Delta G_f = 640 \text{ kJ/mol}$ at 400 °C) or TiO₂ ($\Delta G_f = 870 \text{ kJ/mol}$ at 400 °C), indicating a stronger driving force for Zr oxide formation.

Our SEM and EDX analyses (Fig. 5.22 and 5.23) of (bare) Zr/Ga_2O_3 contacts support this conclusion. Specifically, the SEM images show that a continuous, smooth morphology in the as-deposited condition became rough after the 400 °C anneal, and showed evidence for a melted and resolidified phase after the 600 °C anneal.

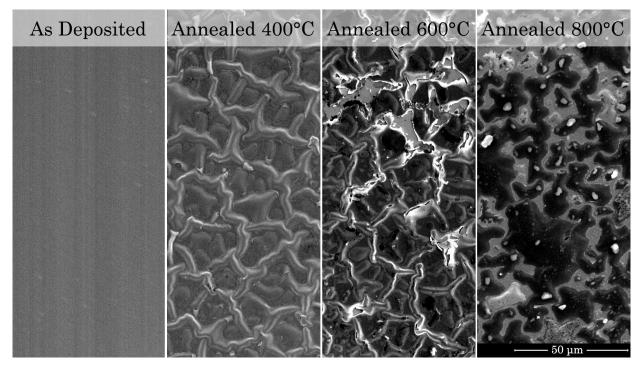


Figure 5.22: SEM of Zr film on single crystal β -Ga₂O₃ in as-deposited condition and after annealing at temperatures indicated.

EDX elemental profiles along a randomly selected line from the 600 °C annealed sample show fluctuations in intensities for Ga, Zr, and O. Also, the peak intensities for Zr and O are approximately co-located and are offset from the locations for the Ga peak intensities. This result indicates that reduction of Ga_2O_3 by Zr to form Ga and Zr oxide occurred during the annealing process.

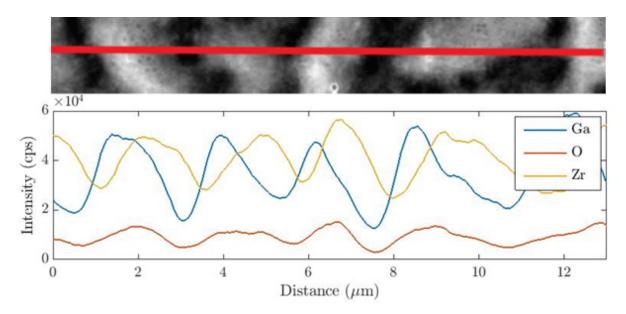


Figure 5.23: EDX intensity profiles of Ga, Zr, and O acquired along the red line across the Zr/Ga_2O_3 topography after annealing at 600 °C.

5.3.6 Tungsten (W)

W, with a moderate work function of 4.55 eV, was also investigated to extend the range of work functions included. W has a very high melting point and less stable oxides than Ga_2O_3 . Neither W nor W/Au contacts displayed ohmic behavior before or after annealing to 800 °C.

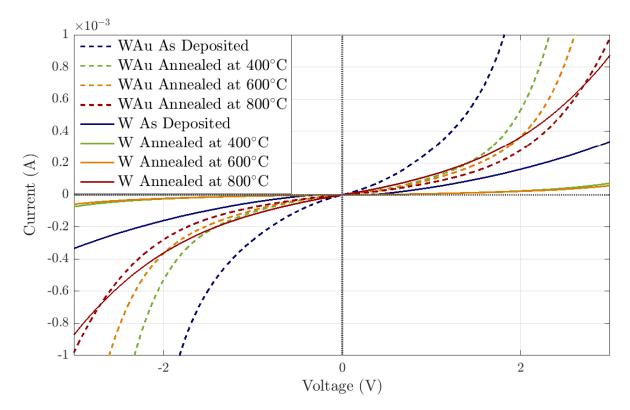


Figure 5.24: Current-voltage plot of W film on single crystal β -Ga₂O₃, as a function of annealing temperature. The anneals were conducted for 1 min. each in Ar.

In general, W/Au demonstrated better conductivity than bare W. However, after annealing to 800 °C, bare W did demonstrate a slight improvement in conductivity. It might be interesting to anneal bare W contacts to a higher temperature. The morphologies of W contacts remained continuous throughout the annealing series. SEM images of W contact at each annealing temperature are shown in Fig. 5.25. The W film appears to have good adhesion to the substrate surface.

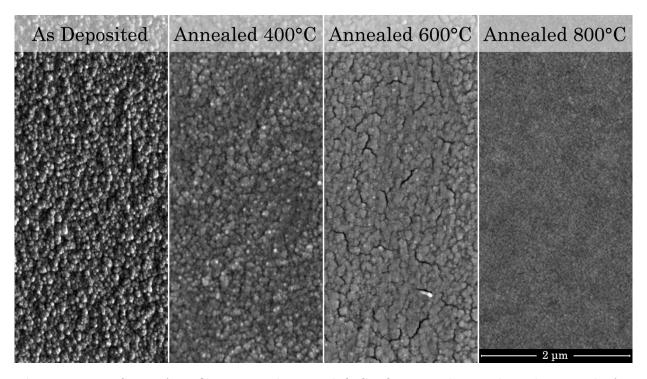


Figure 5.25: SEM of W film on single crystal β -Ga₂O₃ in as-deposited condition and after annealing at temperatures indicated.

5.3.7 Molybdenum (Mo)

Mo was chosen together with W as a moderate work function metal (with a work function of 4.6 eV). Like W, Mo has a high melting point and has less stable oxides than Ga_2O_3 . As expected, Mo showed very similar behavior to W: neither Mo nor Mo/Au contacts displayed ohmic behavior before or after annealing to 800 °C. Unlike W however, the addition of the Au capping layer did not improve conductivity but actually decreased it for unknown reasons.

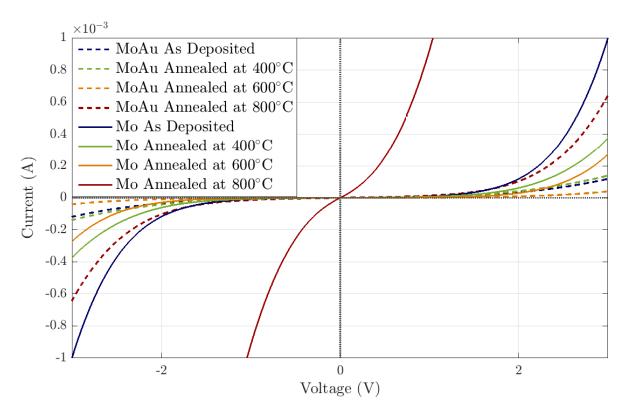


Figure 5.26: Current-voltage plot of Mo film on single crystal β -Ga₂O₃, as a function of annealing temperature. The anneals were conducted for 1 min. each in Ar.

Interestingly, bare Mo annealed to 800 °C demonstrated a drastic improvement in electrical conductivity. While it is not ohmic yet, it might be worth investigating annealing

these contacts to a higher temperature.

Like W, the morphologies of Mo contacts remained continuous throughout the annealing series. SEM images of Mo contacts at each annealing temperature are shown in Fig. 5.27. The Mo film appears to have good adhesion to the substrate surface, and showed no distinguishable change after annealing.

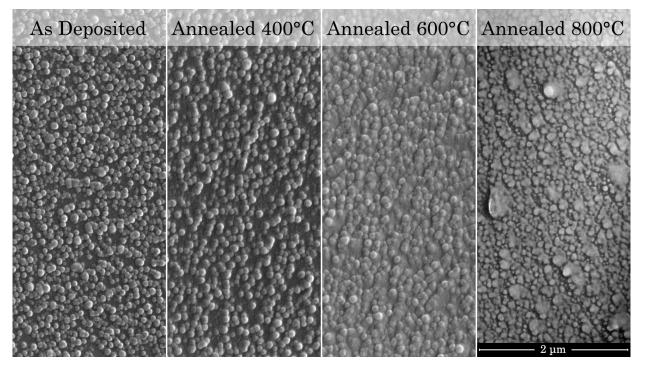


Figure 5.27: SEM of Mo film on single crystal β -Ga₂O₃ in as-deposited condition and after annealing at temperatures indicated.

5.3.8 Zinc (Zn)

Although Zn has a work function that is nearly the same as that of Ti (4.3 eV), Zn and Zn/Au contacts did not show ohmic behavior under any conditions, with Zn/Au being slightly more conductive in general. They also became increasingly resistive with increasing annealing temperature.

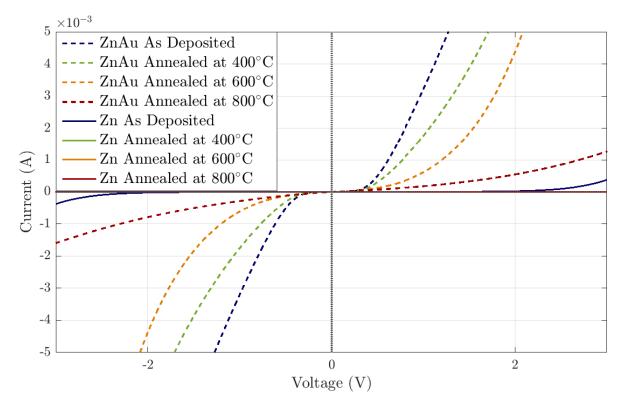


Figure 5.28: Current-voltage plot of Zn film on single crystal β -Ga₂O₃, as a function of annealing temperature. The anneals were conducted for 1 min. each in Ar.

CHAPTER 5. OHMIC CONTACTS TO β -GA₂O₃

While the morphology of Zn contacts remained continuous throughout the annealing series (Fig. 5.29), there are noticeable microstructural changes on the contact surface. Well-defined grains are visible in the as-deposited form, but these became increasingly obscured with increasing annealing temperature, possibly due to surface oxidation.

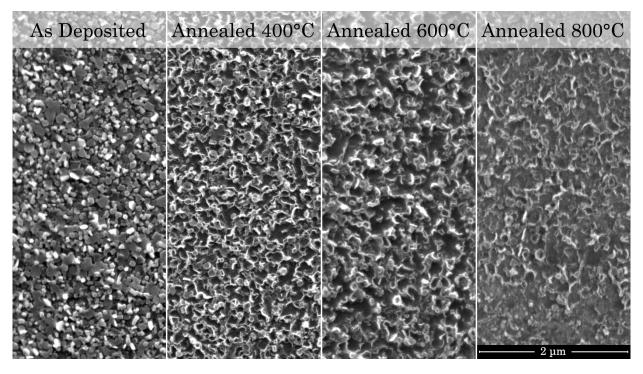


Figure 5.29: SEM of Zn film on single crystal β -Ga₂O₃ in as-deposited condition and after annealing at temperatures indicated.

5.3.9 Scandium (Sc)

Sc was chosen as a metal of interest because it has one of the lowest work functions of all metals ($\Phi_M = 3.5 \text{ eV}$). It is a very reactive metal and would presuably oxidize readily. Although the addition of a Au capping layer did yield contacts with lower resistivity, Sc/Au contacts also did not display ohmic behavior before or after annealing to 800 °C.

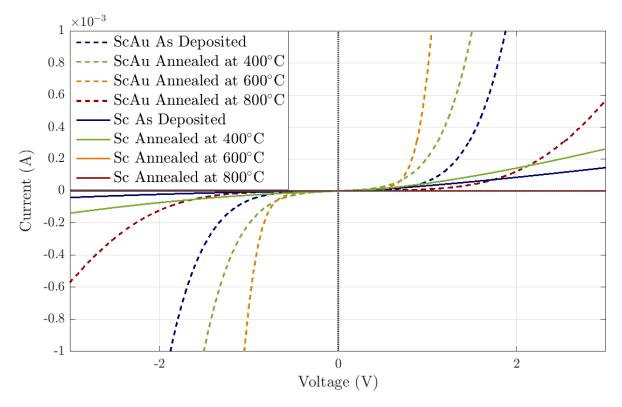


Figure 5.30: Current-voltage plot of Sc film on single crystal β -Ga₂O₃, as a function of annealing temperature. The anneals were conducted for 1 min. each in Ar.

CHAPTER 5. OHMIC CONTACTS TO β -GA₂O₃

The morphology of Zn contacts remained continuous throughout the annealing series (Fig. 5.31). Like Zn, well-defined grains in the as-deposited form became increasingly obscured with increasing annealing temperature, possibly due to surface oxidation.

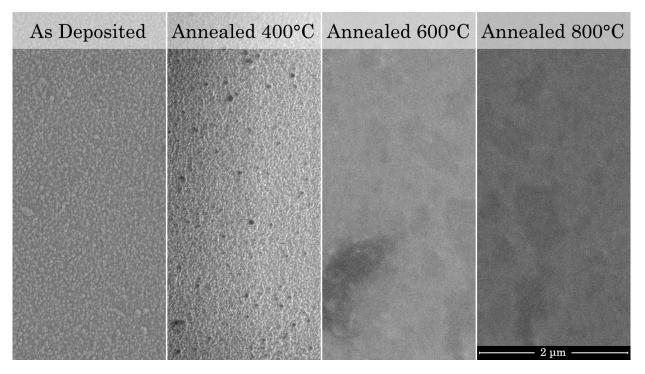


Figure 5.31: SEM of Sc film on single crystal β -Ga₂O₃ in as-deposited condition and after annealing at temperatures indicated.

5.4 Summary

The results of the metal contacts discussed above are summarized in Table 5.2.

In addition, SEM images of metal/Au capping layer were taken for Zr, W, Mo, Zn and

Sc. Refer to Appendix **??** for these SEM images.

Metals	ϕ^{M}	Melting	Stability of	Possible	Morphology Ohmic?	Ohmic?	Conditions,
	(eV) [83]	point (°C)	$\begin{array}{c} \begin{array}{c} \begin{array}{c} \begin{array}{c} \begin{array}{c} \begin{array}{c} \end{array} \end{array} \end{array} \\ \begin{array}{c} \begin{array}{c} \end{array} \end{array} \end{array} \left(\begin{array}{c} \end{array} \right) \left(\begin{array}{c} \end{array} \right) \left(\begin{array}{c} \end{array} \right) \left(\begin{array}{c} \end{array} \right) \end{array} \\ \begin{array}{c} \end{array} \end{array} \left(\begin{array}{c} \end{array} \right) \left(\begin{array}{c} \end{array} \right) \left(\begin{array}{c} \end{array} \right) \left(\end{array} \right) \left(\begin{array}{c} \end{array} \right) \left(\bigg) \left(\end{array} \right) \left(\bigg) \left(\end{array} \right) \left(\bigg) \left$	intermetallic phases [84]			if ohmic/ pseudo- ohmic
$\mathbf{S}_{\mathbf{C}}$	3.5	1541	More Stable	Ga ₃ Sc, Ga ₂ Sc, GaSc, Ga ₄ Sc ₅ , Ga ₃ Sc5	Continuous, smooth	No	
Zr	4.05	1855	More stable	Ga ₃ Zr, Ga ₂ Zr, Ga ₅ Zr ₃ , Ga ₃ Zr ₂ , Ga ₂ Zr ₃ , Ga ₃ Zr ₅ , GaZr ₂	Continuous, smooth as-dep./ rough after anneal	Pseudo	As- deposited only (Zr/Au)
In	4.1	157	Less stable		Dewets	Yes	600 °C, 1 min. anneal
Ag	4.26	962	Less stable		Continuous rough (platelike)	No	As- deposited
Zn	4.3	420	Comparable, depends on temp.		Continuous rough (platelike)	No	
Ti	4.33	1668	More stable	Ti ₃ Ga, Ti ₂ Ga, Ti ₅ Ga ₃ , Ti ₅ Ga ₄ , TiGa, Ti ₂ Ga ₃ , Ti ₃ Ga ₅ TiGa ₂ , TiGa ₃	Continuous, smooth	Yes	400–500 °C, 1 min. anneal
Sn	4.42	232	Less stable		Dewets	Pseudo	As- deposited
Μ	4.55	3422	Less stable		Continuous	N_{O}	
Mo	4.6	2623	Less stable	Mo ₃ Ga, MoGa, MoGa ₂ , Mo6Ga31, Mo ₈ Ga ₄ 1	Continuous	No	

83

5.4. SUMMARY

This study indicates that there are a number of complex factors involved in forming ohmic contacts to n-type (Sn-doped) β -Ga₂O₃ substrates. The results of this study specifically pertain to ($\bar{2}01$)-oriented β -Ga₂O₃ substrates treated with HCl and H₂O₂ prior to metal deposition by electron beam evaporation.

Firstly, it is apparent that metal work function is not a dominant factor in determining whether a metal forms an ohmic contact to these substrates. In particular, Sc, which has a very low Φ_M , did not form an ohmic contact even with a Au capping layer, which should help prevent oxidation of the underlying Sc metal layer.

Secondly, there is evidence that interfacial reaction – in limited amount – plays an important role in forming ohmic contacts to β -Ga₂O₃. Titanium, which has a more stable oxide than Ga₂O₃, reacts with the Ga₂O₃ substrate to form ohmic contacts after annealing at 400 °C. However, too much reaction, as observed for Zr/Au contacts annealed at $T \ge 400$ °C and Ti/Au contacts annealed at T > 500 °C, resulted in contact degradation characterized by an increase in resistance.

This study also shows that morphology is a problem for many metals on Ga_2O_3 . This is not too surprising, considering that oxides tend to have low surface energies. However, interestingly, the metals with the lowest surface energies (In, Ag, and Sn) dewetted the surface, which is opposite to what would be expected. Therefore, the strength of interfacial bonding between the metal and Ga_2O_3 likely determines whether the metal forms a continuous film. We note that the metals that have significant chemical affinity (i.e., higher oxide stability and/or possibility for intermetallic compounds) with Ga_2O_3 formed continuous films, whereas, the metals that have low chemical affinity (i.e., lower oxide stability and no intermetallic compounds) with Ga_2O_3 were observed to dewet the surface. The only exception to this trend is W, which formed a continuous film even though it does not appear to have significant chemical affinity for Ga_2O_3 . One possible reason for this behavior may be a low atom mobility, associated with W's very high melting point.

In conclusion, we found that the formation of ohmic contacts to Ga_2O_3 is not determined by any one dominant factor, such as metal work function, and that limited interfacial reactions appear to play an important role in both the morphology and electrical behavior. Further work is needed to investigate optimized metals stacks and processing conditions for thermally stable ohmic contacts having low contact resistance and suitable morphology.

Chapter 6

Schottky Contacts to β -Ga₂O₃

As we first stated in the introduction (Chapter 1), the high breakdown electric field of β -Ga₂O₃ is of great interest for high power electronics. Schottky contacts are a key component of many high power devices, such as metal-semiconductor field-effect transistors (MESFETs) and high-voltage rectifiers. While excellent rectifying characteristics have been reported for β -Ga₂O₃ Schottky diodes by many groups, there has not yet been a systematic study into the preparation of Schottky contacts to β -Ga₂O₃. Greater understanding of the metal-semiconductor interface is necessary in order to obtain reproducible rectifiers.

As discussed in chapter 3, while the same factors affecting the behavior of ohmic contacts (i.e. crystal quality, defects, interface states, etc.) also affect Schottky contacts, the results are oftentimes more complex. For an ohmic contact, once it displays a linear current-voltage relationship, the only metric for evaluating its 'goodness' is its contact resistance. On the other hand, when one talks about a 'good' Schottky contact, one can really be referring to one of these three different ideas:

1. An ideal Schottky contact in the sense that its transport mechanism perfectly obeys the thermionic emission relation. In other words, a diode with an ideality factor of unity.

- 2. A perfect metal-semiconductor interface, free of any interface layers or interfacial states. In other words, a contact that obeys the Schottky-Mott relation.
- 3. A Schottky contact that produces desirable diode properties such as low series resistance and high breakdown voltage.

The three are often related, for example, deviation from ideality can be caused by electrically active defects, which can provide pathways for electron tunneling and therefore increase reverse leakage currents; or reactions at the metal-semiconductor interface that can cause band bending and change the effective barrier height, while at the same time change the transport mechanism across the interface, sometimes from rectifying to ohmic.

The details of nonideal Schottky contact behavior will be discussed later in the chapter. For the moment it suffices to reiterate that the electrical behavior of any metal contact to a semiconductor is heavily influenced by the metal-semiconductor interface. As such, the barrier height of a Schottky diode is very sensitive to pre-deposition surface preparation and post-deposition treatments of the Schottky metal/semiconductor contact interface. So we begin a systematic investigation of Schottky contact behavior to Ga_2O_3 with a study of surface treatment. We later study the Schottky contact behavior of five Schottky metals (W, Cu, Ni, Ir, and Pt), and discuss their Schottky barrier heights in consideration of the Schottky-Mott relation:

$$\Phi_B = \Phi_M - \chi_S \tag{6.1}$$

Substrate		Classian		Schottky Barrier		
Growth (Doping Conc. (cm^{-3}))	Orientation	Cleaning Method	Metal	$\begin{array}{ccc} & \text{Barrier} & \text{Height} \\ \phi_B \ (\text{eV}) \end{array}$	Ideality Factor n	
FZ single crystal $(10^{17}-10^{18})[85]$	(100)	Solvent, BOE	Au	$1.2 - \sim 1.1 \ (I-V)$	1.8–1.1	
CZ single crystal (UID $0.6-8 \times 10^{17})[86]$	(100)	N. R.	Au	0.98 (I-V) $1.01 \pm 0.05 (PES)$	1.09	
CZ single crystal (UID $0.6-8 \times 10^{17})[87]$	(100)	Cleaved surface	Ni	$\begin{array}{c} 0.97 \ (I\text{-}V) \\ 1.22 \ (C\text{-}V) \end{array}$	_	
FZ single crystal (UID $3-5 \times 10^{16})[88]$	(010)	Solvent, HF, Piranha	Pt	1.46 - 1.47 (I-V) 1.52 (C-V)	1.04 - 1.06	
EFG single crystal (UID $1.1 \times 10^{17})[89]$	(010)	$ BCl_3 \text{ for} \\ ohmic \\ contacts $	Ni	$\begin{array}{l} 1.49 \ (I-V) \\ 1.55 \ (C-V) \\ 1.55 \ (IPE) \end{array}$	1.04	
$2\mu m$ undoped homoepi on <i>n</i> -type $(\sim 4.1 \times 10^{18})[90]$	(010)	N. R.	Ni	$0.95 - 1.01 \; (I-V)$	3.38-1.21	
EFG single crystal $(2.8 \times 10^{17})[91]$	$(\bar{2}01)$	Solvent, piranha, BOE	Ni	$\begin{array}{c} 1.08 \ (I-V) \\ 1.12 \ (C-V) \end{array}$	1.19	
EFG single crystal $(\sim 1.7 \times 10^{17})[92]$	$(\bar{2}01)$	N. R.	Ni	1.05 $(I-V)$ 1.2 $(C-V)$	_	
EFG single crystal (UID $1.2 \times 10^{17})[93]$	$(\bar{2}01)$	N. R.	Ni	1.25 $(I-V)$ 1.18 $(C-V)$	1.01	
EFG single crystal $(3.9 \times 10^{18})[94]$	$(\bar{2}01)$	N. R.	Ni	$\begin{array}{c} 0.9 \ (I-V) \\ 1.49 \ (C-V) \end{array}$	1.4	
PLD epi/ZnO/sapphire $(1.6 \times 10^{18})[95]$	$(\bar{2}01)$	N. R.	Cu	0.92 (<i>I-V</i>)	1.22	
HVPE epi/sapphire $(1.2 \times 10^{16})[96]$	(001)	Solvent & acid. BCl_3 for ohmic contacts	Pt	$\begin{array}{c} 1.15 \ (I-V) \\ 1.12 \ (CV) \end{array}$	~1	

Table 6.1: Summary of Schottky contacts to Ga_2O_3 reported in the literature. Abbreviations used are defined below the table. FZ = floating zone growth, CZ = Czochralski method growth, EFG = edge-defined film-fed growth, PES = photoemission spectroscopy, IPE = internal photoemission, HVPE = halide vapor phase epitaxy.

Table 6.1 summarizes the current literature on Schottky barrier diodes based on β -Ga₂O₃. Only a few studies reported the surface cleaning methods used for Schottky contacts to Ga₂O₃. The ones that are reported generally begin with a solvent clean (typically sonication in acetone, isopropanol and deionized water), followed by either buffered oxide etch (BOE) [85], HF (46%) followed by Piranha etch (H₂SO₄ + H₂O₂) [88], and Piranha etch (H₂SO₄ + 30% H₂O₂ 1:1) followed by BOE [91]. Piranha etch is, however, known to be highly exothermic and therefore potentially hazardous.

6.1 Experimental

All bulk substrates used in this work were pieces cut from 2-inch diameter, *n*-type β -Ga₂O₃ wafers grown using the edge-defined film fed growth (EFG) method and purchased from Tamura Corporation, Japan. The substrates were grown with Sn as an *in-situ* dopant, at a concentration of 5 × 10¹⁸ cm⁻³. Epilayers of unintentionally doped β -Ga₂O₃ on *n*-type (010) β -Ga₂O₃ were also used in this study; these were grown using molecular beam epitaxy (MBE) by Novel Crystal Technology Inc. in Japan. Refer to Chapter 4 for more information on the substrates used.

Based on common practice for surface treatment in other semiconductor systems, we selected three acids etchants (10% dilute HCl, BOE and 10% dilute HF) as possible candidates in our cleaning treatment study. However, HF was eliminated in a preliminary study because it was observed to etch and adversely affect photoresist used for photolithography during sample preparation, as shown in Fig. 6.1. In addition, since H_2O_2 has been reported to improve rectifying behavior in Schottky contacts to ZnO [97], similar behavior could be expected for H_2O_2 treatment on β -Ga₂O₃ since they are both oxide semiconductors. We

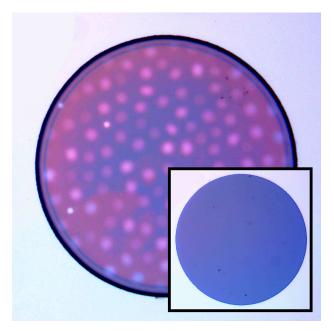


Figure 6.1: Optical image of photoresist after a 5 min HF soak. Insert shows photoresist before HF soak.

therefore included H_2O_2 in the selected acid etchants in our surface treatment study.

In total we in investigated five different wet chemistry cleaning methods. Each substrate was first degreased by sequentially sonicating in (1) acetone, isopropanol and deionized (DI) water. This treatment was used as a performance baseline, for the other four samples, and was followed with a (2) HCl, (3) BOE, (4) HCl and H_2O_2 or (5) BOE and H_2O_2 . For the HCl clean, samples were soaked in 10% hydrochloric acid (HCl) for 5 min at room temperature and rinsed in DI water. For BOE clean, samples were soaked in BOE at room temperature for 5 min. For H_2O_2 clean, samples were soaked in boiling hydrogen peroxide (H_2O_2) at 85 °C for 5 min and rinsed in DI water. All samples were blown dry in nitrogen after each DI water rinse. A summary of our cleaning methods considered and used in our study is shown in Fig. 6.2.

From our wet chemistry surface cleaning study, we concluded that a pre-metallization

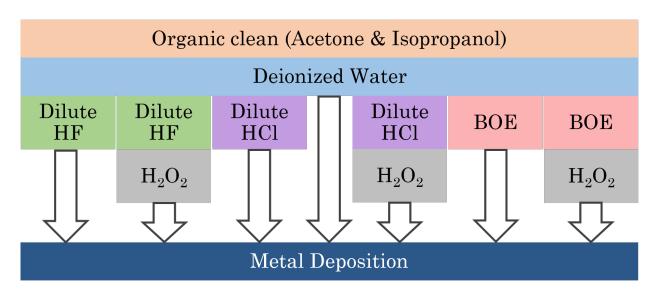


Figure 6.2: Summary of all cleaning methods considered for this study. The first two methods involving HF were rejected, leaving the five remaining methods which we have studied.

treatment of solvent clean, followed by a soak in HCl, and lastly a soak in boiling H_2O_2 produced Schottky diodes with the overall best performance, measured by a decrease in ideality factor, increase in Schottky barrier and a decrease in series resistance.

Having established a common surface treatment procedure, it was then possible for us to investigate and compare the electrical behavior of Schottky diodes with different Schottky metals. In our study, we investigated five different Schottky metals (W, Cu, Ni, Ir, and Pt) with moderate to high metal work functions. Based on the Schottky-Mott model, metals with work functions higher than the electron affinity of the semiconductor should form Schottky contacts. Work functions of the five selected metals are summarized in Table 6.2.

There are several methods that can be used to measure and characterize Schottky barriers at metal-semiconductor interfaces, such as current-voltage (I-V), capacitance-voltage (C-V), activation energy and photoelectric methods. For intimate contacts with a clean interface, all the above methods should yield consistent barrier heights. In reality, discrep-

Element	$\phi~(\mathrm{eV})$
W	4.55
Cu	4.65
Ni	5.15
Ir	5.27
Pt	5.65

Table 6.2: Work functions ϕ_M of selected metals [11].

ancies are often observed between different methods, which may result from causes such as interface contaminants, an intervening insulating layer, edge leakage current, or deep impurity levels. For this work, Schottky barrier heights were calculated from the I-V and C-V measurements of the diodes.

Schottky diodes were prepared with two different device structures–a lateral and vertical device structure. For the lateral structure, both the ohmic and Schottky contacts are formed on the top side of the substrate. The Schottky contacts were circular dots with diameter of 200 μ m, separated from the ohmic contacts by 100 μ m. Contacts were patterned using conventional photolithography lift-off. A schematic of this device structure is shown in Fig. 6.3(b). Samples for the cleaning study were all fabricated using this structure. More than 50 diodes were for prepared on each sample for good statistical analysis. The lack of a passivation/isolation layer and the small separation between the Schottky and ohmic contacts likely increased leakage current due to edge leakage. While this should not significantly affect our conclusions, it should be taken into consideration in future Schottky diode fabrication.

The other structure consisted of a vertical structure. In these devices the ohmic contact covered the entire back side of the device. Schottky diodes were formed using a contact shadow mask on the top side of the substrate. The shadow mask consists of holes with four different diameters (500, 250, 125, and 62.5 m). Schematic of this device structure is shown in Fig. 6.3(a).

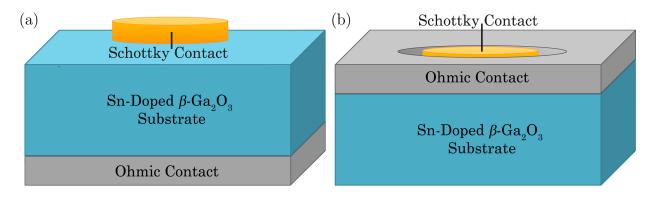


Figure 6.3: Schematic of Schottky diodes with a (a) vertical and (b) lateral device structure.

Ti/Au (20 nm / 100 nm) ohmic contacts were formed by electron-beam evaporation (base pressure: $1-5 \times 10^{-9}$ torr) onto unheated substrates and post-annealing in flowing Ar at 400 °C for 1 min. This metal stack was found to produce the most reliable and consistent ohmic behavior [98]. Anneals were performed in a quartz tube in a resistively heated furnace. The Schottky metals (50 nm) were also deposited by electron beam evaporation onto unheated substrates.

Current-voltage (I-V) measurements were performed using an Agilent 4155C Semiconductor Parameter Analyzer and a Signatone S-1060H-4QR probe station. Capacitancevoltage (C-V) measurements were performed using the HP 4284A LCR meter.

The ideality factor n, Schottky barrier height ϕ_B , and series resistance R_s values were calculated from the *I-V* measurement using the method described by Cheung and Cheung [99]. The built-in voltage (ψ_{bi}) , carrier concentration $(n_d - n_a)$ and Schottky barrier height ϕ_B are calculated from the *C-V* measurement.

6.1.1 Extracting Schottky Barrier Data

As discussed in Chapter 3, for rectifying Schottky diodes, carrier injection follows the thermionic emission theory, which states that

$$I = I_S \left[\exp \frac{qV_D}{kT} - 1 \right]$$

$$I_S = A_{\text{eff}} A^{**} T^2 \exp\left(-\frac{q\phi_B}{kT}\right)$$
(6.2)

where I_S is the saturation current, q is the electronic charge, V_D is the voltage applied across the diode, k the Boltzmann constant, T the absolute temperature, A_{eff} is the effective area of the diode, A^{**} is the Richardson constant, and ϕ_B is the Schottky barrier height of the diode.

For our calculations, we used an effective Richardson constant of 33.65 A/(cm·K)², based on an electron effective mass is $m^* = 0.28m_0$ [2, 17, 38]. However, most other reports in the literature assumed an electron effective mass of $m^* = 0.342m_0$ [35] to calculate an effective Richardson constant of 40.8 A/(cm·K)². The actual value of the Richardson constant, however, does not change the Schottky barrier height of a Schottky diode significantly.

We can extract information about the Schottky barrier heights and ideality factors of the device contacts. Details of how this is done is provided in Appendix A. An example of a representative measured I-V data is shown in Fig. 6.4, which shows the forward I-Vbehavior of a Ni on β -Ga₂O₃ Schottky diode in both linear and log scale. Current increases exponentially at first, illustrated by the linear region in the logI-V plot. The linear region continues for several decades until the series resistance component begins to dominate. The region where series resistance begins to dominate is represented by the straight region in the linear plot, or the turnover region in the log plot.

After the device properties are extracted from the measured data, they are substituted back into the thermionic emission equation and the calculated fit is compared to the original measurement. A *R*-squared coefficient of determination (R^2) is calculated to determine goodness of fit. Only data with $R^2 > 0.98$ were selected for the subsequent analysis. Data whose fit did not meet the R^2 criterion generally corresponds to data that did not follow the thermionic emission model. This will be discussed in later sections.

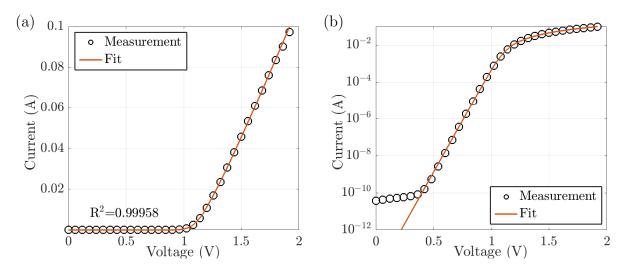


Figure 6.4: Forward current-voltage (I-V) characteristics of a Ni/Ga₂O₃ Schottky diode in the (a) linear, and (b) log scale.

Under reverse bias, the depletion region at the metal-semiconductor interface acts as a capacitor. For an *n*-type semiconductor with permittivity ε_s and electron carrier concentration n_d , its depletion width is given by,

$$W_D = \sqrt{\frac{2\varepsilon_s}{qn_d} \left(\psi_{bi} - V - \frac{k_B T}{q}\right)} \tag{6.3}$$

where ψ_{bi} is the built-in voltage of the substrate. Capacitance can therefore be expressed

as,

$$C = \frac{A\varepsilon_s}{W_D} = A\sqrt{\frac{q\varepsilon_s n_d}{\psi_{bi} - V - k_B T/q}}$$
(6.4)

where A is the area of the diode. Alternatively,

$$\frac{1}{C^2} = \frac{2(\psi_{bi} - V - k_B T/q)}{A^2 q \varepsilon_s n_d}$$
(6.5)

A plot of $1/C^2$ vs V therefore gives ψ_{bi} as the intercept, and n_d can be calculated from the slope. The Schottky barrier height is related to the built-in voltage and carrier concentration by

$$\phi_B = \psi_{bi} + \phi_n + \frac{k_B T}{q} \tag{6.6}$$

$$\phi_n = E_C - E_F = k_B T \ln \frac{N_c}{n_d} \tag{6.7}$$

Where N_c is the effective density of states in the conduction band,

$$N_c = 2 \left(\frac{2\pi m^* k_B T}{h^2}\right)^{3/2}$$
(6.8)

An example of a representative plot of measured C-V data is shown in Fig. 6.5, which shows the capacitance decrease monotonically with increased reverse voltage bias. The $1/C^2$ vs. V graph (Fig. 6.5(b)) is linear, as expressed in Eqn. 6.5.

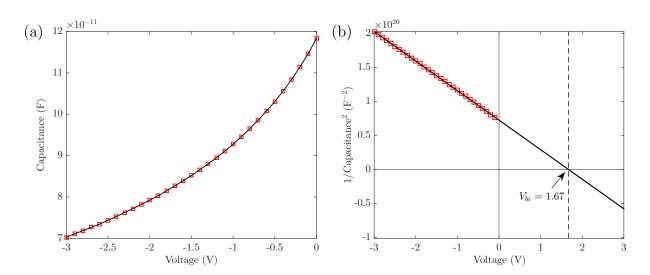


Figure 6.5: Capacitance-voltage (C-V) characteristics of of a Ni/Ga₂O₃ Schottky diode. (a) Plot of C vs. V. (b) Plot of $1/C^2$ vs. V.

6.2 Effects of Surface Preparation

Table 2 shows a summary of results from the five different cleaning methods. The calculated Schottky barrier heights, which range between 0.87 - 0.94 eV, are at the lower end but within the range of values reported for Ni Schottky contacts in the literature.

Method	$\phi_B~({ m eV})$	n	$R_{s}~(\Omega)$	$\begin{array}{c} {\rm Leakage\ Current}\\ {\rm Density\ @\ -5V}\\ {\rm (A/cm^2)} \end{array}$
Organic	0.87 ± 0.01	1.40 ± 0.06	3.73×10^6	0.60 ± 0.28
HCl	0.89 ± 0.01	1.26 ± 0.05	$9.06 imes 10^5$	0.84 ± 0.29
BOE	0.89 ± 0.01	1.26 ± 0.02	2.03×10^5	1.12 ± 0.20
$\mathrm{HCl} \& \mathrm{H}_2\mathrm{O}_2$	0.94 ± 0.01	1.30 ± 0.02	6.94×10^2	0.63 ± 0.20
BOE & H_2O_2	0.90 ± 0.01	1.37 ± 0.03	2.30×10^3	1.37 ± 0.20

Table 6.3: Summary of results from I-V measurements of Schottky diodes pre-treated with different wet chemicals.

Each of the acid treatments yielded diodes with lower ideality factors, higher Schottky barrier heights and lower series resistances than those treated with organic solvents only. The samples cleaned with HCl and H_2O_2 exhibited the highest barrier heights and lowest series resistances of any of the treatments. The large variation in leakage current density is attributed to edge effects and defects. Figure 6.6 shows a comparison of barrier height versus ideality factor and series resistance for the 5 samples.

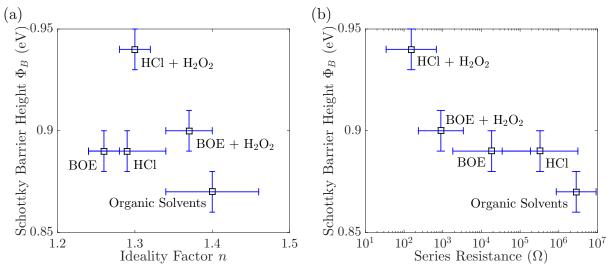


Figure 6.6: Schottky barrier height vs. (a) ideality factor and (b) series resistance for diodes pre-treated using different wet chemicals.

Previous work on ZnO suggests that H_2O_2 treatment yields an O-rich surface, and lowers the oxygen vacancy concentration near the surface [100]. Oxygen vacancies, which are deep level defects in ZnO, are suspected to pin the Fermi level. It has been observed that H_2O_2 treatment increases the Schottky barrier height on ZnO [97] in correspondence with an upward band banding at the surface of ZnO [97]. Similar effects from H_2O_2 might be expected in Ga₂O₃, where oxygen vacancies are also predicted to be deep level defects [101]. Upward band bending has also been proposed in β -Ga₂O₃ for the reduction of carrier concentration on annealing in oxygen rich atmospheres [102]. The results from this study indicate that treatment of the Ga_2O_3 surface with HCl and H_2O_2 is beneficial but likely insufficient to create an ideal surface for device fabrication. Further investigation is needed to determine if additional surface treatment steps (e.g., BCl_3) could be combined with these wet chemicals to remove surface damage and yield a more ideal surface.

6.3 Schottky Barrier Heights of Different Metals

Metal	Work Function	Structure	Substrate	IV		CV
Wietai	ϕ_M (eV)	Structure	Substrate	$\phi_B~({ m eV})$	\boldsymbol{n}	$\phi_B~({ m eV})$
Sn	4.42	Lateral	Bulk	0.81	2.0	
W	4.55	Lateral	Bulk	$0.91{\pm}0.09$	1.40 ± 0.4	1.94 ± 0.1
		Lateral	Epi	$1.05{\pm}0.03$	2.68 ± 0.3	
Cu	4.65	Vertical	Bulk	1.13 ± 0.1	1.53 ± 0.2	
Au	5.10	Lateral	Bulk	0.89	1.7	
Ni	5.15	Lateral	Bulk	$1.04{\pm}0.02$	$1.33 {\pm} 0.03$	1.61 ± 0.1
		Lateral	Epi	1.08 ± 0.1	1.68 ± 0.4	1.96 ± 0.4
		Vertical	Bulk	$1.00 {\pm} 0.06$	1.57 ± 0.2	
Ir	5.27	Lateral	Bulk	1.29 ± 0.1	1.45 ± 0.2	2.3 ± 0.4
		Lateral	Epi	$1.40 {\pm} 0.08$	1.64 ± 0.2	2.7 ± 0.8
Pt	5.65	Lateral	Bulk	$1.05 {\pm} 0.03$	$1.40{\pm}0.04$	1.9 ± 0.1
		Lateral	Epi	1.34 ± 0.1	1.87 ± 0.3	6.0 ± 1.8

Table 6.4: Summary of results from Schottky metal comparison.

Table 6.4 lists the Schottky barrier heights and ideality factors calculated from I-V and C-V measurements of Schottky diodes fabricated with different Schottky metals. Results are listed according to the Schottky metal used, its device structure (lateral vs. vertical), and whether the diode is directly on a ($\overline{2}01$) substrate or a (010) substrate with epitaxial

layer. All of these diodes were cleaned using $HCl + H_2O_2$ prior to metal deposition.

The Schottky barrier heights are plotted as a function of the Schottky metal work function in Fig. 6.7. It is noted that the barrier height values calculated from C-V measurements in each case were significantly higher than the respective I-V determined values. It is normal for C-V determined barrier heights to be higher than I-V determined barrier heights, especially in the presence of barrier inhomogeneity in a non-ideal diode. It is therefore expected that the I-V values in this study are underestimated due to the fact that the ideality factors are > 1.0. Inhomogeneous Schottky barriers and their effect on electrical behavior of Ga₂O₃ diodes are discussed in more detail in the next section.

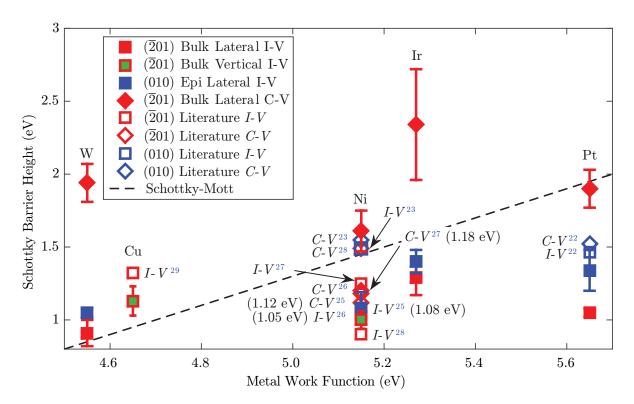


Figure 6.7: Calculated Schottky barrier heights vs metal work function for Schottky diodes on $(\overline{2}01)$ bulk and (010) epitaxial β -Ga₂O₃. Schottky barrier height values on β -Ga₂O₃ as reported in the literature are also included for comparison.

There are a few important observations we can make in this study. Firstly, as mentioned above, the I-V determined barrier heights are significantly lower than the C-V determined barrier heights (Table 6.4 and Fig. 6.7). It is expected that the C-V determined values are more accurate in terms of an areal average Schottky barrier within each diode; however, the I-V determined values should be more representative of the effective Schottky barrier heights for current flow. The non-ideal (n > 1.0) behavior is attributed to defects in the Ga₂O₃ that act as low-barrier regions that yield inhomogeneous Schottky diodes.

6.4 Inhomogeneous Barrier

Most devices show a linear region at moderately high current, a rollover due to series resistance at the highest current, and in many cases an excess leakage component at low current. The nonidealities are broadly of two types. First, the ideality factor high current (the 'ideal' region) is usually significantly greater than one (\sim 1.5), and the corresponding barrier height is reduced. Second, excess currents are sometimes observed as distinct portions of the characteristics of low currents. The series resistance associated with the excess current appears to be higher than that associated with the ideal current. An additional spreading resistance therefore seems to be associated with the path of the excess current flow.

In the latter nonideal diodes, their I-V plots consists of a linear region extending only about two orders of magnitude. the low forward voltage region exhibits a higher current than what is expected using the thermionic emission model if ϕ_B is extracted from the high voltage linear region. Excess current is in the range $10^{-11} - 10^{-7}$ A. An example of such behavior observed for a Ni vertical Schottky diode is shown in Fig. 6.8(a).

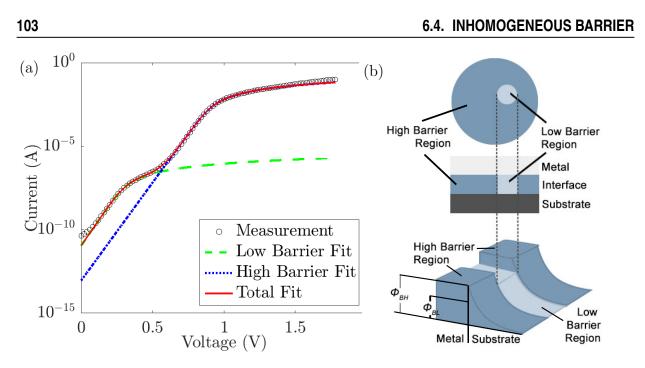


Figure 6.8: (a) Schematic of a diode with two distinct barrier regions. (b) Log I vs V plot of a Ni/Ga_2O_3 Schottky diode at room temperature showing two linear regions, modeled as two regions of different barrier heights.

From this low forward voltage region, a linear trend parallel to the first one is evidenced. Therefore, it appears that the forward characteristic I-V behaves as a rectifier with two SBH. The first one is the high SBH and is responsible for the linear region at high current, while the second one is the low SBH and is responsible for the linear region observed at low voltage. This double barrier behavior can be modeled as two diodes in parallel, one with a high barrier height and one with a low barrier height. The total current through the contact would be the sum of currents through the high and low barrier patches [35]:

$$I = I_{sat}^{H} \exp\left[\frac{q\left(V - IR_{s}^{H}\right)}{n^{H}kT}\right] + I_{sat}^{L} \exp\left[\frac{q\left(V - IR_{s}^{L}\right)}{n^{L}kT}\right]$$
(6.9)

where the superscripts H and L denote the high and low barrier regions, respectively.

The six fitting parameters are the two saturation currents I_{sat}^{H} and I_{sat}^{L} , correspond-

ing ideality factors, n^H and n^L , and corresponding specific series resistance R_s^H and R_s^L . The first term represents the ideal, thermionic emission current, whereas the second term represents the excess current typically observed at low bias. The two regions are fitted separately using the method outlined in previous sections. After a good fit is obtained for each region, they are combined and the overall fit is calculated, the fit is then refined by varying all six parameters simultaneously. The apparent barrier height is extracted from the ideal saturation current according to the usual relation

$$J_{sat}^{H} = A^{*}T^{2} \exp\left(-\frac{q\phi_{B}}{kT}\right)$$
(6.10)

where A^* is the effective Richardson constant and ϕ_B is the barrier height.

In this case the low barrier region was calculated to have a Schottky barrier of 0.8 0.06 eV. As evidence that this behavior is associated with defects in the material, it was observed that the percentage of diodes displaying this behavior increased as the diode area increased (Fig. 6.9). I.e., the larger the contact size, the more likely it is to overlap with an area of defects.

The dependence of nonhomogeneous diode behavior on contact size suggests discrete patches of low barrier. The larger the contact size, the more likely it is to overlap with one of these patches. The low barrier region could be due to defects at the metal-semiconductor interface.

Another important observation from this study is that neither I-V nor C-V determined Schottky barrier height values showed a strong correlation with the work functions of the metals (Fig. 6.7). The slopes of the least-squares line fits to the bulk lateral I-V, epi lateral I-V, and bulk lateral C-V sets of data were 0.18, 0.29, and 0.16, respectively. The bulk

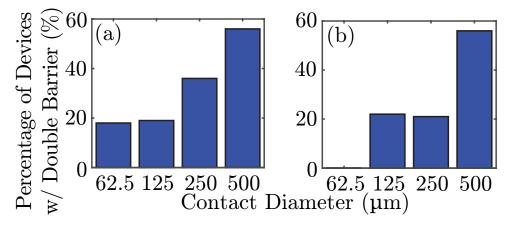


Figure 6.9: Percentage of devices showing double barrier behavior at room temperature for (a) Ni and (b) Cu as a function of contact size.

vertical I-V, which only has two data points, actually has a slight negative slope (-0.18). In comparison with the Schottky-Mott predicted behavior (dashed line in Fig. 6.7) for an ideal diode, these results indicate that factors other than metal work function dominate the Schottky behavior in the Ga₂O₃ diodes reported here. One of the dominant effects on the Schottky diode behavior is likely due to defects in the bulk and/or near surface region of the Ga₂O₃. The inhomogeneous behavior displayed in Fig.6.8 is evidence of the effect of defects within the material.

In the past, I-V derived barrier heights with idealities greater than one have often been corrected to obtain a more fundamental flat-band barrier height using a model due to Wagner *et al.* []. This model assumes the barrier height is lowered because of the voltage drop across a laterally uniform interfacial layer. This assumption is not supported by analysis. Moreover, if a uniform interfacial layer accounted for the variations in I-V barrier height and ideality factor, we would expect annealing of the diodes to reduce ideality factors and correspondingly increase barrier heights.

6.5 Nonideal Schottky Behavior

Schottky barrier effects can be understood in terms of a competition between different interfacial charge transport mechanisms and chemically active interface material structures. The reasons for the highly variable, nonideal properties of these diodes can broadly be classified into four categories,

- 1. Competing transport mechanisms
- 2. Deviation from Schottky-Mott behavior
- 3. Interfacial effects
- 4. Material inhomogeneities

We will address each one of these causes in the sections below.

6.5.1 Competing Transport Mechanisms

The first cause for ideality factors deviating from unity is the existence of transport mechanisms other than thermionic emission, such as tunneling current through the barrier. A rough criterion can be set by comparing the thermal energy kT to E_{00} which is defined as

$$E_{00} = \frac{q\hbar}{2} \sqrt{\frac{N}{m^* \varepsilon_s}} \tag{6.11}$$

When $kT \gg E_{00}$, thermionic emission dominates and no tunneling takes place. When $kT \ll E_{00}$, field emission or tunneling dominates. When $kT \approx E_{00}$, the main transport mechanism is thermionic field emission, which is a combination of thermionic emission and field emission.

The effect of tunneling is roughly equivalent to an effective lowering of the barrier height by an amount $\Delta \phi$, where

$$\Delta \phi = \left(\frac{3E_{00}}{2q}\right)^{2/3} V_d^{1/3} \tag{6.12}$$

and V_d is the diffusion potential of the diode (i.e., total band bending).

6.5.2 Deviation From Schottky-Mott Behavior

The Schottky-Mott relation is calculated based on bulk electronic properties of the metal and semiconductor. An important assumption is that the surface dipole contribution of ϕ_m and χ_S do not change when the metal and semiconductor are brought into contact (or, at least, that the difference between them does not change), that there are no localized states on the surface of the semiconductor, and that there is perfect contact between the semiconductor and the metal.

However, the surface of a crystalline solid differs from the bulk in many important ways. Firstly, because of the absence of neighboring atoms, the equilibrium positions of the surface atoms differ from those corresponding to the perfect crystal lattice. The surface is often said to be relaxed or reconstructed. Relaxation refers to the situation where the surface retains the symmetry of the bulk in a plane parallel to the surface, but the spacing and arrangement of atoms is different from that in the bulk in the direction perpendicular to it.

There is also the presence of localized states at the metal-semiconductor interface. These states can introduce dipoles that screen part of the potential difference between metal and semiconductor, causing Fermi level 'pinning', where band bending becomes insensitive to applied bias across the contact.

$$\phi_B = \phi_M - \chi_S - \Delta\chi \tag{6.13}$$

where semiconductor electronic affinity χ_S and metal work function ϕ_M are measured with respect to the vacuum level E_{VAC} before contact and $\Delta \chi$ represents the interface dipole. For high enough densities of interface states, small movements of the Fermi level within the band gap result in large changes of localized state occupation. As a result, most of the potential difference between metal and semiconductor produces changes in the interface dipole rather than in the surface space charge region. For high interface state densities, the barrier height ϕ_B and band bending qV_B are then relatively independent of metal work function ϕ_M .

6.5.3 Interfacial Effects

Perfectly clean surfaces can only be generated in certain ideal conditions, such as cleaving under vacuum. Under normal atmospheric conditions the surfaces of metals and semiconductors are not clean but are covered by one or more atomic layers of contaminant, usually carbon, hydrocarbon, or oxygen. Nearly always these real as opposed to clean surfaces are disordered, i.e. they lack periodicity both parallel and perpendicular to the surface. Gas absorption at the Ga_2O_3 surfaces causes band bending analogous to Schottky barrier formation, since it involves charge transfer at the semiconductor interface and alters the carrier concentration within the surface space charge region. Some adsorbates such as OH molecules induce band bending and charge conductivity in a manner similar to transistor action. Here, charge transfer between the surface and the adsorbate replaces the gate bias action of the transistor. The field effect is especially pronounced for wide bandgap semiconductors in which the intrinsic carrier concentration is very low.

Chemical interactions at the microscopic metal-semiconductor interface can also alter the classical Schottky barrier picture. Metal-Ga₂O₃ reactions can introduce interfacial insulating dielectric layers, this would be accompanied by interface states at the metal/insulator boundary. Interface states block transport or change the effective work function at the intimate junction. The field-dependent voltage drop across the interfacial layer causes an ideality factor greater than one. The ideality factor n is given by

$$n = 1 + \frac{\delta\left(\varepsilon_s/w + q^2 D_{ss}\right)}{\varepsilon_i \left(1 + \delta q^2 D_{sm}/\varepsilon_i\right)} \tag{6.14}$$

where δ and ε_i are the thickness and dielectric constant of the interfacial layer, respectively; w is the depletion layer width at zero bias; D_{ss} is the areal density of interface states in equilibrium with the semiconductor; and D_{sm} is the areal density of interface states in equilibrium with the metal [103].

Band bending can also occur due to semiconductor anion and/or cation outdiffusion as well as metal or other impurities indiffusion that change the effective carrier concentration at different depths. Such chemical changes thereby contribute to the tunneling and/or hopping transport.

Strong dependence on surface preparation indicates that extrinsic factors such as crys-

tal quality and surface treatment have a large effect on the barrier heights. In contrast, in other semiconductors narrow ranges of barrier heights are typically attributed to Fermi level pinning or metal-induced gap states (MIGS).

6.5.4 Material Inhomogeneities

Thermionic emission of carriers over the Schottky barrier is often augmented by tunneling through the barrier and hopping transport through states within the bandgap. Impurities or defects that increase carrier density inside the surface space charge region will decrease the barrier width, thereby increasing the contribution of tunneling to the overall current. Similarly, increasing defect concentration near the semiconductor surface enables charge movement through the semiconductor depletion region, especially for high defect densities at which their gap state wave functions begin to overlap.

As we observed with the double Schottky barriers, small patches of low barrier height can have a disproportionately large effect on I-V properties, because of the exponential dependence of current flow on barrier height. The C-V barrier height is much less affected, because it is an average over the contact area. The theory of transport in laterally nonuniform Schottky barriers have been discussed extensively by Tung and by Sullivan *et al.* [104, 105]. The temperature dependence of ideality factor can be explained by this model, since transport through the main, large area and high barrier height region becomes dominant over the contributions of the smaller, lower barrier height regions as the temperature is increased.

The lateral nonuniformity model gives the best explanation of our data. This model can readily explain diode-to-diode variations, if it is assumed that the low barrier height patches involve randomly distributed defects.



Characterization of β -Ga $_2$ O $_3$ Thin Films Grown

at SMI

7.1 Growth System

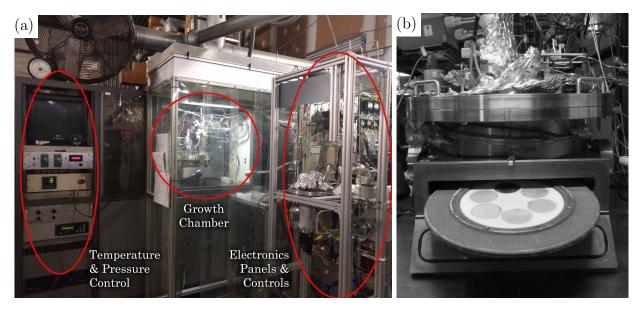


Figure 7.1: (a) Photo of vapor deposition system at Structured Materials Industries Inc. (SMI), used in growing all samples discussed in this chapter. (b) Close-up photo of the growth chamber, containing the 13-inch graphite platter.

All samples discussed in this and the next chapter were grown in an in-house vertical vapor phase epitaxy (VPE) reactor at Structured Materials Industries (SMI) Inc. (Fig. 7.1). The reactor consists of a 16-inch diameter stainless steel chamber and a single rotating disc reactor with a 13-inch graphite platter. Gas flows from a showerhead at the top of the chamber. Resistive heaters below the graphite platter are capable of providing growth temperatures up to 800 °C.

In this chapter, the growth of β -Ga₂O₃ using metal-organic chemical vapor deposition (MOCVD) techniques will be discussed. Trimethylgallium (TMGa) was used as the Ga precursor, with Ar as the carrier gas. Oxygen was used as the oxidizer. The majority of films were grown heteroepitaxially on *c*-plane (001) unintentionally doped (UID) sapphire (Al₂O₃) wafers, but films were also grown homoepitaxially on ($\overline{2}01$) and (001) β -Ga₂O₃.

7.2 Heteroepitaxy on Sapphire (001)

Single-side chemo-mechanically polished 2-inch diameter *c*-plane (001) unintentionally doped (UID) sapphire (Al₂O₃) wafers were used. Wafers were epi-ready as received and did not undergo any pre-deposition treatment. Based on prior studies by SMI into the conditions that optimize film growth (Fig. 7.2), the chamber pressure and growth temperature for subsequent growths were set at 45 Torr and 650 °C respectively.

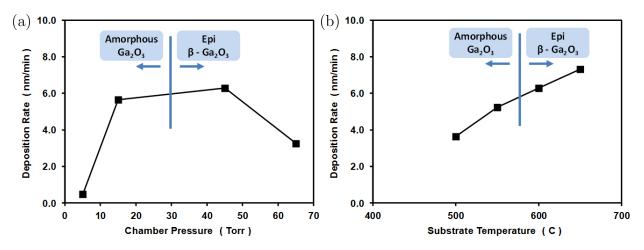


Figure 7.2: Growth rates as a function of (a) chamber pressure and (b) substrate temperature.

Fig. 7.3 shows the XRD of a β -Ga₂O₃ thin film grown by SMI on sapphire (001). Similar to the XRD profile of single crystal β -Ga₂O₃, peaks were observed at $2\theta = 18.949^{\circ}$, 38.420°, 59.184° and 82.375°. These peaks can be assigned to the ($\overline{2}01$), ($\overline{4}02$), ($\overline{6}03$) and ($\overline{8}04$) reflections of β -Ga₂O₃, respectively. An intense peak at $2\theta = 42.269^{\circ}$ is attributed to the (006) reflection of the sapphire substrate. In addition, weak peaks were observed at $2\theta = 30.355^{\circ}$, 52.565° and 64.201°, corresponding to the (110), ($\overline{6}01$) and ($\overline{2}04$) Bragg reflections of β -Ga₂O₃. It is worth noting that ($\overline{6}01$) and ($\overline{2}04$) are vicinal to ($\overline{2}01$). The presence of these additional weak peaks indicate defects in the epitaxial film, which we will later see can be attributed to the columnar growth ~100 nm away from the substrate interface.

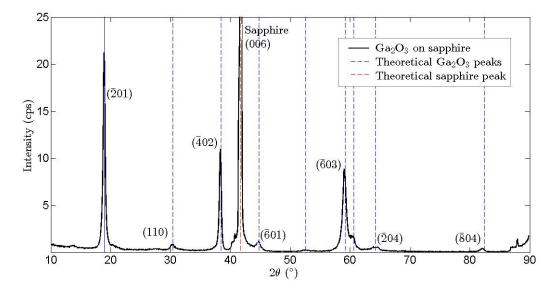


Figure 7.3: 10-90° θ -2 θ XRD scan of β -Ga₂O₃ oriented film grown on c-plane sapphire (001) substrate.

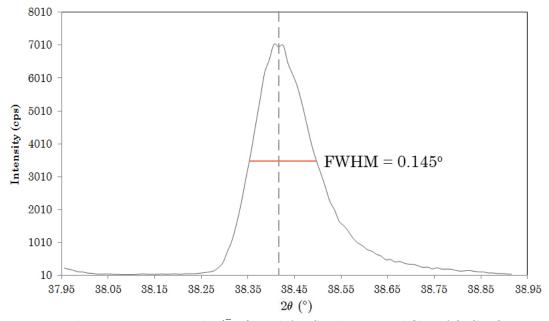


Figure 7.4: Rocking curves around the $(\bar{4}02)$ peak for SMI's epitaxial film of β -Ga₂O₃ on sapphire substrates. The FWHM is 0.145°.

The rocking curve of the $(\bar{4}02)$ peak was measured with high resolution optics. The full width half maximum (FWHM) was calculated to be 0.145°, this is about 5 times wider than the FWHM of single crystal β -Ga₂O₃.

Transmittance of the β -Ga₂O₃ film on sapphire compared to bulk β -Ga₂O₃ and bulk sapphire is shown in Fig. 7.5.

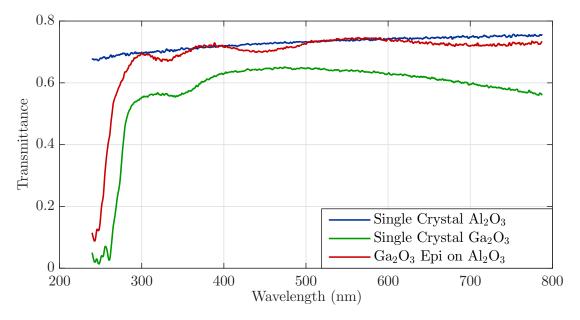


Figure 7.5: Transmittance spectra of bulk sapphire substrate, bulk β -Ga₂O₃ substrate, and a β -Ga₂O₃ thin film grown on sapphire by SMI.

The lower transmittance of bulk β -Ga₂O₃ at higher wavelength is due to the presence of Sn dopants in the crystal, and the overall lower transmittance is due to the fact that the sample is thicker (680 μ m).

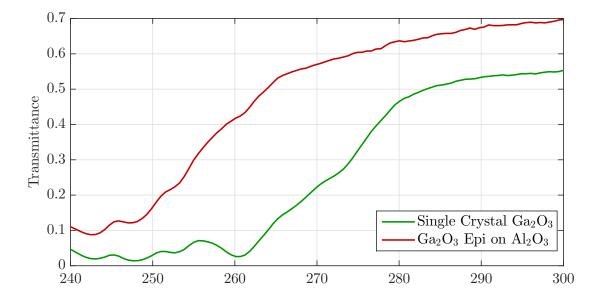


Fig 7.6 shows the transmittance of β -Ga₂O₃ near its absorption edge.

Figure 7.6: Transmittance spectra of bulk β -Ga₂O₃ substrate and β -Ga₂O₃ thin film on sapphire near their adsorption edge.

The adsorption edge of the β -Ga₂O₃ epilayer appears to be at ~ 248 nm, which would correspond to a band gap of ~ 5.0 eV. This is higher than the adsorption edge of bulk β -Ga₂O₃, which at ~ 261 nm, corresponds to a band gap of ~ 4.75 eV. The difference is absorption edge could be due to Sn doping in the bulk substrate, which could change the electronic structure of the crystal. Fig. 7.7 shows TEM images of the epitaxial layer grown on sapphire (0001) by SMI, For more details on sample preparation, please refer to Section 5.1. The near-interface region shows coherent epitaxial growth (Fig. 7.7(a)), but after ~ 100 nm, columnar growth behavior took over (Fig. 7.7(b)). Each column is approximately 50 nm wide.

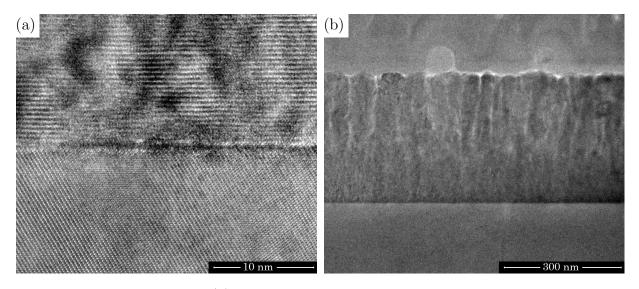


Figure 7.7: TEM images of (a) interface between β -Ga₂O₃ and sapphire substrate. Growth is epitaxial with an abrupt interface. (b) β -Ga₂O₃ epitaxial layer on sapphire substrate. Columnar growth can be observed after ~100 nm.

This observed columnar growth agrees with our observation of the film surface morphology under the AFM and SEM (Fig. 7.8).

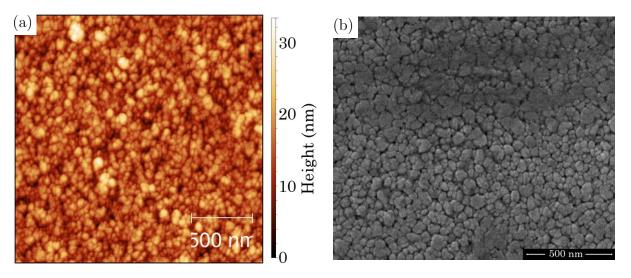


Figure 7.8: (a) $2 \times 2 \ \mu m \ AFM \ images \ and$ (b) SEM image of β -Ga₂O₃ thin film on sapphire.

Under both AFM and SEM, we observed a granular surface with grain size approximately 50 nm. The RMS roughness from AFM measurement is 3.87 nm. This also explains the broadening of the XRD peaks, as well as presence of vicinal planes. It is evident that the film followed the Stranski-Krastanov growth mode. Initial layer-by-layer growth is strained, and is replaced by islands that grew upwards in the form of columns. Since this is not a common problem reported in the literature, further studies need to be carried out to verify this is intrinsic to β -Ga₂O₃ growth on sapphire, and whether such growth behavior can be eliminated.

7.3 Homoepitaxy on β -Ga₂O₃

SMI has also grown homoepitaxial films of β -Ga₂O₃ using MOCVD on single crystal β -Ga₂O₃. On ($\overline{2}01$) β -Ga₂O₃ bulk substrates that we have provided. Without any pre-growth treatment, a very rough film was grown. A SEM image of the homoepitaxial surface is shown in Fig. 7.9. We observe cluster formations ~200 nm in diameter on the surface. It is unclear whether there was any epitaxial growth at all.

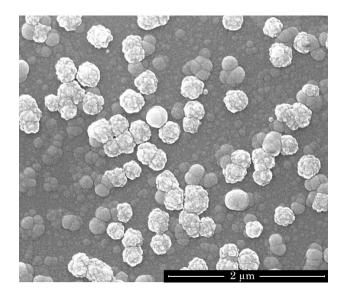


Figure 7.9: SEM of β -Ga₂O₃ thin film grown on untreated bulk β -Ga₂O₃ single crystal substrate.

However, growing on a substrate that has been cleaned immediately prior to loading into the chamber, by sonication in organic solvents (acetone, and then isopropanol), and in DI water, the surface morphology greatly improved.

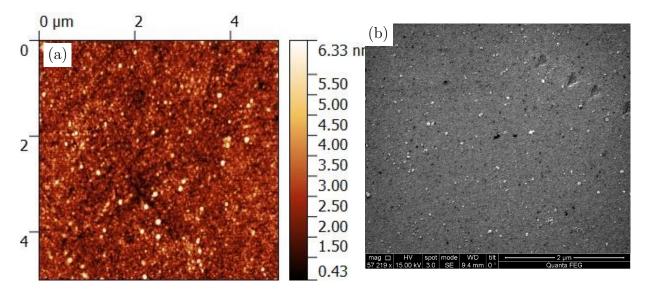
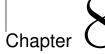


Figure 7.10: (a) $5 \times 5 \ \mu m \ AFM$ image and (b) SEM image of β -Ga₂O₃ thin film grown on cleaned bulk β -Ga₂O₃ single crystal substrate.

Preliminary work on fabricating vertical Schottky diodes on this material was not successful. Future work needs to be done to make this material device-quality.



Epitaxial Growth of Ga₂**O**₃ **Polymorphs**

This chapter continues the discussion on the growth of heteroepitaxial films of Ga_2O_3 on *c*plane sapphire (006), but will be focusing on the use of halide vapor phase epitaxy (HVPE) growth technique. For HVPE, instead of using a metalorganic (in this case TMGa or TEGa) as the Ga precursor, a halide is used instead. Gallium chloride precursors are created one of two ways for this work, either by flowing GaCl₃, or by flowing HCl gas over Ga metal to produce GaCl.

The growth of different phases of Ga_2O_3 were observed under different growth conditions, specifically, the metastable α - and ε -phases were grown instead of the stable β -phase. We hypothesize that the presence of Cl may occupy O sites, causing compressive stress on the crystal, which favors the formation of the metastable polymorphs.

8.1 Crystal Structures

As discussed in the introduction, there are five known polymorphs of Ga_2O_3 (α -, β -, γ -, δ -, and ε - Ga_2O_3). Since only α -, β - and ε -phases are observed in our work, the discussion in the remainder of this chapter will be limited to these phases. The crystal structure of the α - Ga_2O_3 , β - Ga_2O_3 , and ε - Ga_2O_3 are summarized in Table 8.1. Unit cells of these three phases, as well as sapphire, are shown in Fig. 8.1.

Table 8.1: Crystal parameters for α -Ga₂O₃, β -Ga₂O₃, ε -Ga₂O₃ [12, 13].

Phase	Crystal System	Point Group	a (Å)	b (Å)	c (Å)	β (°)
α	Trigonal	$R\bar{3}c$	4.983	13.433	_	_
β	Monoclinic	C2/m	12.23	3.04	5.80	103.7
ε	Hexagonal	$P6_3mc$	2.907	9.2559	—	—

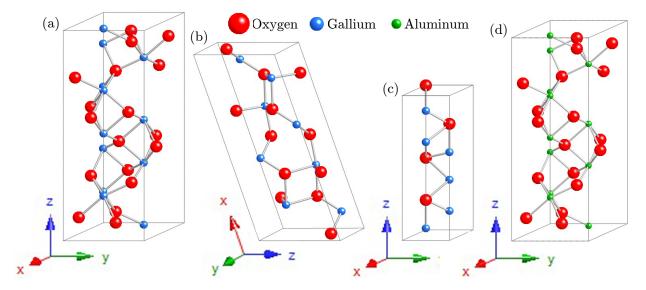


Figure 8.1: Unit cells of (a) α -Ga₂O₃, (b) β -Ga₂O₃, (c) ε -Ga₂O₃, and (d) sapphire.

8.1.1 Lattice Mismatch with Sapphire (0001)

On *c*-plane sapphire, the typical orientation of the α -, β -, ε -phases are (001), ($\overline{2}$ 01) and (001) respectively. This also corresponds to a lattice mismatch of 4.7%, 6.12%, and 5.6%, respectively. α -Ga₂O₃ is the best lattice matched polymorph and has the same corundum structure of sapphire. Fig. 8.2 shows the crystal structure of sapphire along the (001) plane, together with the crystal structure of α -Ga₂O₃, β -Ga₂O₃, and ε -Ga₂O₃ along their growth planes.

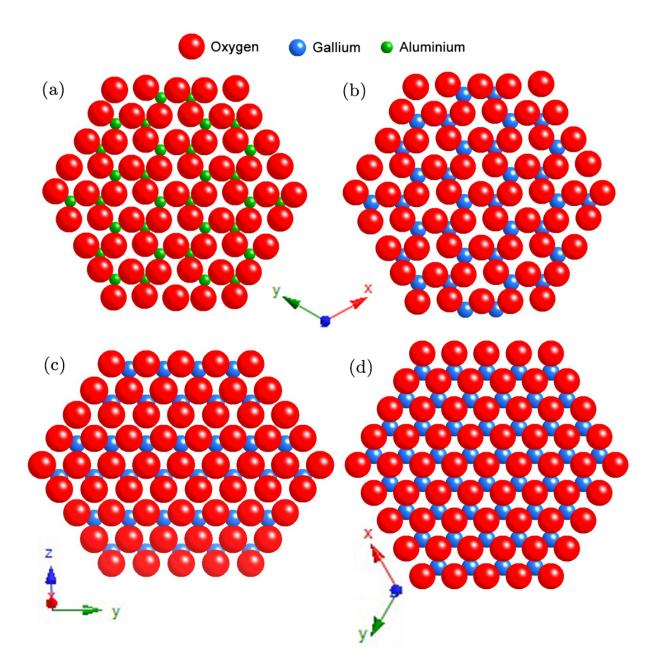


Figure 8.2: Plane view crystal structure of (a) sapphire (001), (b) α -Ga₂O₃ (001), (c) β -Ga₂O₃ ($\overline{2}$ O1), and ε -Ga₂O₃ (001). This shows an average lattice mismatch between α , β , ε -Ga₂O₃ to sapphire of 4.7%, 6.12%, and 5.6% respectively.

Method	Substrate	Precursors	Temperature	Pressure	Film Growth
MBE, PLD, MOCVD [106]	<i>c</i> -plane sapphire				3 monolayer pseudomorphic α -Ga ₂ O ₃ followed by β -Ga ₂ O ₃
RF magnetron sputtering [107]	a-plane sapphire	O_2	≥ 600 °C		(110) α -Ga ₂ O ₃
Ultrasonic mist CVD [108]	<i>c</i> -plane sapphire	Ga(C ₅ H ₈ O ₃) ₃ , DI water, HCl, N ₂ carrier gas	$430 - 470 \ ^{\circ}C$ (amorphous at lower <i>T</i> , includes β -Ga ₂ O ₃ at higher <i>T</i>)	Atmospheric pressure	α -Ga ₂ O ₃
HVPE [109]	<i>c</i> -plane sapphire	gallium chloride (Ga metal $+$ HCl), O_2 , N ₂ carrier gas	525-650 °C	atmospheric pressure	$lpha$ -Ga $_2$ O $_3$
Plasma assisted MBE [110]	<i>c</i> -plane sapphire	Ga and O ₂ plasma	$500 - 1000 \ ^{\circ}C$	gallium beam equivalent pressure $(BEP) = 3 \times 10^{-8} - 1.5 \times 10^{-7}$	eta-Ga ₂ O ₃ with lpha-Ga ₂ O ₃ inclusions
MOCVD [111, 112]	<i>c</i> -plane sapphire	TMGa and H_2O	650 °C	100 mbar	ε -Ga $_2O_3$
ALD [112]	c-plane sapphire	TMGa and H_2O	550 °C	N.R.	ε -Ga $_2O_3$
HVPE [113]	GaN (0001), AlN (0001), and β -Ga ₂ O ₃ ($\bar{2}01$)	GaCl ₃ and O ₂	550 °C		ε -Ga $_2$ O $_3$
Mist CVD [114]	MgO (111), YSZ (111)	Ga(C ₅ H ₈ O ₃) ₃ , DI water, HCl, N ₂ carrier gas	400-700 °C		ε -Ga $_2O_3$

Table 8.2: Summary of epitaxial growths of α - and ε -Ga₂O₃ reported in the literature. RF = radio frequency, CVD = chemical vapor deposition.

8.2 Growth of Different Polymorphs

Table 8.2 is a summary of growths of α and ε -Ga₂O₃ reported in the literature. α -Ga₂O₃ and ε -Ga₂O₃ are generally observed to grow at low growth temperatures of < 700 °C, and growth setups with high growth rates of > 20 nm/min.

Fig. 8.3 shows the x-ray diffraction of three samples grown by SMI at 650 °C with different precursors and growth conditions. The three phases, α -, β -, and ε -Ga₂O₃ were observed. The growth conditions for these samples are summarized in Table 8.3.

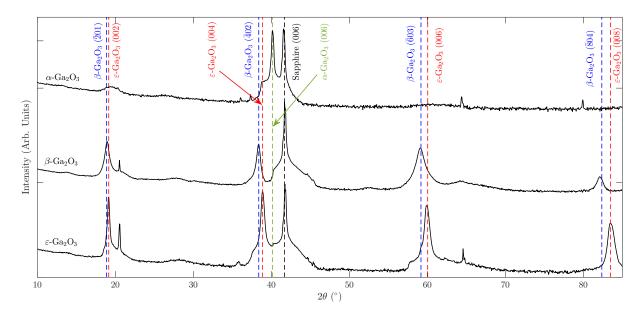


Figure 8.3: *XRD of* α , β , and ε -*Ga*₂*O*₃. *The growth conditions are shown in Table. 8.3.*

The common peak at $2\theta = 42.269^{\circ}$ is attributed to the (006) reflection of the sapphire substrate. For α -Ga₂O₃, an XRD peak was observed at $2\theta = 40.15^{\circ}$, corresponding to its (006) reflection. For β -Ga₂O₃, peaks were observed at $2\theta = 18.949^{\circ}$, 38.420° , 59.184° and 82.375° , corresponding to the ($\overline{2}01$), ($\overline{4}02$), ($\overline{6}03$) and ($\overline{8}04$) reflections of β -Ga₂O₃, respectively. For ε -Ga₂O₃, peaks were observed at 19.16° , 38.88° , 60.06° , and 83.50° , corresponding to its (002), (004), (006), and (008) reflections.

Run # Phase		Flow Rate (sccm)			Pressure	Growth	
nun #	1 Hase	15% HCl in Ar	TMGa	O_2	(Torr)	$\frac{\text{Rate}}{(\text{nm/min})}$	
105	α	138	_	80	550	9.5	
49	β	_	85	2800	45	1	
114	ε	300	—	800	550	18	

Table 8.3: Growth conditions for α , β , and ε -Ga₂O₃ films with growth conditions shown in Table. 8.3. All films were grown at 650 °C.

The optical transmittances of these samples were measured (Fig 8.11). Interference fringes were observed for α - and β -phase films, which indicates good crystallinity. Interference fringes were not noticeable for the ε -phase film. This is consistent with visual inspection, which shows these films to be hazy, indicating a rough surface morphology.

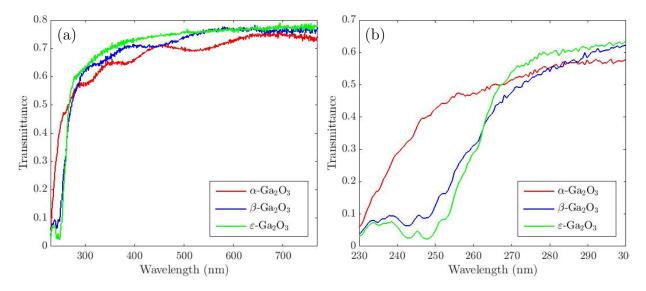


Figure 8.4: (a) Transmittance spectra and (b) optical adsorption edge of α , β , and ε -Ga₂O₃ films with growth conditions shown in Table. 8.3.

The absorption edge of α -Ga₂O₃ is ~ 229 nm, which corresponds to a band gap of ~ 5.4 eV. The adsorption edge of ε -Ga₂O₃ is ~ 252 nm, which corresponds to a band gap

of ~ 4.9 eV.

The growth of β -Ga₂O₃ was always observed when growing with MOVPE, using TMGa and O₂ as precursors, between temperature of 500 – 850 °C. However, A shoulder was observed at $2\theta = 40.15^{\circ}$, which is the location of the α -Ga₂O₃ (006) peak. This could suggest an interfacial layer of α -Ga₂O₃, consistent with a report of a 3-nm-thick pseudomorphic layer of α -phase at the Ga₂O₃/sapphire interface [106]. However, cross-sectional TEM of β -Ga₂O₃/sapphire did not reveal the presence of this interfacial layer (Fig.8.5).

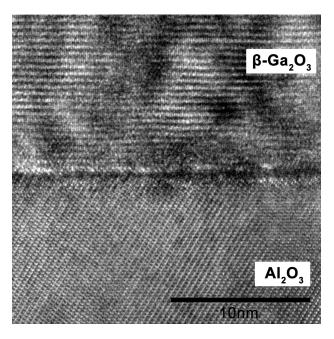


Figure 8.5: Cross-section TEM of β -Ga₂O₃ film on sapphire (006).

For the HVPE studies, we ran a series of growth runs with varying GaCl₃ pressure, chamber pressure, Ar flow rate and O_2 flow rate (Table 8.4). GaCl₃ was kept at a temperature of 80 °C, and the chamber pressure and growth temperature were kept at 550 Torr and 650 °C respectively. All growths runs were for 1 hr.

Table 8.4: Growth conditions for four growth runs of heteroepitaxial Ga_2O_3 on c-plane sapphire using HVPE.

em) (Torr) 700	Rate (sccn 500	$\frac{(n) \text{Rate (sccm)}}{500}$
700	500	500
		000
700	2000	500
700	2000	10
700	5000	200
_	700	700 2000

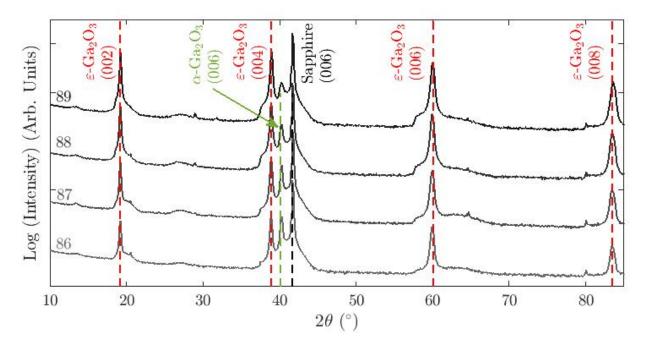


Figure 8.6: XRD of five heteroepitaxial Ga2O3 grown on c-plane sapphire under growth conditions given in Table 8.4.

The XRD results show a combination of α - and ε -phases, with the relative ε -to- α -

phase peak intensity ratio increasing with increase of Ar flow rate (Fig. 8.6). As will be shown later, film growth follows the Stranski Krastanov growth mode, beginning with a semi-coherent layer of α -Ga₂O₃ on the sapphire substrate, followed by island growth of ε -Ga₂O₃.

SEM scans of the surface regions of films #86-#89 grown under conditions detailed in Table 8.4 are presented in Fig. 8.7(a)–(d), respectively. Distinctive hexagonal shaped islands are observed on the surface of films #86 abd #87, as shown in Fig. 8.7(a) and (b). This is consistent with reported domain matched epitaxy island growth mechanism for ε -Ga₂O₃ [112]. Growth runs with lower oxygen flow rate appear to produce ε -Ga₂O₃ film with better surface coverage. Comparison of the SEM images of sample #87 and #88 shows that the islands grew to a maximum size of ~200 nm and then started coalescing. For sample #89, the ε -Ga₂O₃ achieved total coverage.

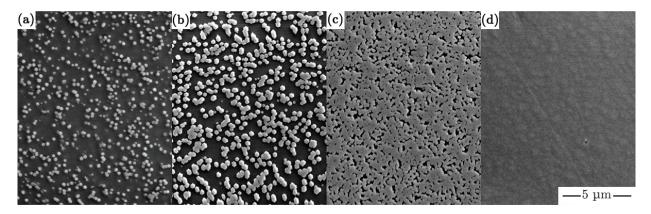


Figure 8.7: SEM images showing growth of heteroepitaxial β -Ga₂O₃ grown on c-plane sapphire under growth conditions given in Table 8.4. Images correspond to (a) #86, (b) #87, (c) #88, and (d) #89, respectively.

Cross-section HRTEM image of sample #89 along the [110] zone axis of sapphire is shown in Fig. 8.8. An initial layer on the sapphire substrate possessed the same crystal structure, which is identified as α -Ga₂O₃, followed by domains of a thick layer which we later identified as ε -Ga₂O₃.

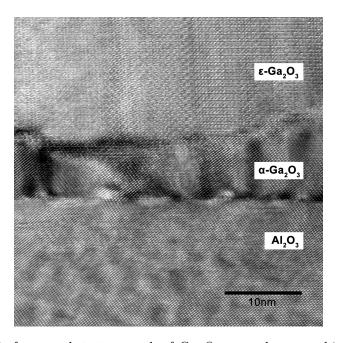


Figure 8.8: *HRTEM of near-substrate growth of* Ga_2O_3 *on c-plane sapphire along the* [110] *zone axis.* A interfacial layer of α - Ga_2O_3 with approximate thickness of ~ 10 nm begins growing on the substrate, followed by a thick layer of ε - Ga_2O_3 .

The diffraction pattern of the interfacial layer matches well with the [110] zone axis of α -Ga₂O₃ (Fig. 8.9(d)), furthermore, a closer examination of the sapphire/ α -Ga₂O₃ interface reveals a lattice match that corresponds to the lattice mismatch between α -Ga₂O₃ and *c*-plane sapphire. This shows that α -Ga₂O₃ [100] is oriented parallel to sapphire [100] and that the α -epilayer grew semi-coherently on the sapphire substrate.

Since the spacings of the (100) planes of α -Ga₂O₃ and sapphire are 4.3 Åand 4.1 Årespectively, every 20 layers of α -Ga₂O₃ corresponds to 21 layers of sapphire. A misfit

dislocation is generated every 8.6 nm (Fig 8.9(c)). The planes that are observed corresponds to the $(1\overline{1}2)$ and $(\overline{1}14)$ series of planes, as shown in the crystal model in Fig. 8.9(b).

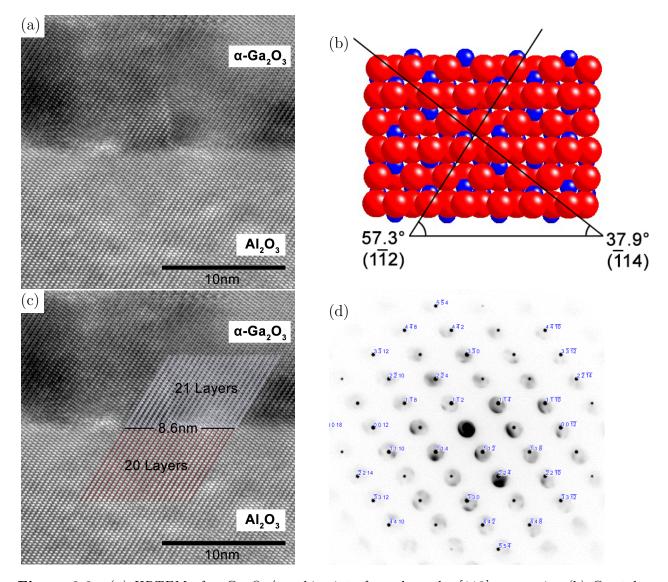


Figure 8.9: (a) HRTEM of α -Ga₂O₃/sapphire interface along the [110] zone axis. (b) Crystal model of α -Ga₂O₃ along the (110) view direction. (c) We can match the lattice planes observed to be the (112) and (114) planes. Since the spacings of the (100) planes of α -Ga₂O₃ and sapphire are 4.3 Åand 4.1 Årespectively, every 20 layers of α -Ga₂O₃ corresponds to 21 layers of sapphire. A misfit dislocation is generated every 8.6 nm. (d) Selected area diffraction of α -Ga₂O₃ along the [110] zone axis.

The α -phase transitioned to the ε -phase after ~10 nm. Diffraction pattern of the top layer matches well with the [$\overline{1}10$] zone axis of ε -Ga₂O₃ (Fig. 8.10(d). Fig. 8.10(a) and (c) shows that ε -Ga₂O₃ [110] is oriented parallel to α -Ga₂O₃ [100]. The lattice spacing did not match theoretical values, possibly indicating slight rotational misfit in crystal orientation.

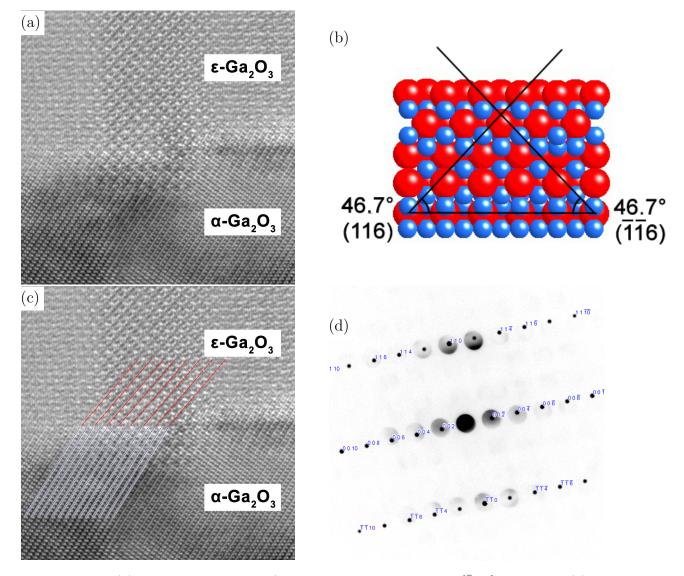


Figure 8.10: (a) HRTEM of ε -Ga₂O₃/ α -Ga₂O₃ interface along the [$\overline{1}$ 10] zone axis. (b) Crystal model of ε -Ga₂O₃ along the ($\overline{1}$ 10) view direction. (c) We can match the lattice planes observed to the (116) and ($\overline{1}$ $\overline{1}$ 6) planes. (d) Selected area diffraction of ε -Ga₂O₃ along the [$\overline{1}$ 10] zone axis.

8.3 Discussion

The first conclusion we can draw from our observations is that HVPE favors the growth of α and ε -Ga₂O₃ compared to β -Ga₂O₃, despite the latter being the thermodynamically stable phase. This is true up to a growth temperature of 850 °C. The fact that both the α - and ε -Ga₂O₃ are better lattice matched with the substrate alone does not explain why the same is not observed when grown using MOVPE techniques at the same growth temperature. A possible explanation might be found in the faster growth rate through HVPE compared to MOVPE. The precursors are more likely to chemisorb at sites that lattice matches the substrate, but due to higher growth kinetics, they may not have time to rearrange to form the thermodynamically stable β -form. This is consistent with the observation that metastable phases tend to be observed at lower growth temperatures (Table. 8.2).

As for the formation of ε -Ga₂O₃ instead of α -Ga₂O₃, where the former is neither thermodynamically favorable nor more latticed matched. A possible explanation is the inclusion of Cl⁻ ions. Cl⁻ has a ionic radius of 167 pm, compared to 126 pm for O²⁻. If Cl substitutes for O in the crystal lattice, it will cause compressive stress, therefore making the ε -phase, which has a larger lattice spacing, the preferred phase. This is verified through a SIMS study, where the samples grown in the ε -phase (# 112 and # 165) contained a higher concentrations of Cl⁻ impurities than one that was grown in the α -phase (# 105), regardless of growth temperature (Fig. 8.12). Growth conditions for the three samples we sent for SIMS analysis are given in Table 8.5.

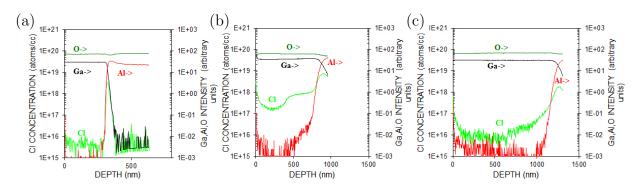


Figure 8.11: Cl^- concentration in atoms/cc and Ga_2O_3 , Al_2O_3 and O concentration in arbitrary units, measured by SIMS for samples (a) #105, (b) #112, and (c) #165, respectively. Sample #105 shows α -phase and was grown at low temperature, sample #112 shows ε -phase and was grown at low temperature. Sample #165 shows ε -phase and was grown at high temperature.

Table 8.5: Growth conditions for three growth runs of heteroepitaxial Ga_2O_3 on c-plane sapphire using HVPE. Sample 105 shows α -phase and was grown at low temperature, sample 112 shows ε -phase and was grown at low temperature. Sample 165 shows ε -phase and was grown at high temperature.

Run	Phase	Temp.	HCl %	HCl to	Thickness	Growth	SIMS Aver.
#		$(^{\mathrm{o}}\mathrm{C})$	of Total	O_2 Ratio	(nm)	Rate	Cl Conc
			Flow			(nm/min)	(atoms/cc)
105	α	650	0.37%	0.25875	350	11.7	3×10^{15}
112	ε	650	0.69%	0.05625	900	30	5×10^{17}
165	ε	850	0.71%	0.075	1250	41.7	2×10^{16}

Higher concentration of Cl⁻ in ε -Ga₂O₃ epilayers suggest that Cl impurities introduced during HVPE are associated with the formation of the less stable, more mismatched ε -phase.

While these points to the likelihood of Cl or other impurities playing a dominant role in the formation of different phases of Ga_2O_3 , the exact mechanism for this behavior is not understood. Further studies could be done to better understand this.

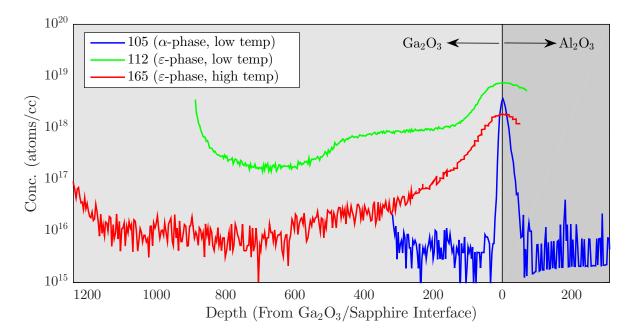


Figure 8.12: Cl^- concentration in three samples measured by SIMS. Sample 105 shows α -phase and was grown at low temperature of 650 °C, sample 112 shows ε -phase and was grown at low temperature of 650 °C. Sample 165 shows ε -phase and was grown at high temperature of 850 °C.



Summary and Future Work

9.1 Summary

In summary, we have carried out systematic studies of metal contacts to β -Ga₂O₃. We studied the effects of five different chemical cleaning methods on the behavior of Schottky diodes to β -Ga₂O₃. We also observed the growth of three different phases of Ga₂O₃ when grown using vapor phase expitaxial techniques on *c*-plane sapphire.

Specifically, we investigated nine metals (Ti, In, Ag, Sn, W, Mo, Sc, Zn, and Zr) as ohmic contacts to *n*-type single-crystal β -Ga₂O₃ substrates. We found that Ti contacts with a Au capping layer were ohmic with the lowest resistance after annealing at 400 °C for 1 min. We also found that the formation of ohmic contacts to β -Ga₂O₃ is not determined by any one dominant factor, such as metal work function and that limited interfacial reactions play an important role in both the morphology and electrical behavior of the metal contacts.

In our surface treatment study, we compared the effects of five different wet chemical treatments on the electrical behavior (e.g., barrier heights and ideality factors) of Ni/($\overline{2}01$) β -Ga₂O₃ Schottky diodes. Pretreatment with organic solvents, followed by HCl, H₂O₂, and

DI water was found to yield the most favorable characteristics.

Schottky diodes with five different Schottky metals (W, Cu, Ni, Ir, and Pt) were fabricated using the HCl and H_2O_2 pretreatment and characterized using *I-V* and *C-V* measurements. Schottky barrier heights were calculated from the measurements and found to have little correlation with the metal work functions. It was concluded that bulk or near-surface defects and/or surface states have a dominant effect on the electrical behavior. The results point to the need for further investigations on the types of defects in Ga₂O₃ and how to prepare a more ideal surface for device structures.

Heteroepitaxial films of Ga_2O_3 were grown on *c*-plane sapphire (001) using vapor phase epitaxy techniques. The stable β -phase was observed to grow when trimethylgallium (TMGa) or triethlygallium (TEGa) was used as a growth precursor, regardless of oxidizer and growth conditions. However, we observed the growth of two metastable phases, i.e. the α - and ε -phases when gallium chloride is introduced as a precursor. The evidence suggests that the formation of these metastable phases is a result of lattice matching, growth rate and/or lattice strain due to impurity concentration.

9.2 Current and Future Work

9.2.1 Surface Treatment

The results from the wet chemistry surface treatment study indicate that treatment of the Ga_2O_3 surface with HCl and H_2O_2 is beneficial but likely insufficient to create an ideal surface for device fabrication. Further investigation is needed to determine if additional surface treatment steps could be combined with these wet chemicals to remove surface damage and yield a more ideal surface. In particular, we are investigating the effects

of inductively coupled plasma reactive ion etch (ICP-RIE) using BCl_3 [115]. This has been reported to lower contact resistance for ohmic contacts, and through discussion with colleagues at Ohio State University, might lower the ideality factor of Schottky contacts too.

9.2.2 Electrical Devices

Having an extensive study of metal contacts to β -Ga₂O₃, the next logical step is to incorporate our findings into fabricating β -Ga₂O₃-based electronic devices. Together with new students in the Porter research group, we are in the process of studying metal-oxidesemiconductor (MOS) capacitors, and metal-oxide-semiconductor field effect transistors (MOSFET). Fig. 9.1 shows schematics of these devices.

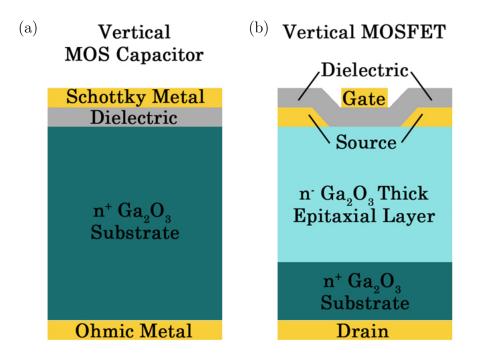


Figure 9.1: Schematic of a (a) metal-oxide-semiconductor (MOS) capacitor, and (b) metal-oxidesemiconductor field effect transistor (MOSFET).

 Al_2O_3 and HfO_2 are potential choices as dielectrics, and will be deposited using atomic layer deposition (ALD) method. We are currently in the process of measuring capacitance results for our first set of MOS capacitors. We are also in the process of designing photolithography lift-off masks for MOSFET fabrication.

9.2.3 Improved Crystal Growth and Doping

As mentioned in section 7.3, we were able to grow undoped β -Ga₂O₃ homoepitaxially on Sn-doped β -Ga₂O₃ single crystals. However, due to time constraints, devices and electrical behavior of these films have not be studied. However, being able to grow good quality homoepitaxial films is a critical step towards future device applications.

Also, it would be interesting to study growth of multilayer heterostructures involving ZnO, In_2O_3 and Al_2O_3 and their alloys, for functional materials and other tunable behaviors.

Lastly, we have been working extensively with SMI to incorporate doping into our epitaxial films. So far the dopants we have studied include Si, Ge, Sb and Ti. We have not been successful in introducing doping into the films yet, but this is very important to enable control of device characteristics.



Schottky Diodes

A.1 Thermionic Emission Theory

According to thermionic emission theory, A Schottky diode under forward bias has the following current-voltage (I-V) characteristics,

$$I = I_S \left[\exp \frac{qV_D}{kT} - 1 \right] \tag{A.1}$$

where q is the electronic charge, V_D the voltage applied across the diode, k the Boltzmann constant, T the absolute temperature and I_S the saturation current expressed by

$$I_S = A_{\text{eff}} A^{**} T^2 \exp\left(-\frac{q\phi_B}{kT}\right)$$
(A.2)

 A_{eff} is the effective area of the diode, A^{**} is the Richardson constant and ϕ_B is the Schottky barrier height of the diode.

Since most practical Schottky diodes show deviations from ideal thermionic emission

behavior. A dimensionless parameter called the ideality factor n is usually included to account for nonideal diode behaviors [8]

$$I = I_S \left[\exp \frac{qV_D}{nkT} - 1 \right] \tag{A.3}$$

A.2 Extracting Schottky Diode Parameters

The effect of the diode series resistance R is usually modeled with a series combination of a diode and a resistor with resistance R through which the current I flows. The voltage V_D across the diode can then be expressed in terms of the total voltage drop V across the series combination of the diode and the resistor. Thus, $V_D = V - IR$, and for $V_D > 3kT/q$. Eqn. A.3 becomes

$$I = I_S \left[\exp \frac{q \left(V - IR \right)}{nkT} \right] \tag{A.4}$$

A method to extract the series resistance R of ideal Schottky diodes (i.e. n = 1) was first proposed by Norde.[116] for the n > 1 cases, Sato and Yasumura [117] had modified Norde's approach to extract the values of n, ϕ_B and R from the forward I-V data of a Schottky diode. Their approach requires, for a given Schottky diode, two experimental I-V measurements conducted at two different temperatures and the determination of the corresponding minima to the modified Norde's function.

We extracted Schottky Diode parameters according to the method outlined in Cheung

and Cheung [99]. Eqn. A.4 can be rewritten in terms of current density $J=I/A_{\rm eff},$

$$I = A_{\text{eff}} A^{**} T^2 \exp\left(-\frac{q\phi_B}{kT}\right) \left[\exp\frac{q\left(V - IR\right)}{nkT}\right]$$
$$J = A^{**} T^2 \exp\left(-\frac{q\phi_B}{kT}\right) \left[\exp\frac{q\left(V - JA_{\text{eff}}R\right)}{nkT}\right]$$

Rearranging,

$$\frac{J}{A^{**}T^2} = \exp\left[\frac{q\left(V - JA_{\text{eff}}R\right)}{nkT} - \frac{q\phi_B}{kT}\right]$$
$$\ln\left(\frac{J}{A^{**}T^2}\right) = \frac{q\left(V - JA_{\text{eff}}R - n\phi_B\right)}{nkT}$$

Substituting,

$$\beta = \frac{q}{kT} \tag{A.5}$$

we have

$$\frac{n}{\beta} \ln\left(\frac{J}{A^{**}T^2}\right) = V - JA_{\text{eff}}R - n\phi_B$$
$$V = JA_{\text{eff}}R + n\phi_B + \frac{n}{\beta} \ln\left(\frac{J}{A^{**}T^2}\right)$$
(A.6)

Differentiating Eqn. A.6 with respect to $\ln J,$

$$\frac{dV}{d\ln J} = \frac{dV}{dJ} \cdot \frac{dJ}{d\ln J} = \left[A_{\text{eff}}R + \frac{n}{\beta}\left(\frac{1}{J}\right)\right] \cdot J = JA_{\text{eff}}R + \frac{n}{\beta} \tag{A.7}$$

APPENDIX A. SCHOTTKY DIODES

Thus a plot of $dV/d\ln J$ vs. J will give RA_{eff} as the slope and n/β as the *y*-intercept. To evaluate ϕ_B , we can define a function H(J),

$$H(J) \equiv V - \frac{n}{\beta} \ln\left(\frac{J}{A^{**}T^2}\right) \tag{A.8}$$

Substituting this into Eqn. A.6,

$$H(J) = JA_{\text{eff}}R + n\phi_B \tag{A.9}$$

Using the *n* value derived from Eqn. A.7, a plot of H(J) vs. *J* will produce a straight line with ϕ_B being the *y*-intercept. The slope of this plot also provide a second determination of *R* which can be used to check the consistency of this approach.

A.3 Schottky Barrier Height From Capacitance Voltage (C-V)

The Schottky barrier ϕ_B is given by

$$\phi_B = \psi_{bi} + \phi_n + \frac{kT}{q} - \Delta\phi \tag{A.10}$$

where ψ_{bi} is the built-in voltage, $\phi_n = E_C - E_F$ and $\Delta \phi$ is the barrier change due to image lowering. ϕ_n can be calculated from

$$\phi_n = E_C - E_F$$
$$= kT \ln\left(\frac{N_C}{N_D}\right) \tag{A.11}$$

 N_C is the effective density of states in the conduction band and can we calculated from

$$N_C = \frac{2\pi m_e kT}{h^2} \tag{A.12}$$

and N_D is related the depletion layer capacitance by

$$N_D = \frac{2}{q\varepsilon_s} \left[-\frac{1}{d\left(1/C^2\right)/dV} \right] \tag{A.13}$$

The depletion layer capacitance is

$$C_D \equiv \frac{\varepsilon_s}{W_D}$$
$$= \sqrt{\frac{q\varepsilon_s N_D}{2\left(\psi_{bi} - V - kT/q\right)}}$$
(A.14)



Additional SEM of Ohmic Contacts to β -Ga₂O₃

The following are additional SEM images of ohmic contacts to β -Ga₂O₃ not included in Chapter 5 for brevity.

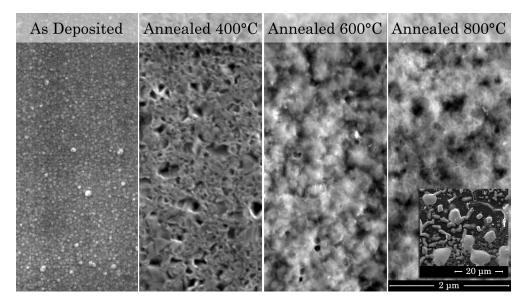


Figure B.1: SEM image of Zr/Au film on single crystal β -Ga₂O₃ after annealing at temperatures indicated.

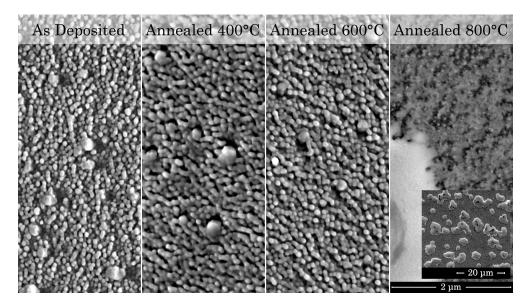


Figure B.2: SEM image of W/Au film on single crystal β -Ga₂O₃ after annealing at temperatures indicated.

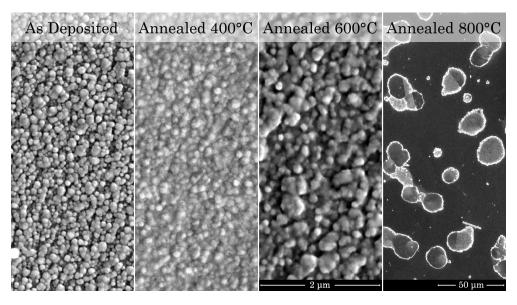


Figure B.3: SEM image of Mo/Au film on single crystal β -Ga₂O₃ after annealing at temperatures indicated.

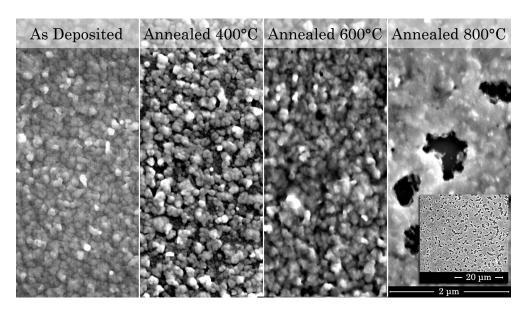


Figure B.4: SEM image of Zn/Au film on single crystal β -Ga₂O₃ after annealing at temperatures indicated.

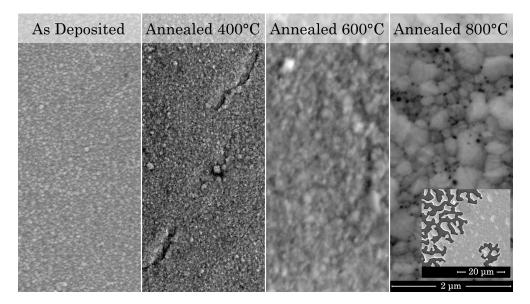


Figure B.5: SEM image of Sc/Au film on single crystal β -Ga₂O₃ after annealing at temperatures indicated.

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